

L13 ARTWORK
L13 BOTTOM SILK

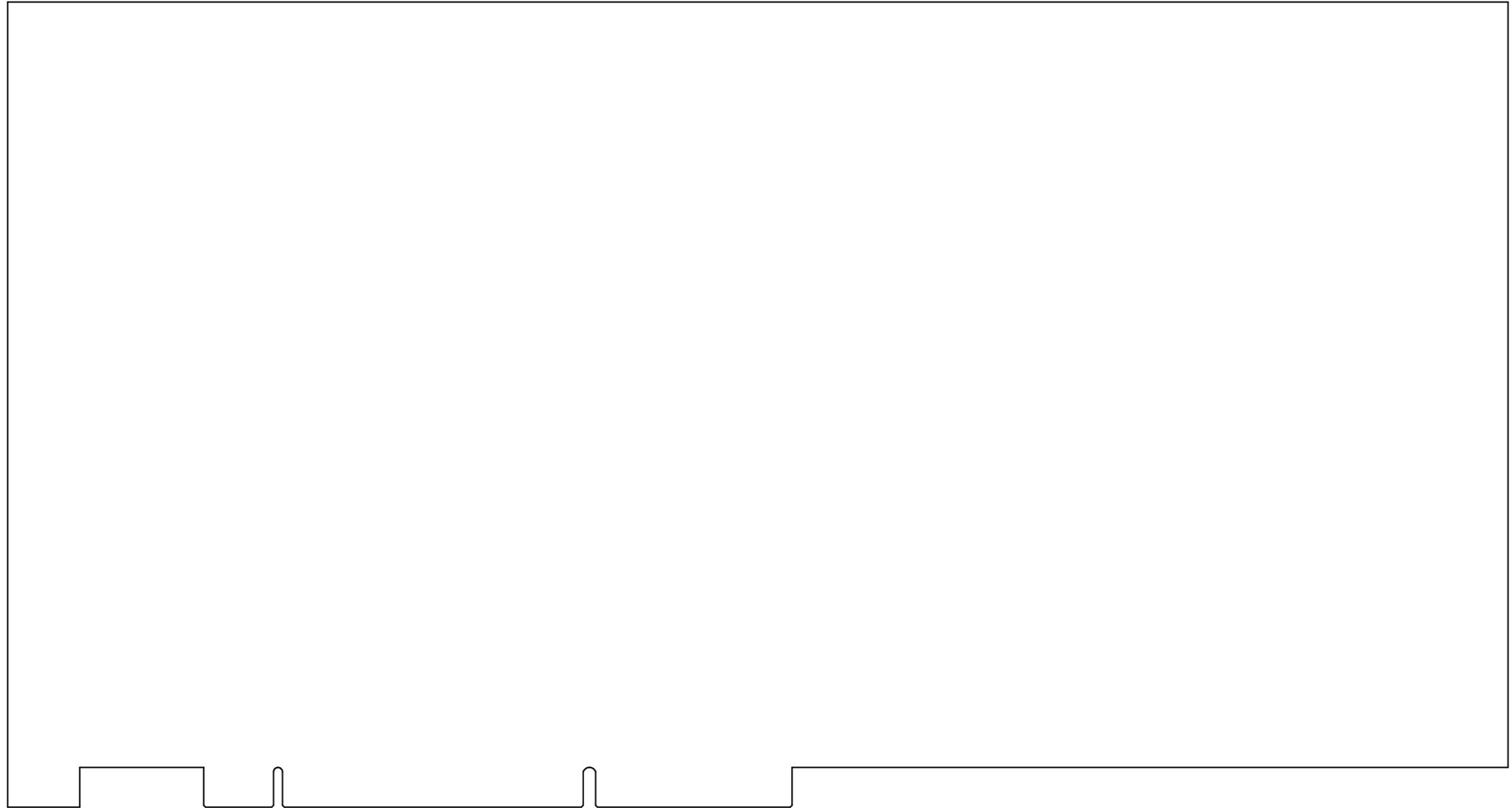
ART015.PHO
22B01501.PHO

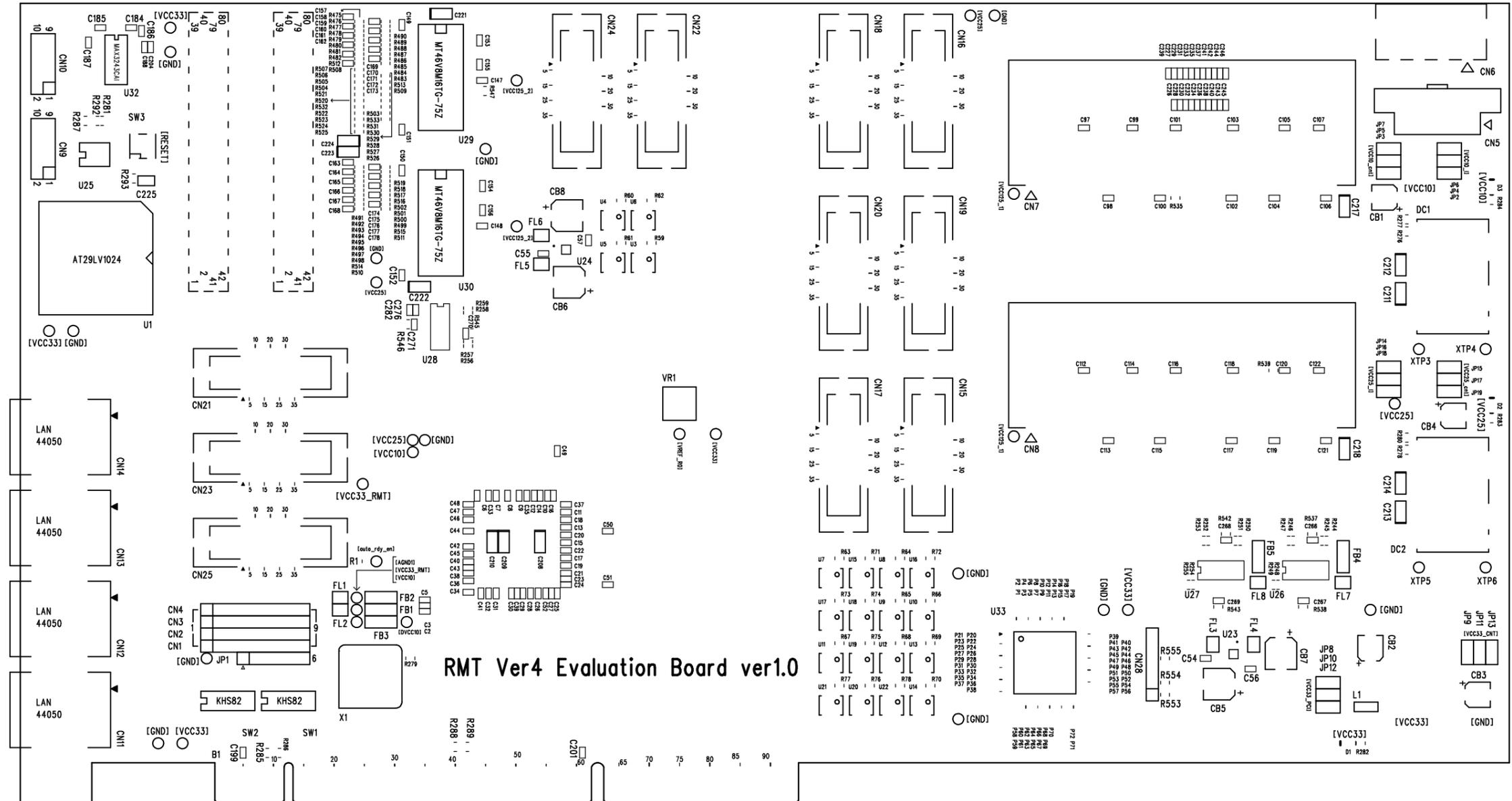
KB0501_D(RMT VER4 EVA BOARD)2ANSEI SYSTEM 2002.09.18

KB05091_D(RMT VER4 EVA PCI EVA BOARD)SANSEI SYSTEM 2005.09.16

BOARDOUTLINE

GEN030.PHO

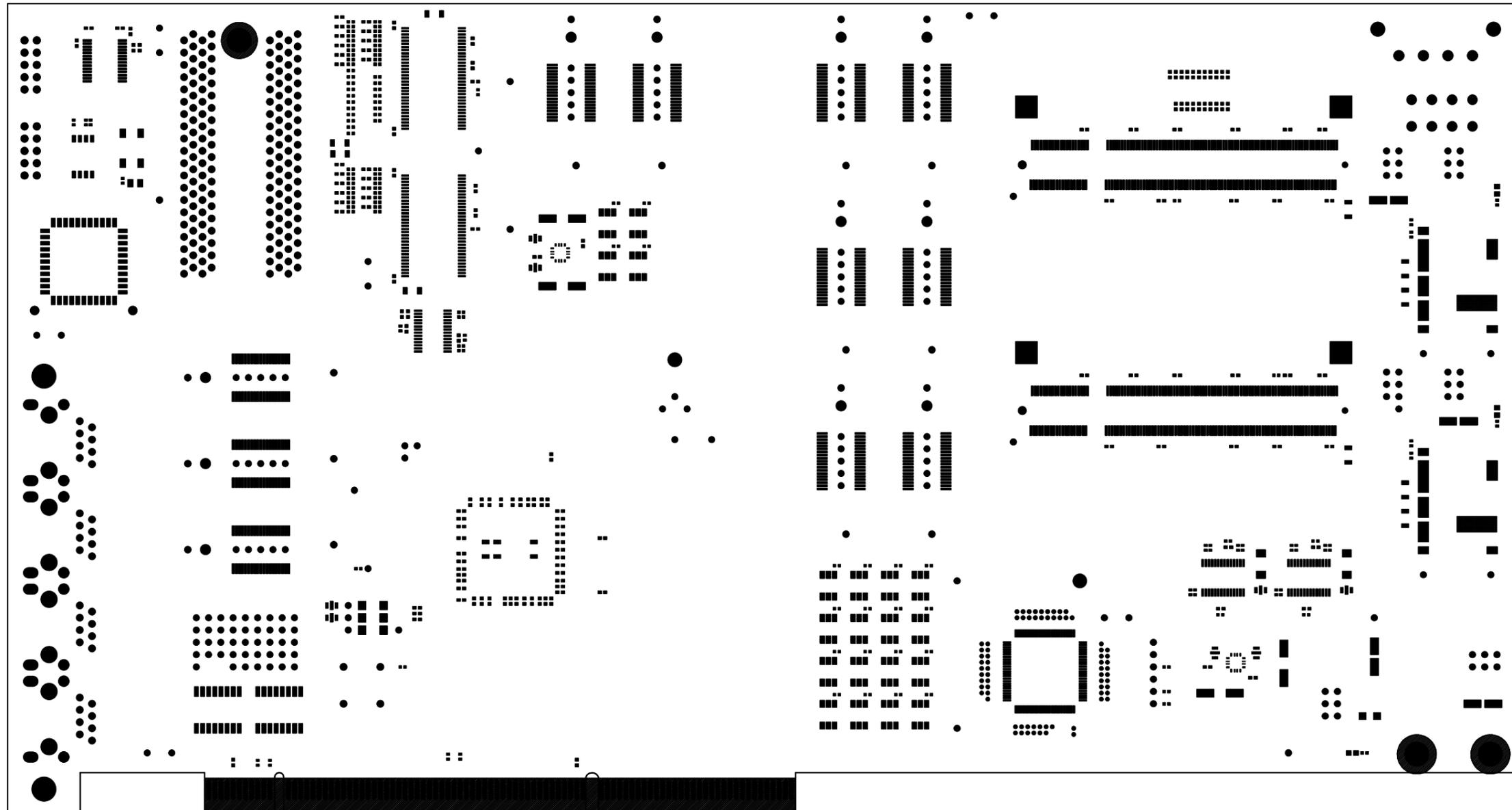


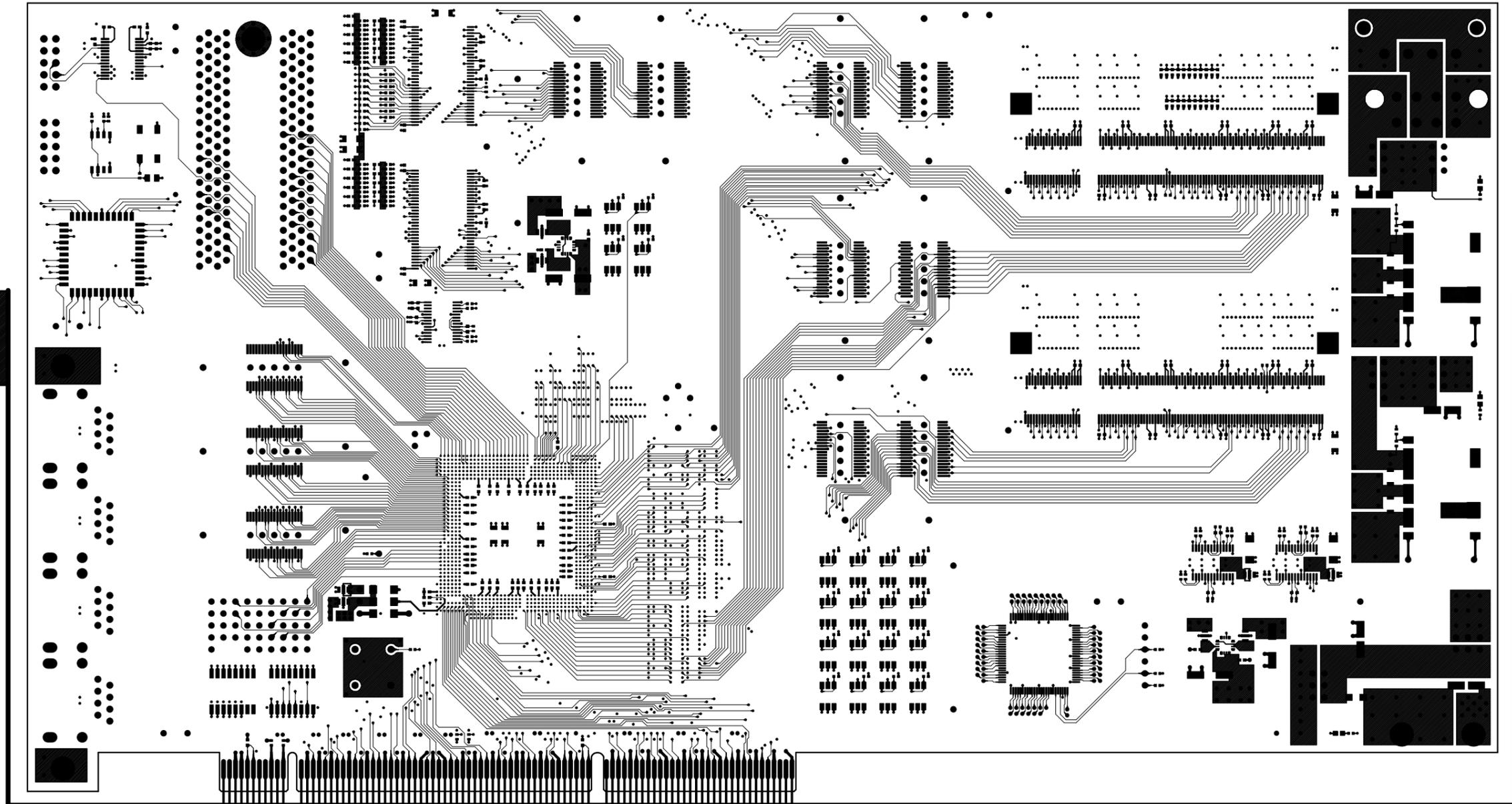


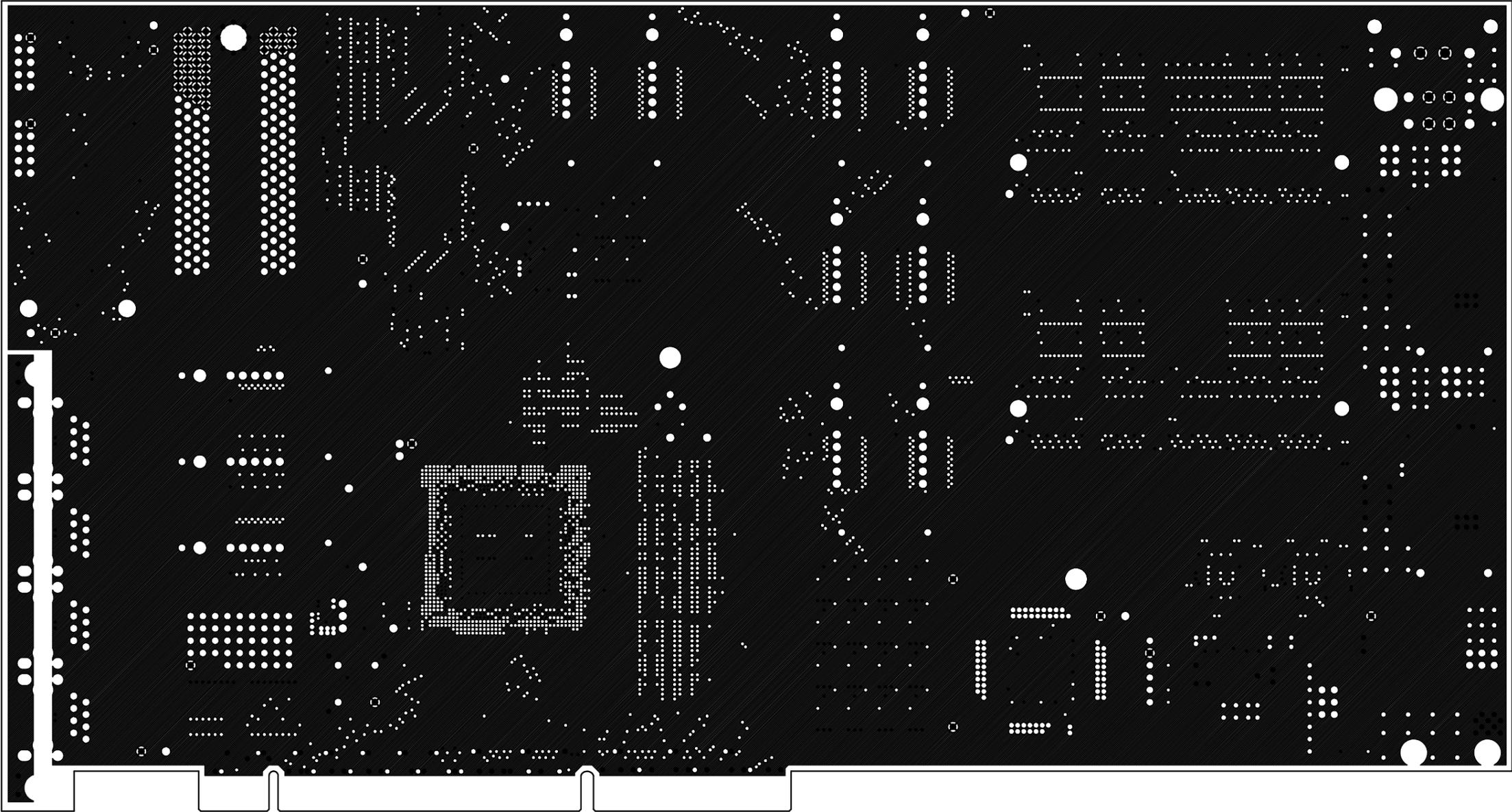
KB05091_D(RMT VER4 EVA PCI EVA BOARD)SANSEI SYSTEM 2005.09.16

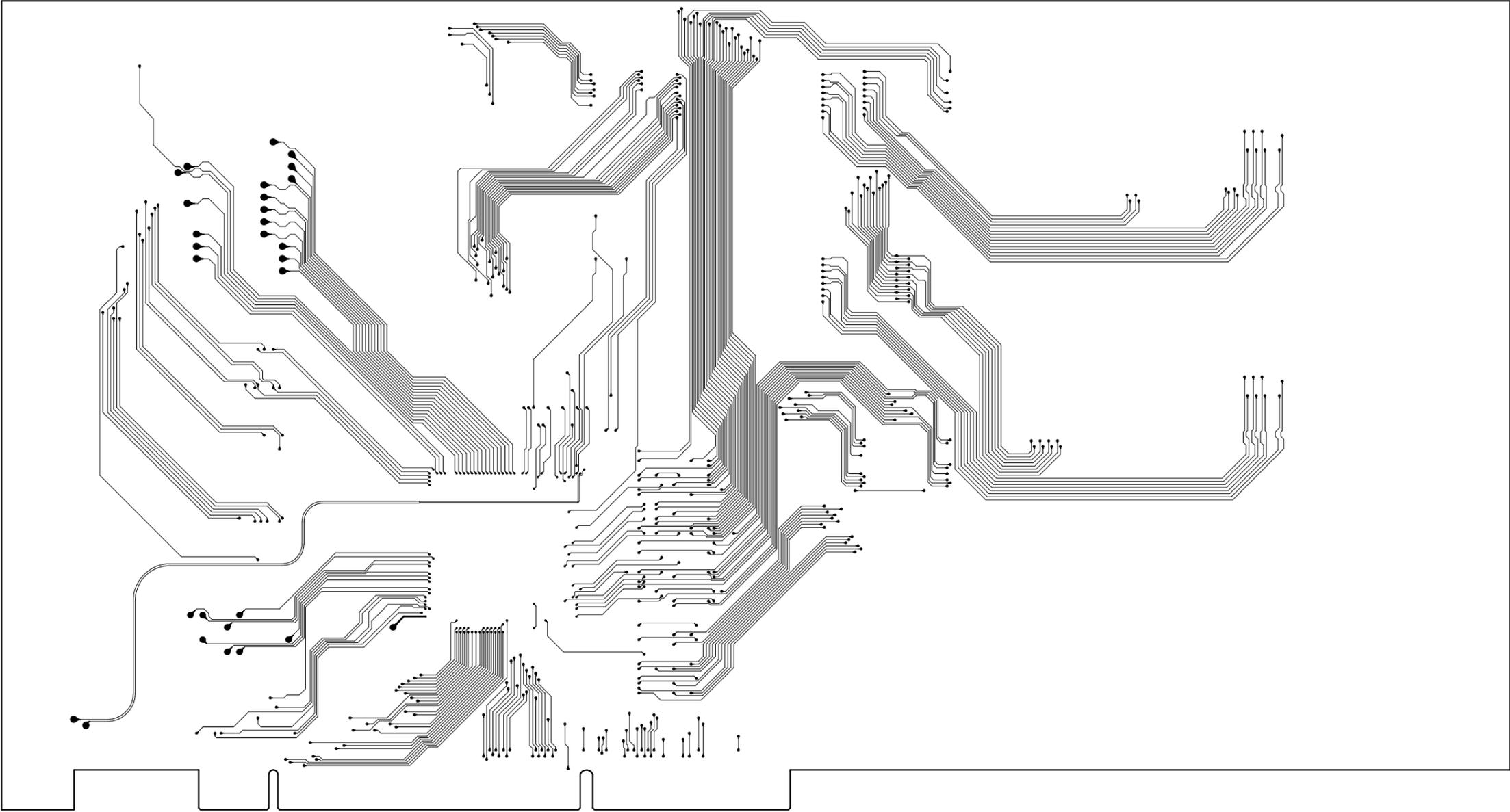
L1 SOLDERMASK(TOP)

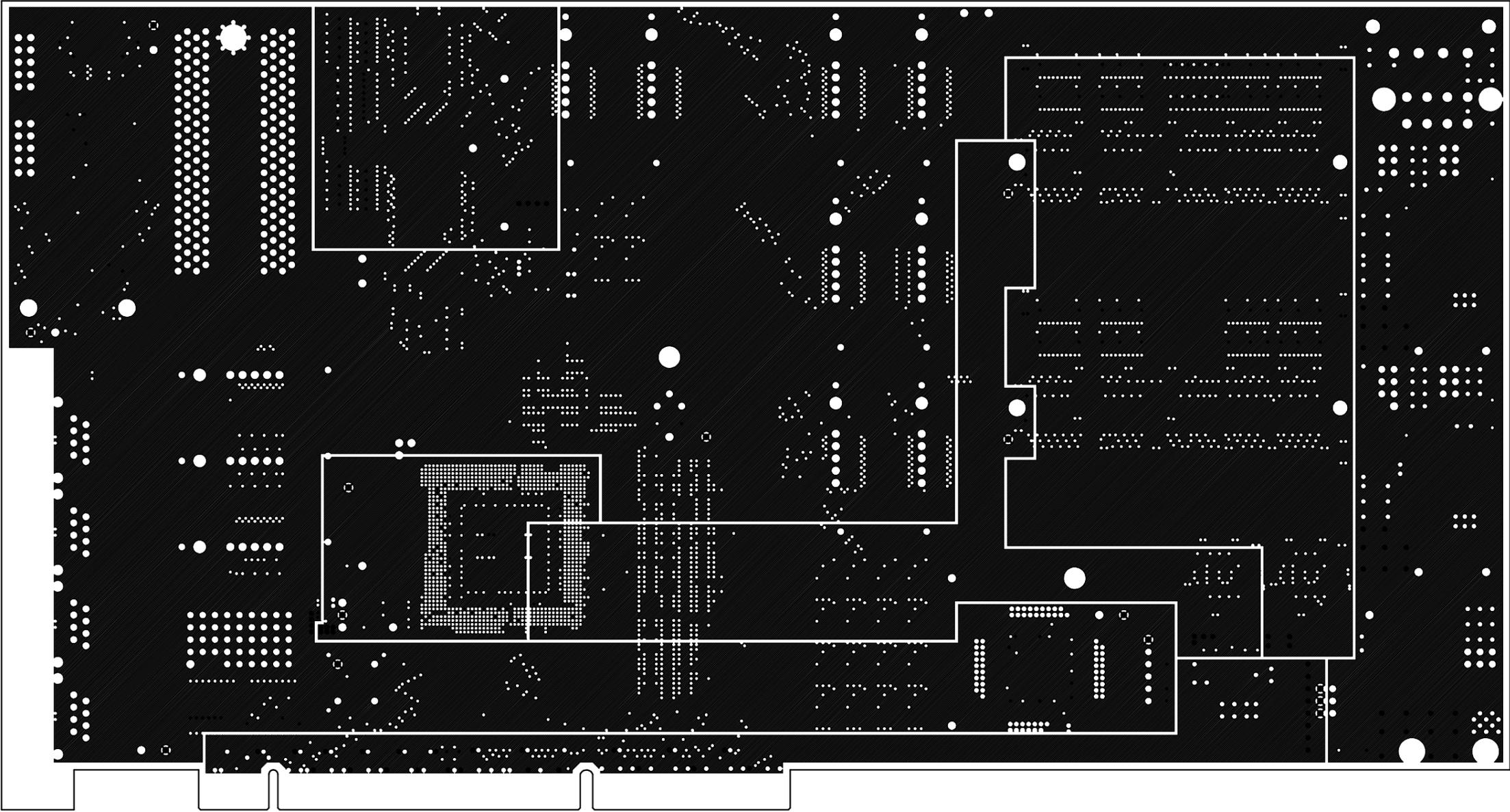
SM001024.PHO

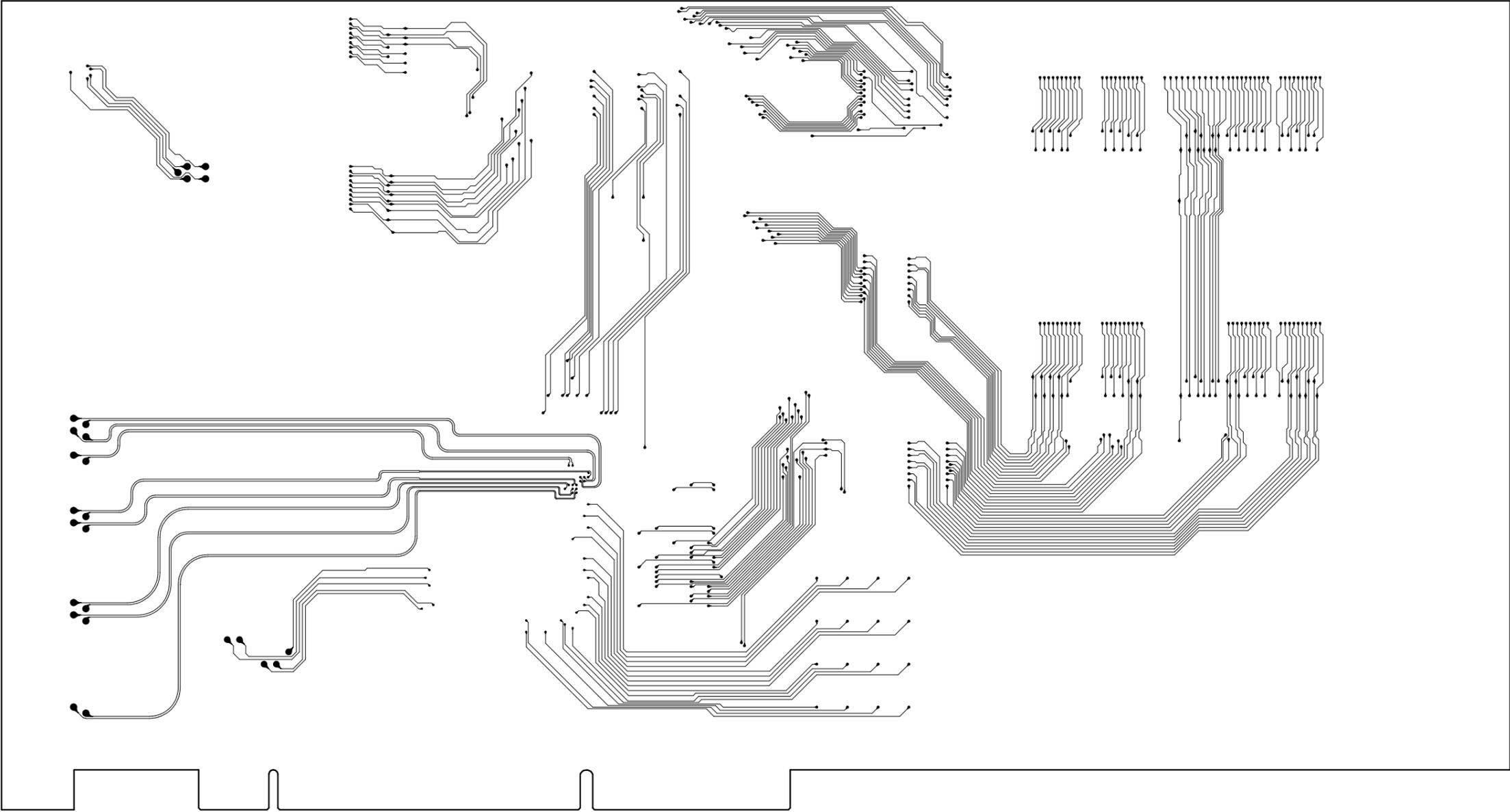


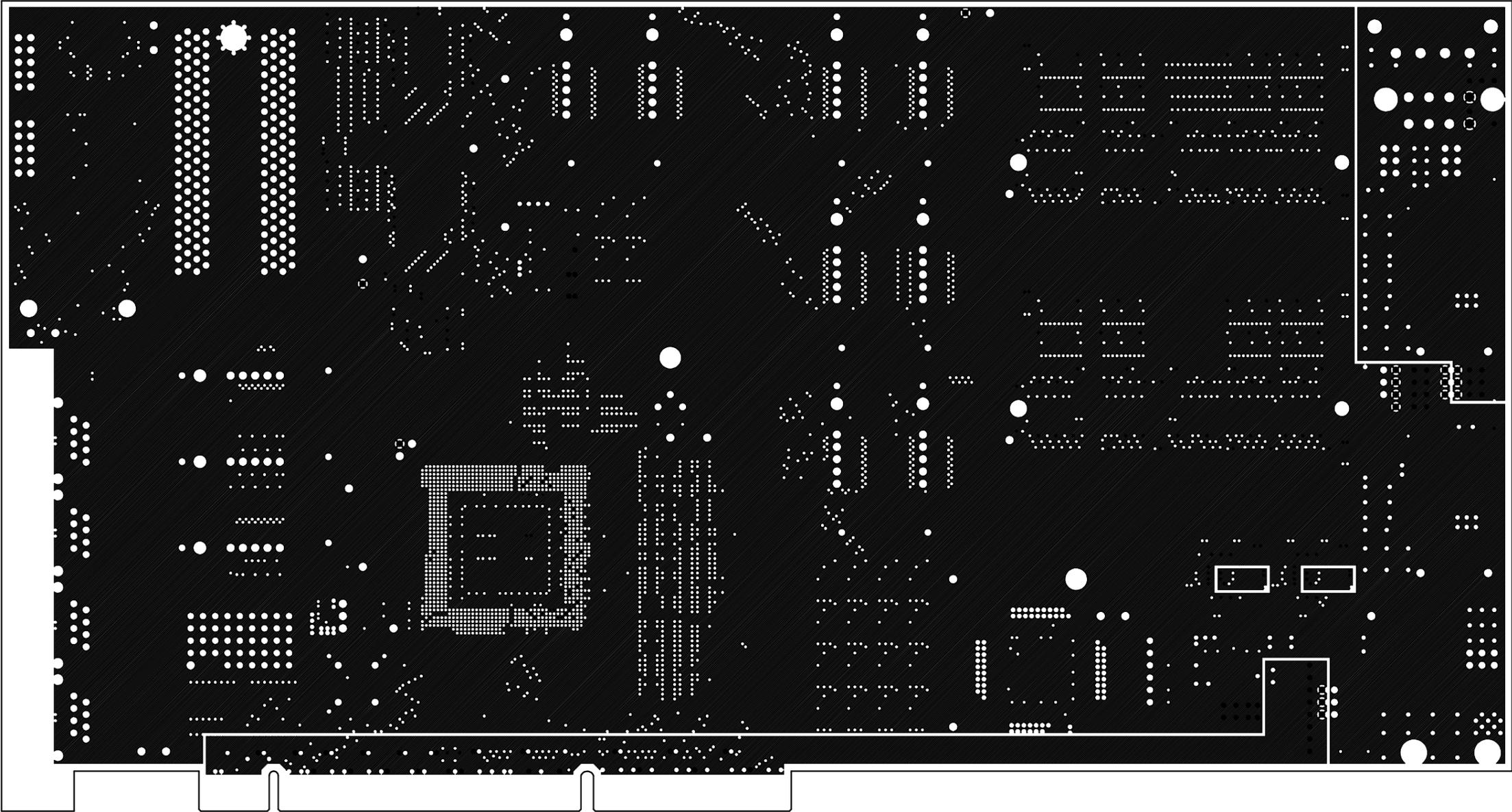


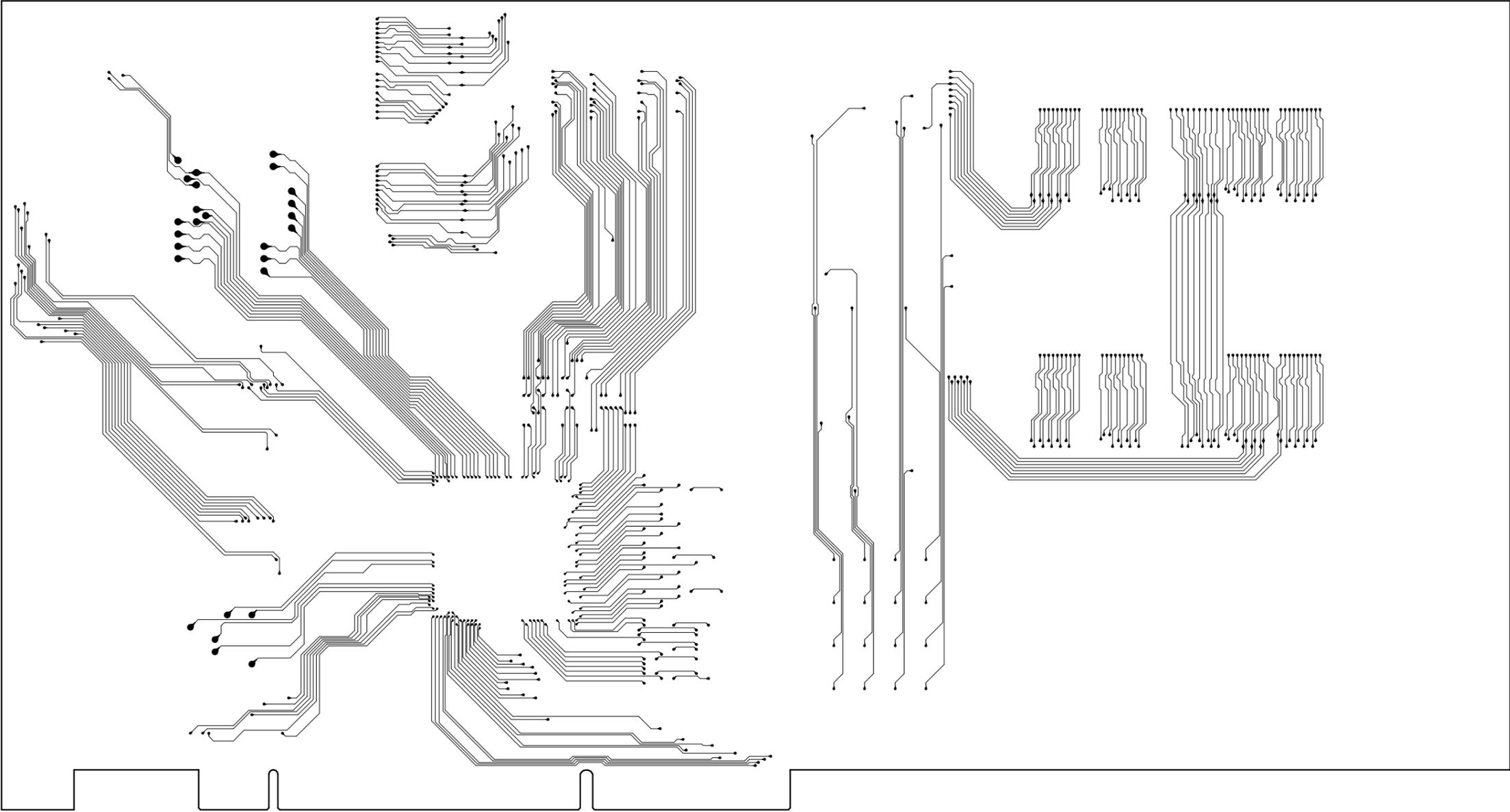


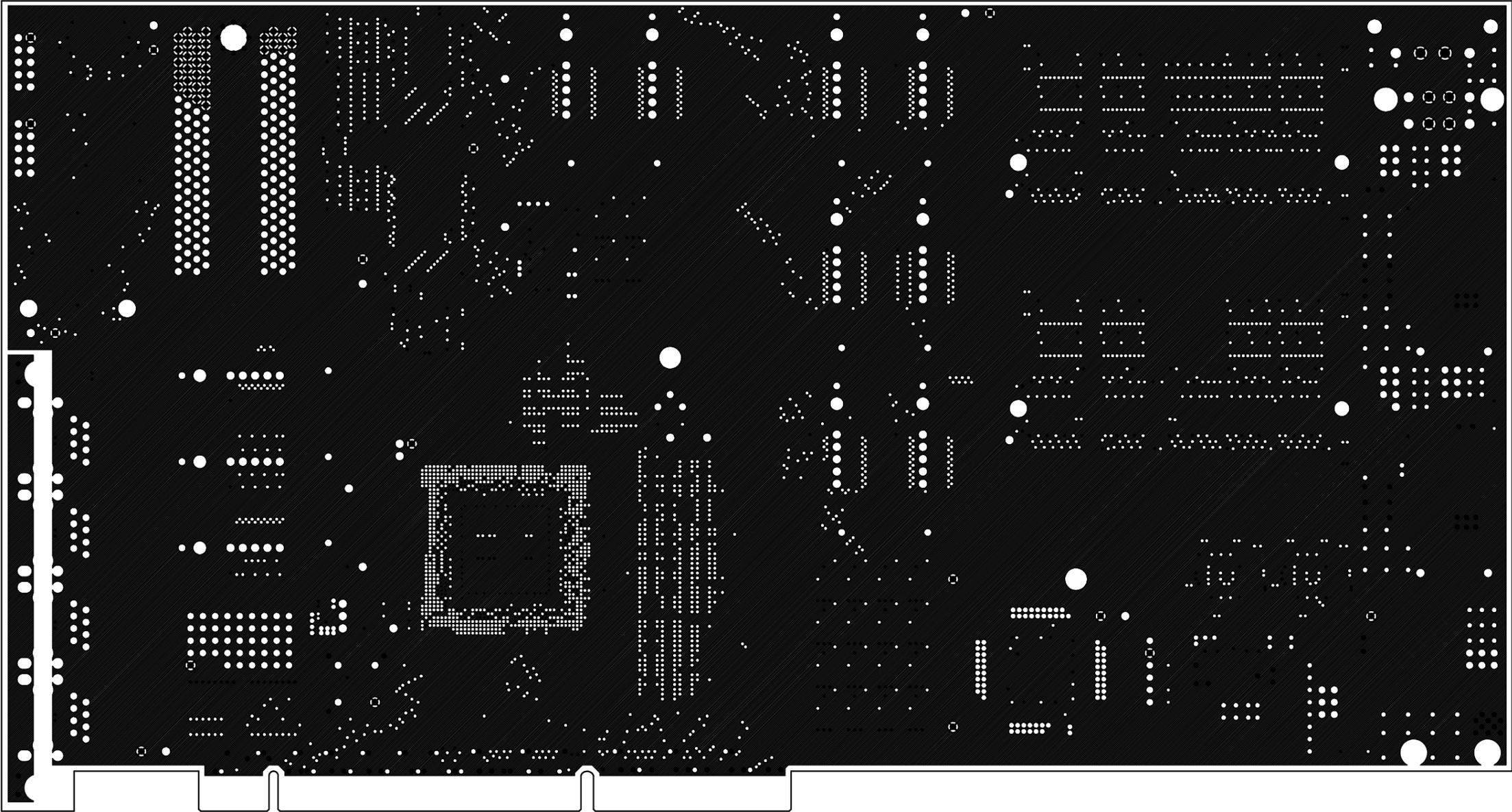


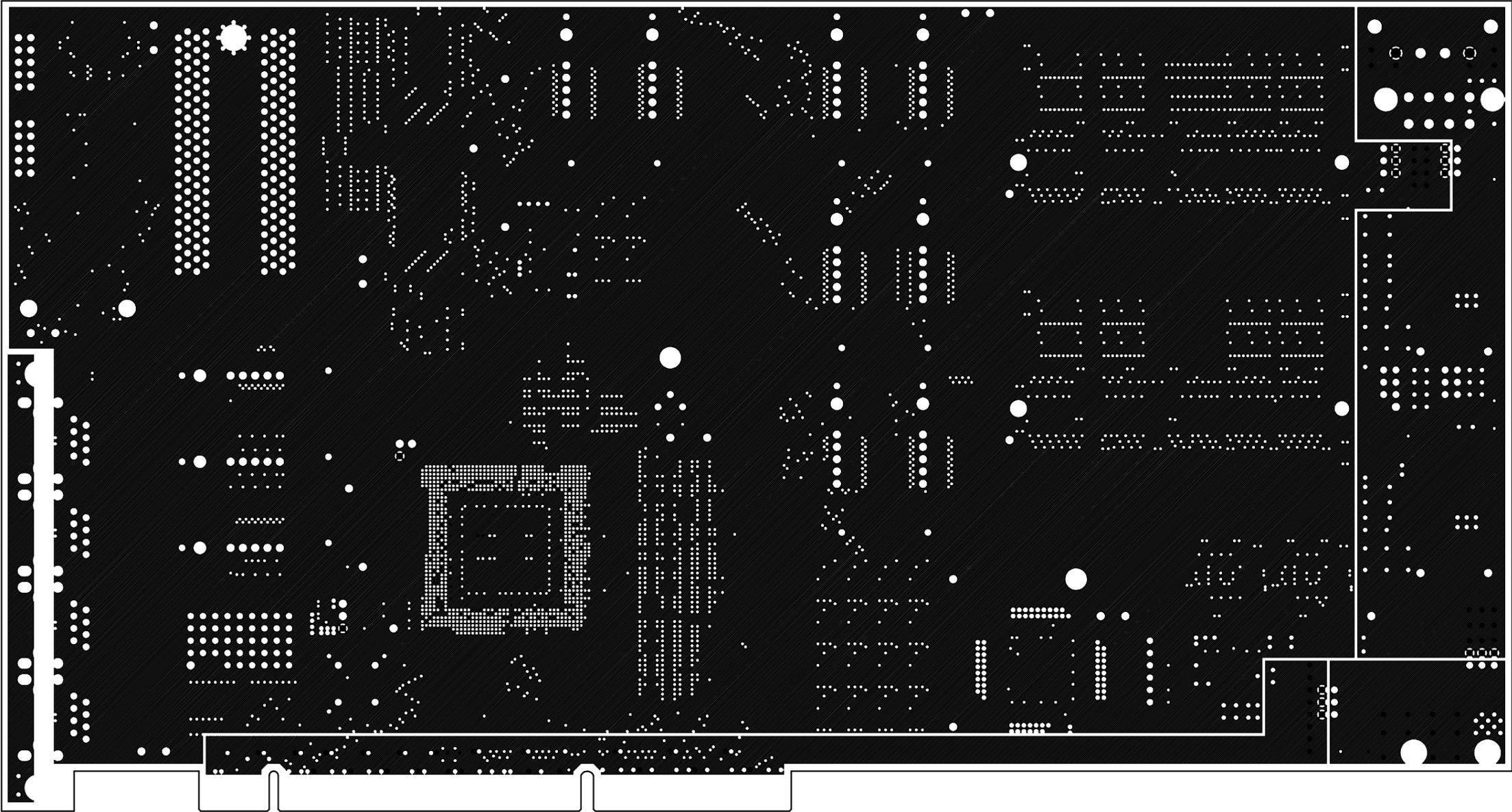


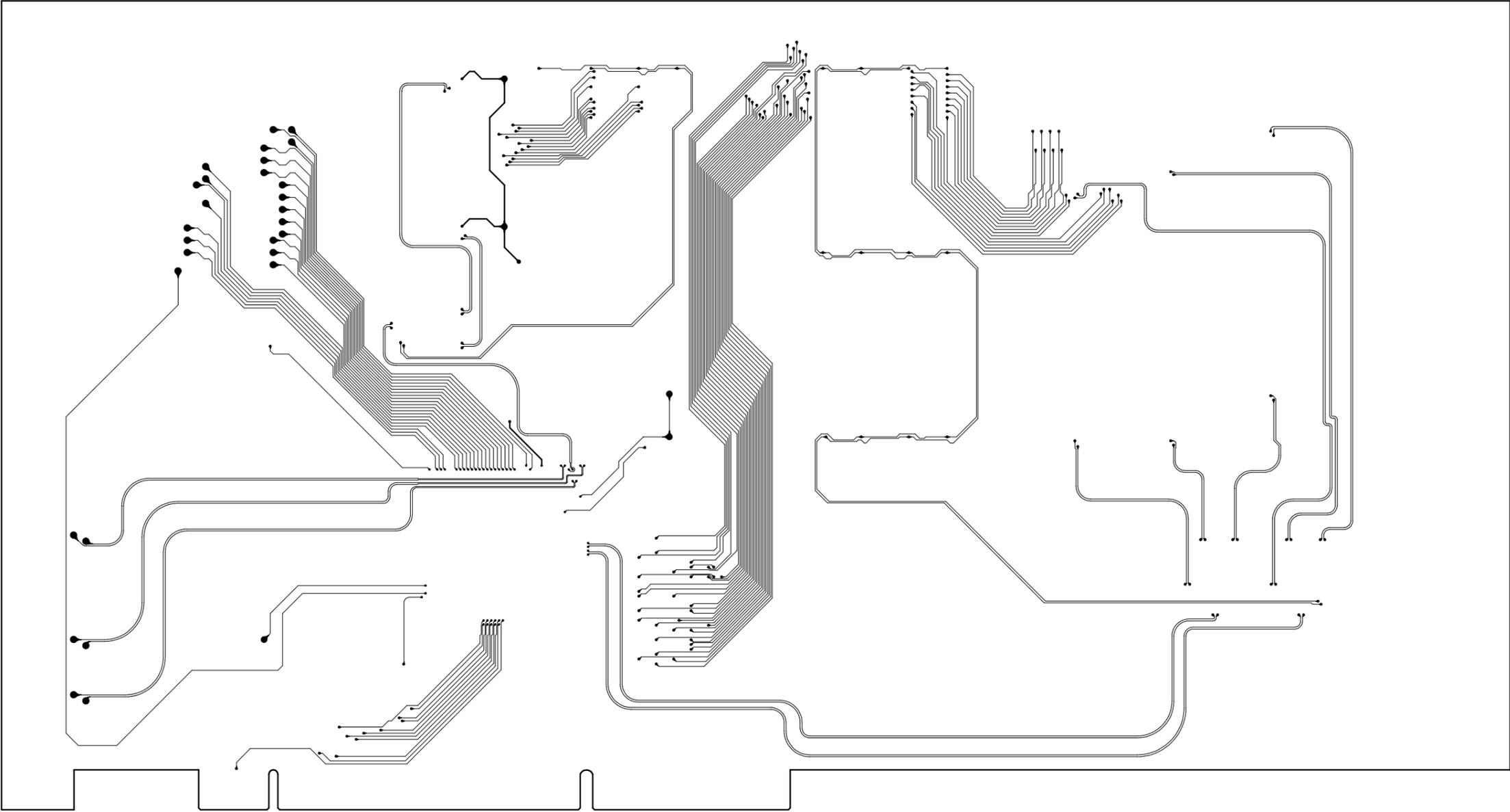


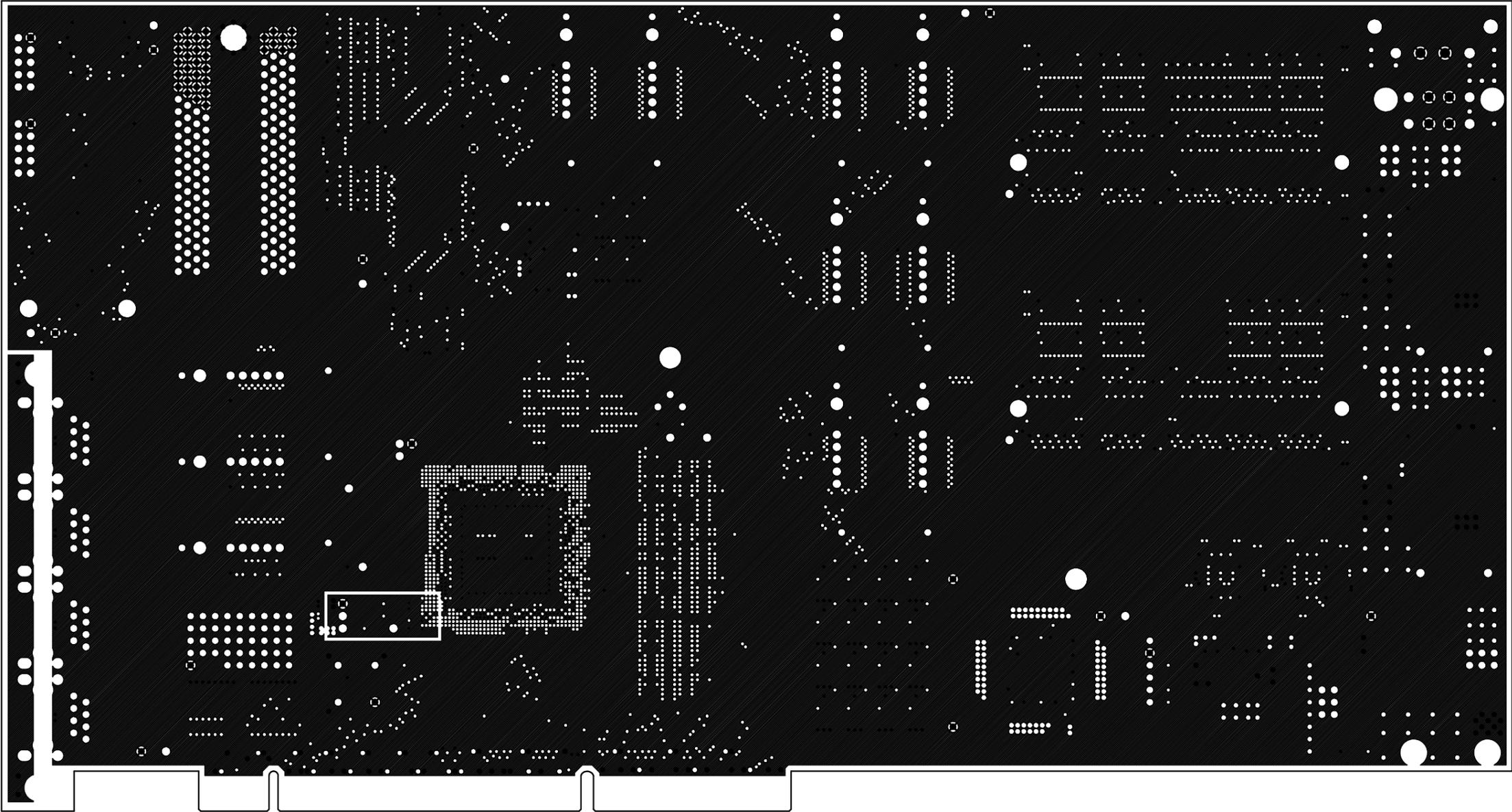








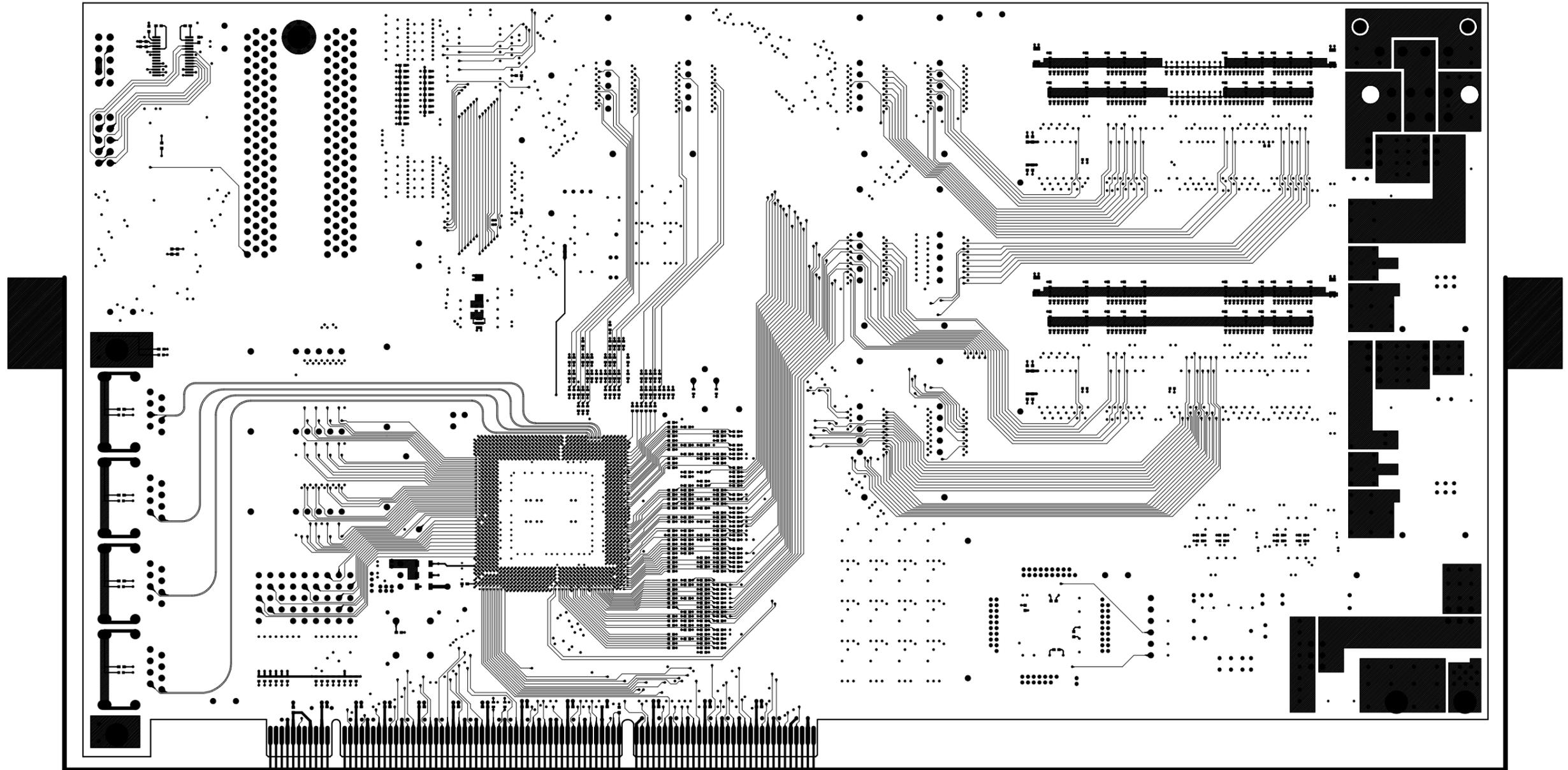




KB05091_D(RMT VER4 EVA PCI EVA BOARD)SANSEI SYSTEM 2005.09.16

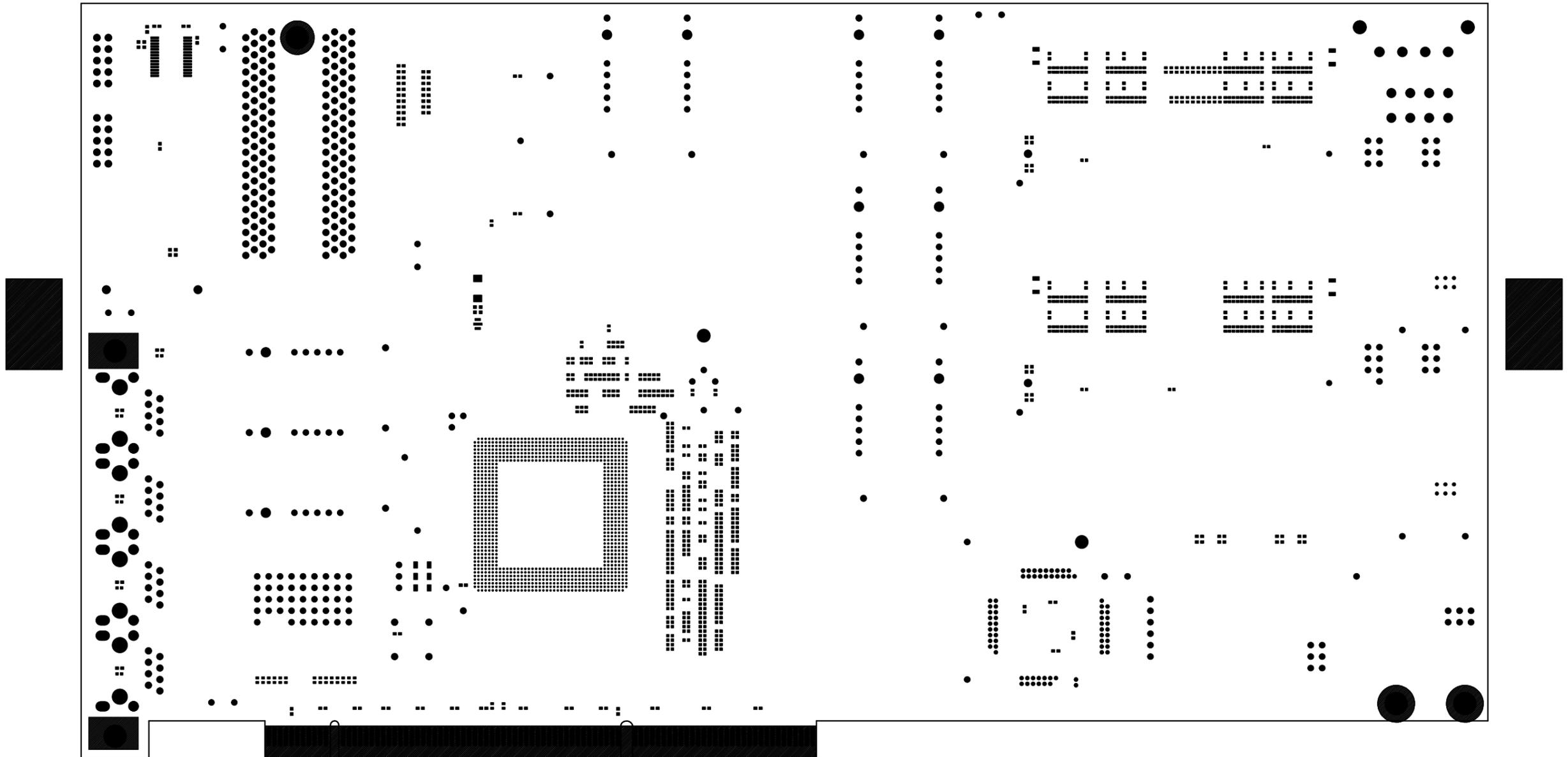
L12 ARTWORK

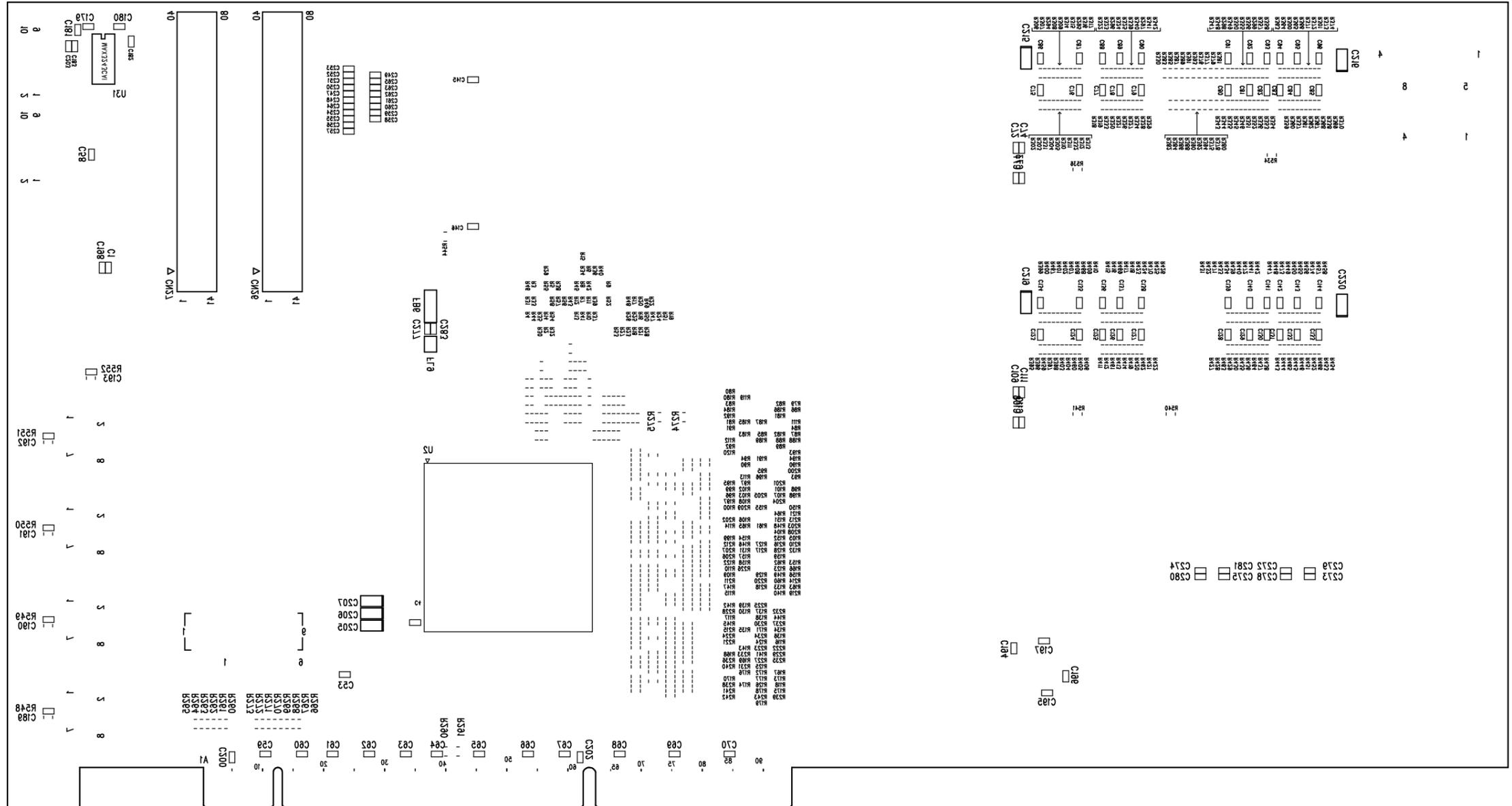
ART012.PHO

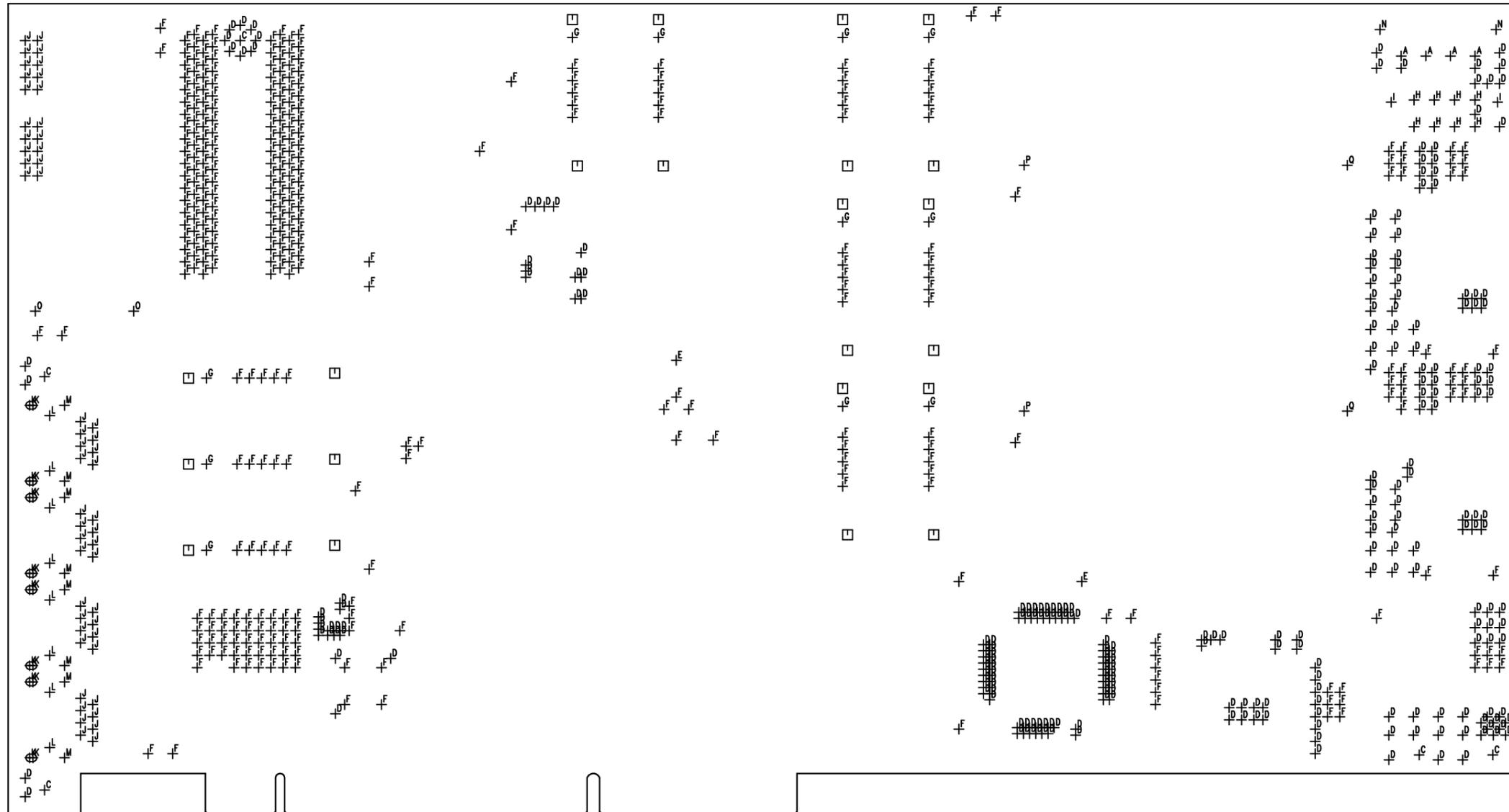


KB05091_D(RMT VER4 EVA PCI EVA BOARD)SANSEI SYSTEM 2005.09.16

L12 SOLDERMASK(BOTTOM) SM012025.PHO







寸法	個数	記号	PLTD	TOL
30	22	□	YES	+/-0.0
60	4	⊕ ^A	YES	+/-0.0
138	5	⊕ ^C	YES	+/-0.0
16	325	⊕ ^B	YES	+/-0.0
98	2	⊕ ^E	NO	+/-0.0
32	343	⊕ ^F	YES	+/-0.0
78	11	⊕ ^G	NO	+/-0.0
55	8	⊕ ^M	YES	+/-0.0
118	2	⊕ ^I	NO	+/-0.0
36	52	⊕ ^J	YES	+/-0.0
62 x 87	8	⊕ ^K	YES	+/-0.0
128	8	⊕ ^L	NO	+/-0.0
62	8	⊕ ^N	YES	+/-0.0
91	2	⊕ ^M	YES	+/-0.0
67	2	⊕ ^O	NO	+/-0.0
63	2	⊕ ^P	NO	+/-0.0
43	2	⊕ ^Q	NO	+/-0.0