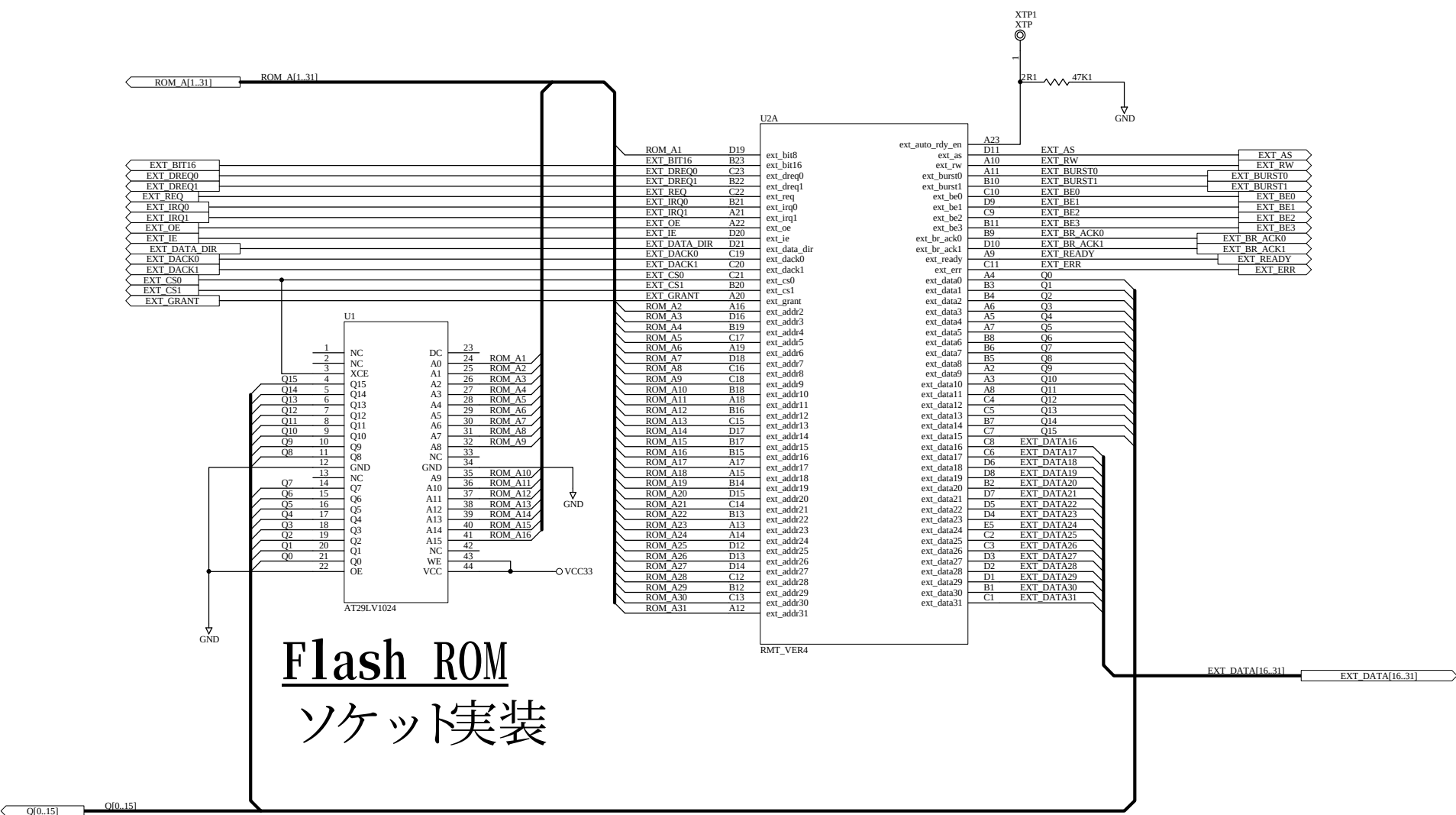
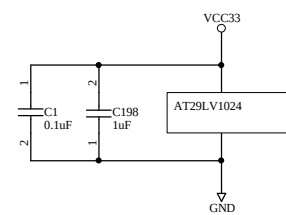




RMTプロセッサ

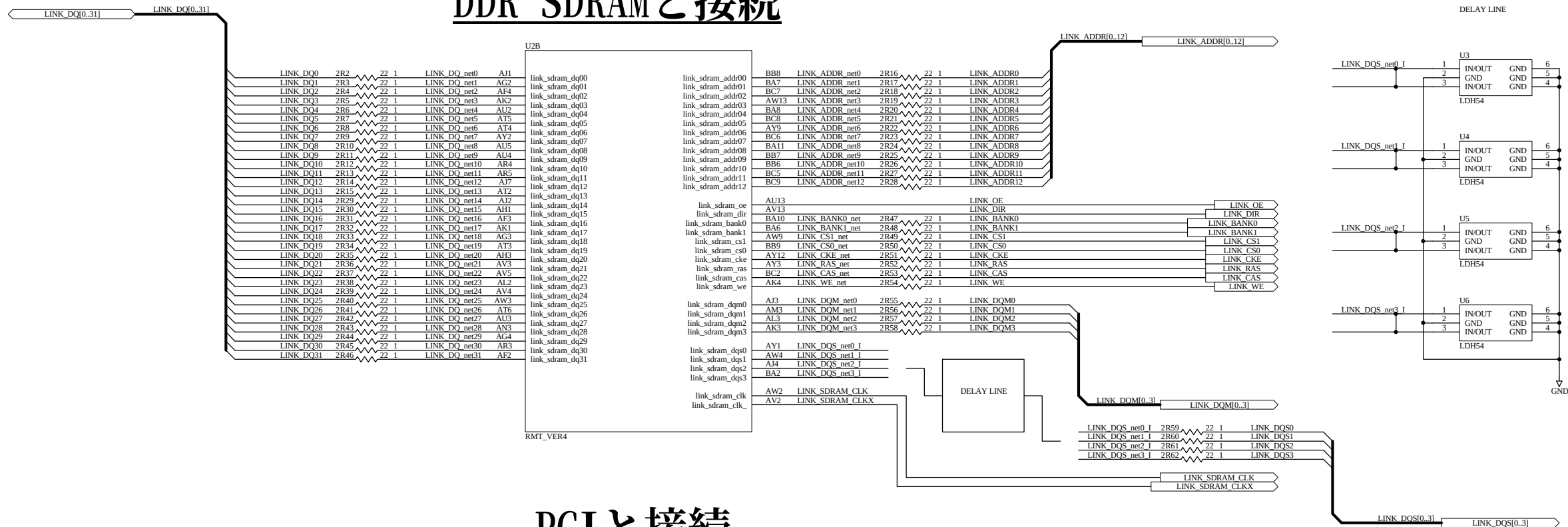


Flash ROM
ソケット実装

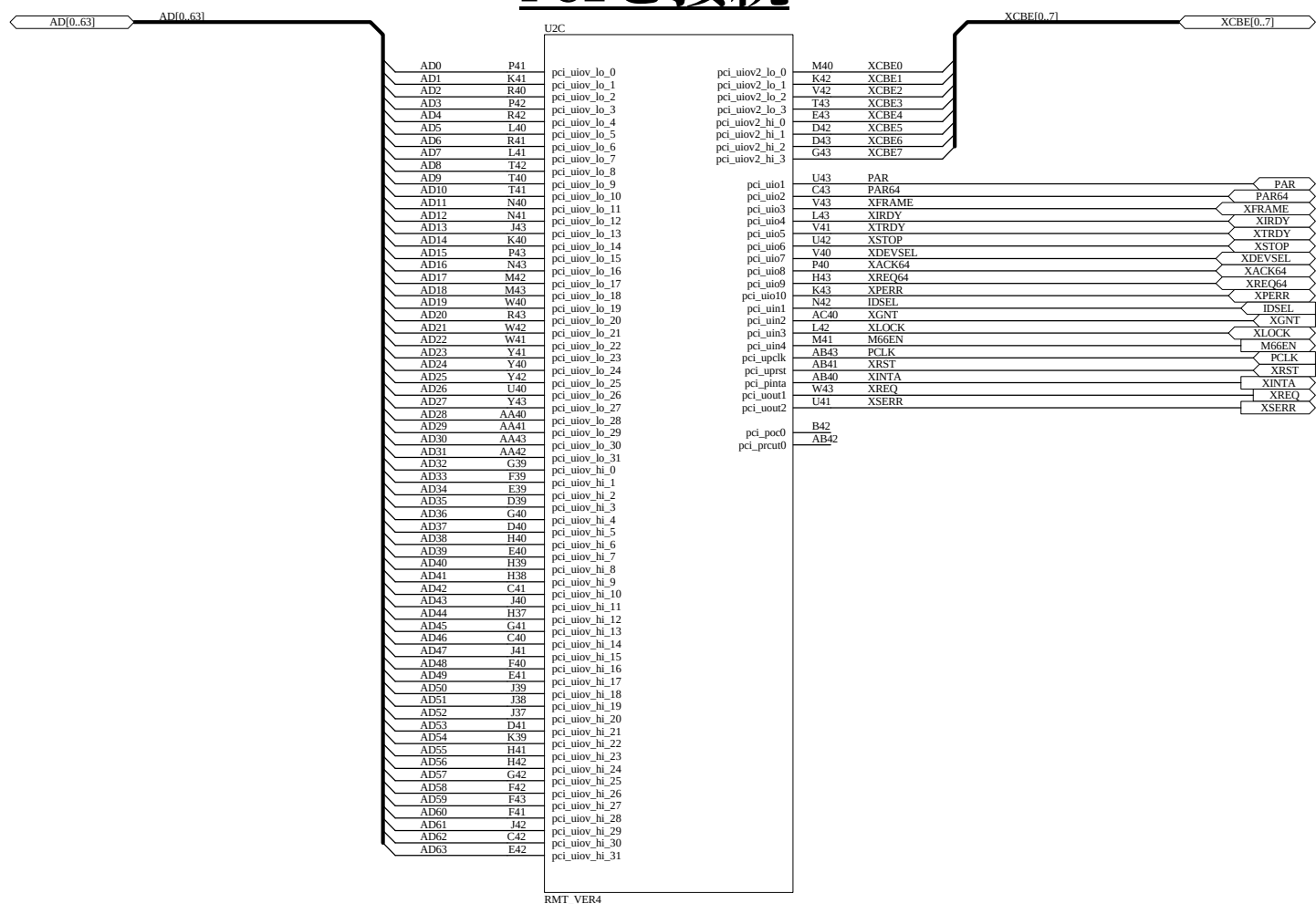


RMTプロセッサ

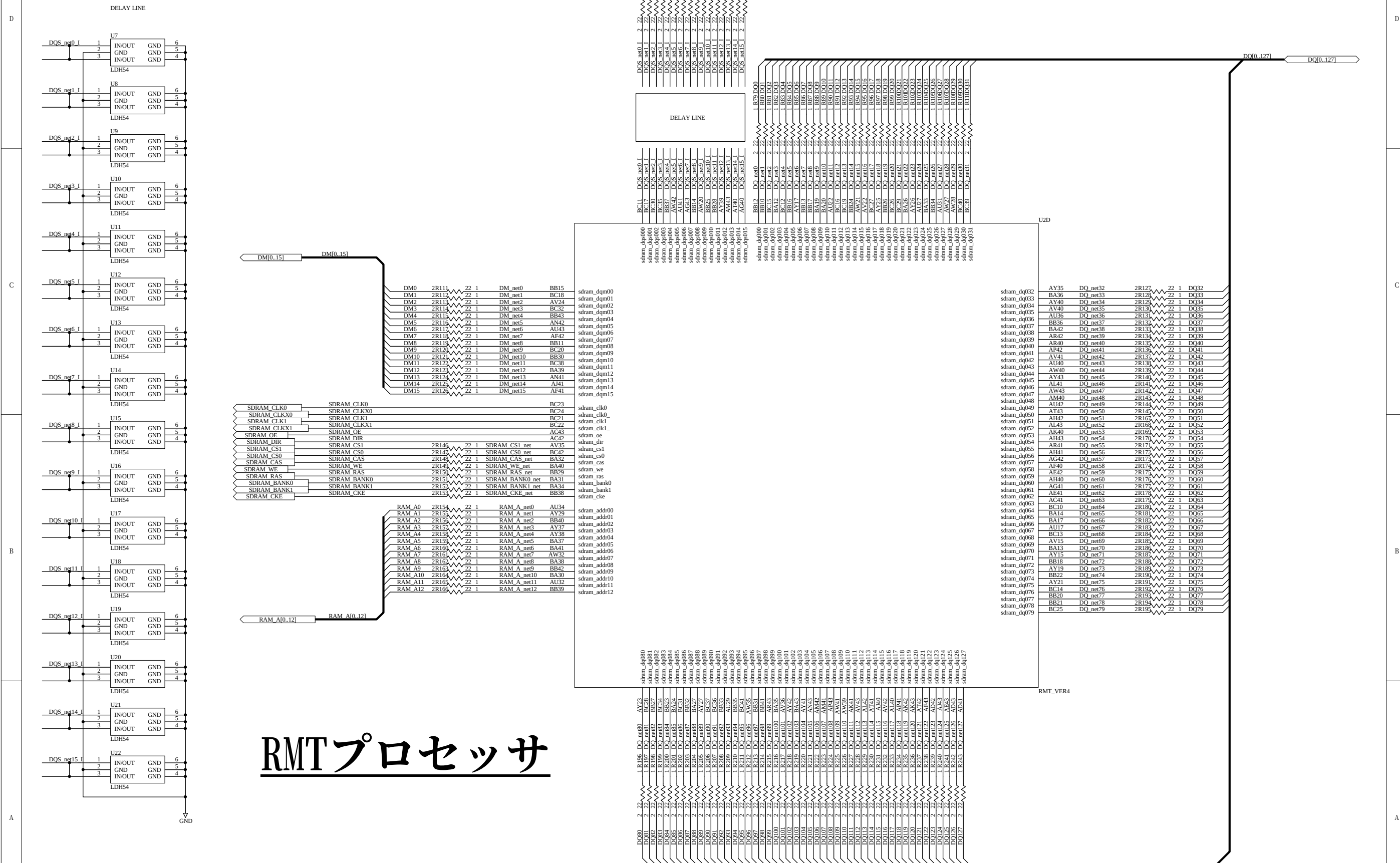
DDR SDRAMと接続



PCIと接続



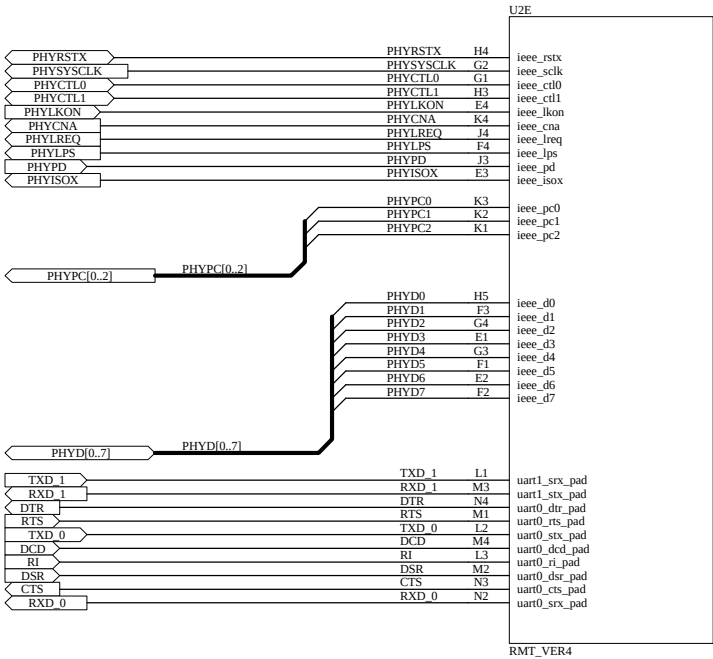
DDR S0-DIMMと接続



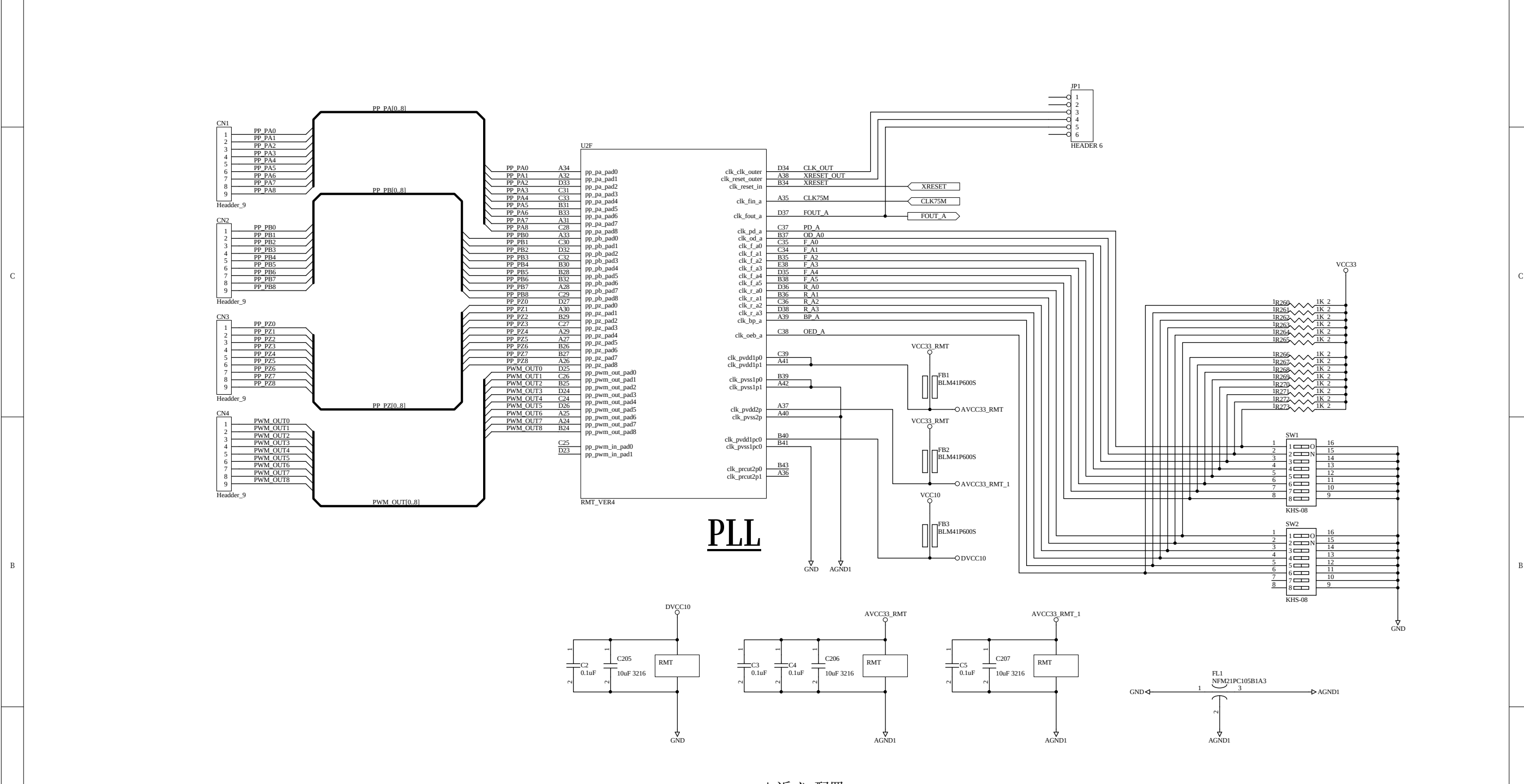
RMTプロセッサ

1394へ

UARTへ

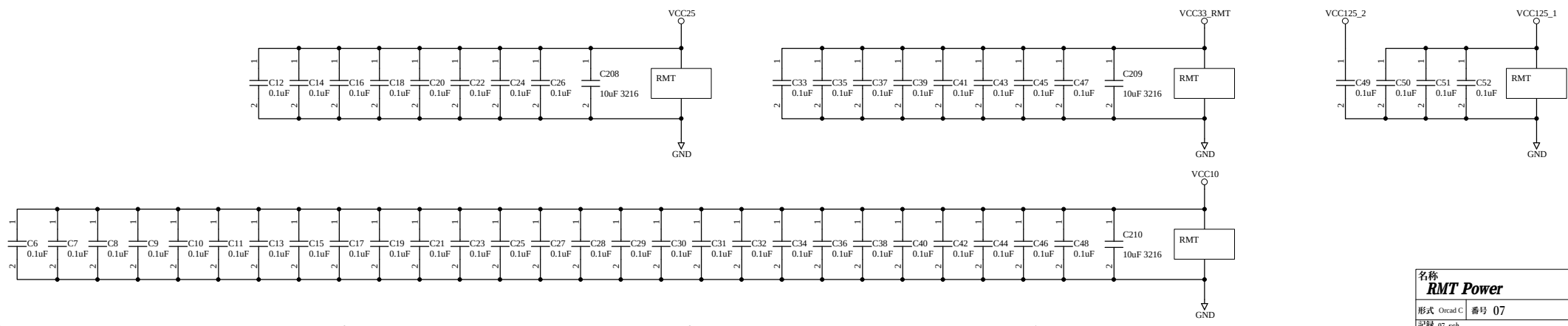
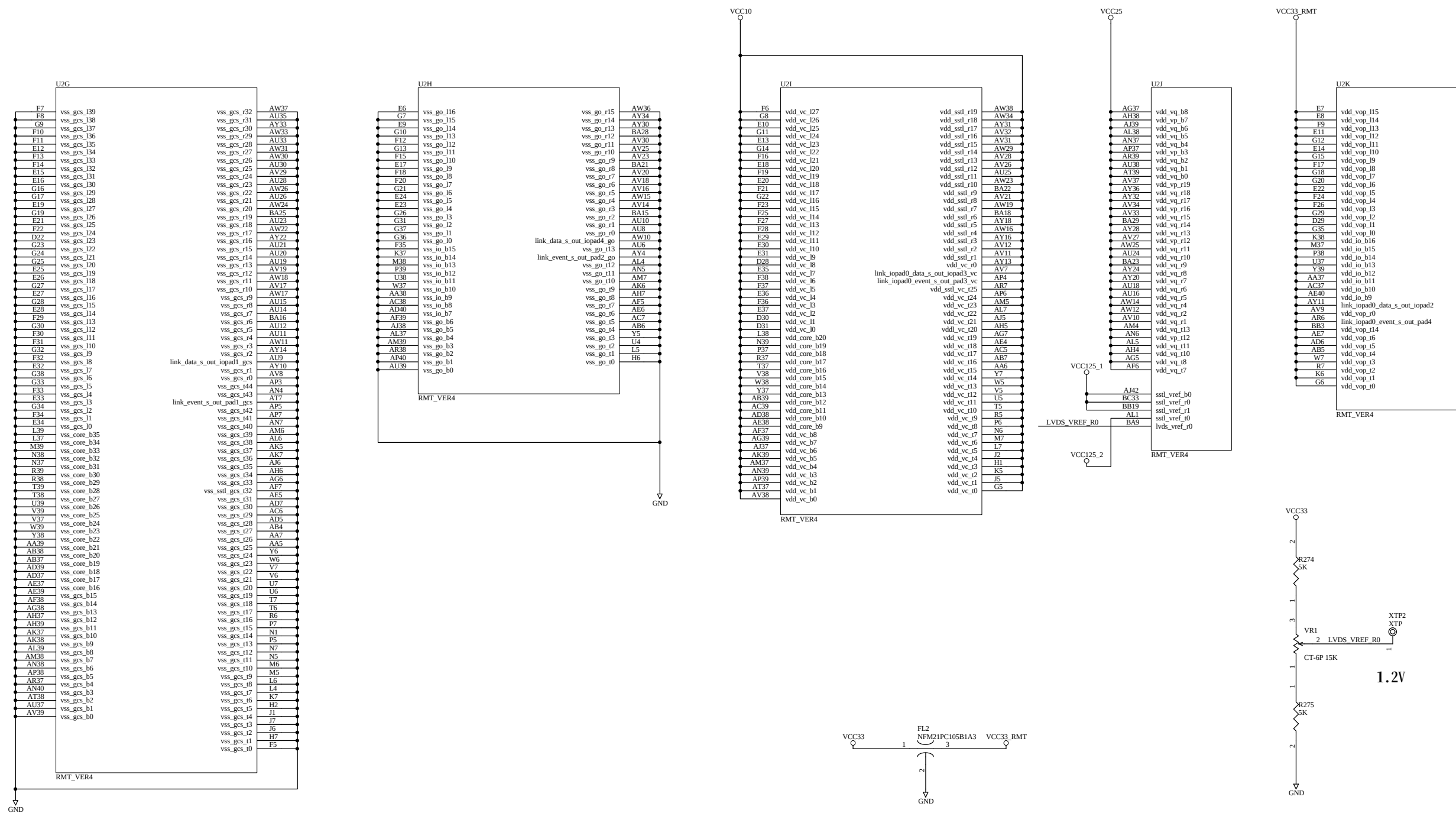


D		D
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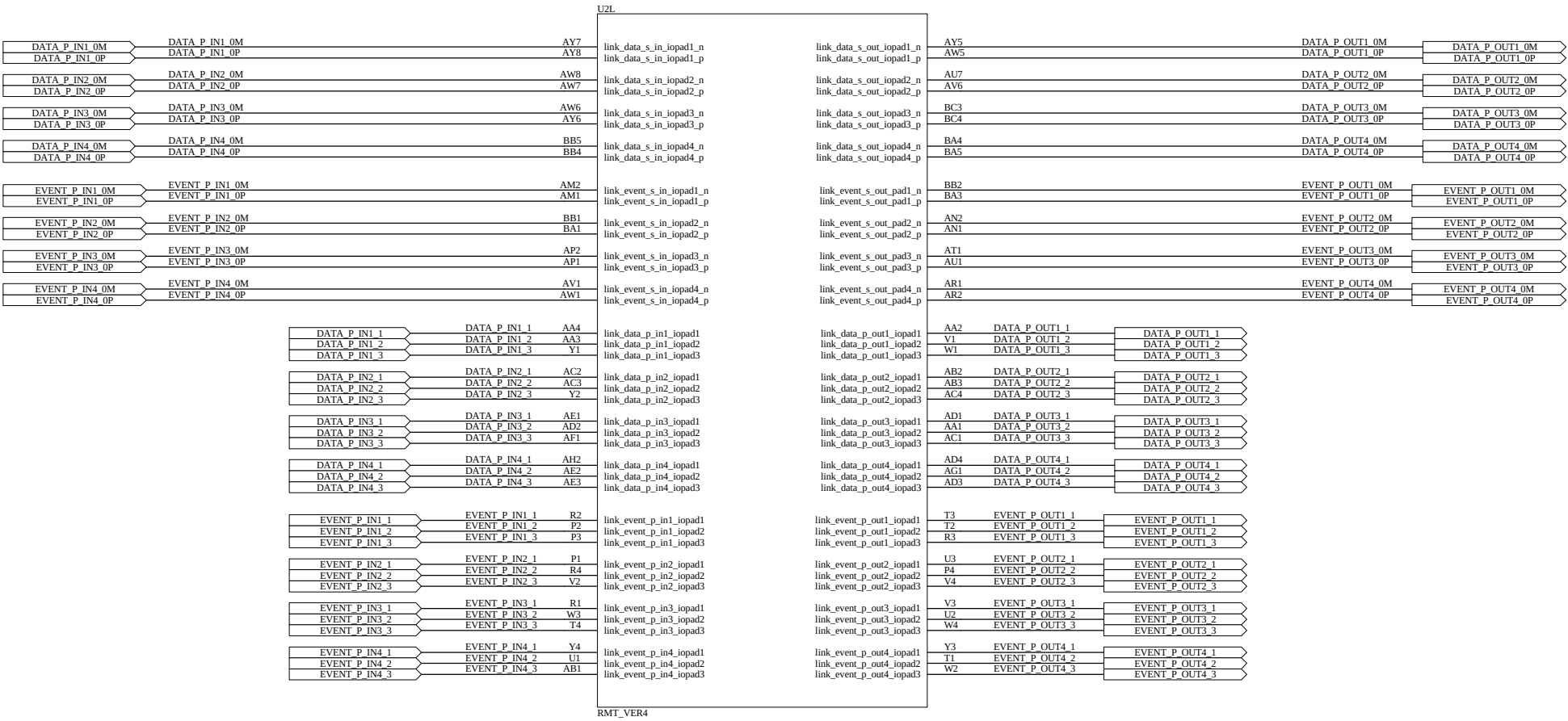


pin近くに配置

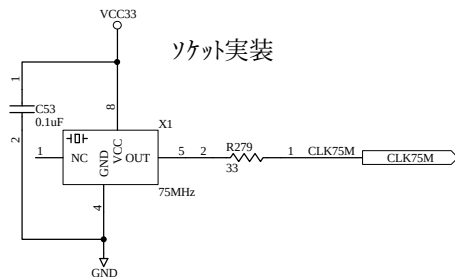
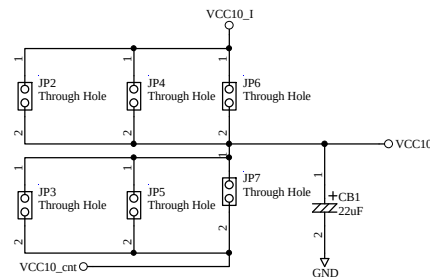
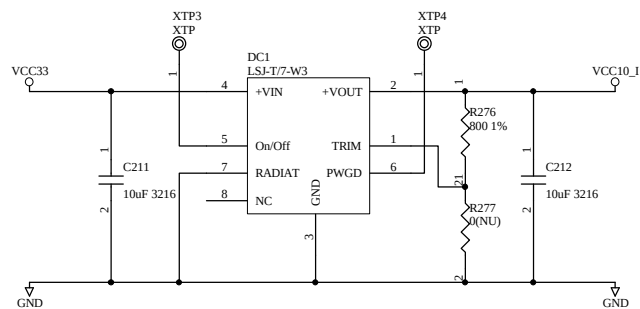
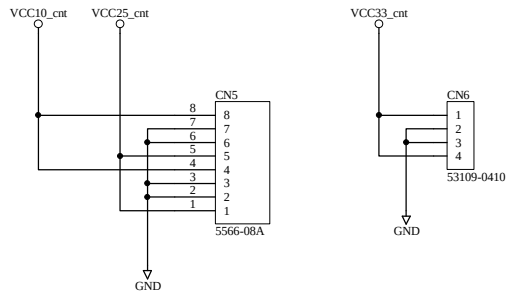
RMTプロセッサ



RMTプロセッサ

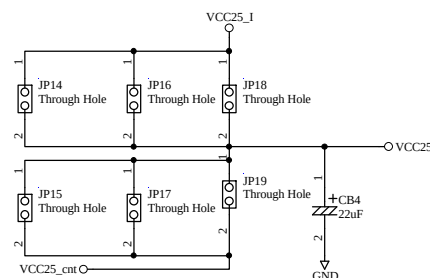
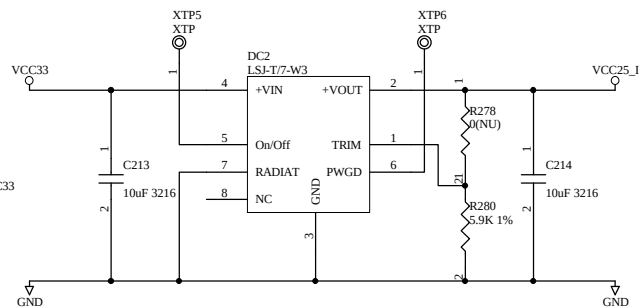
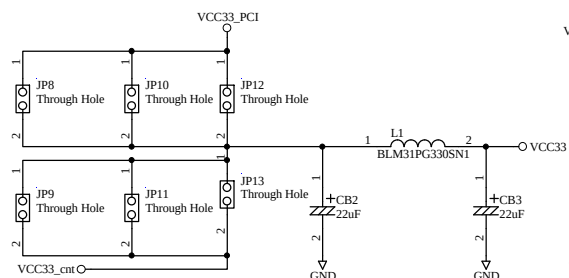


1V OUTPUT

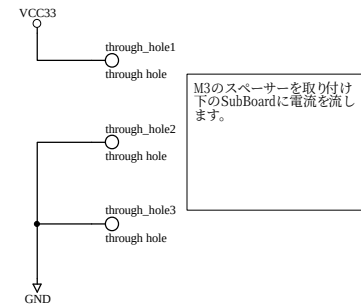
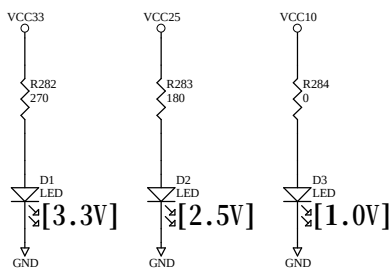
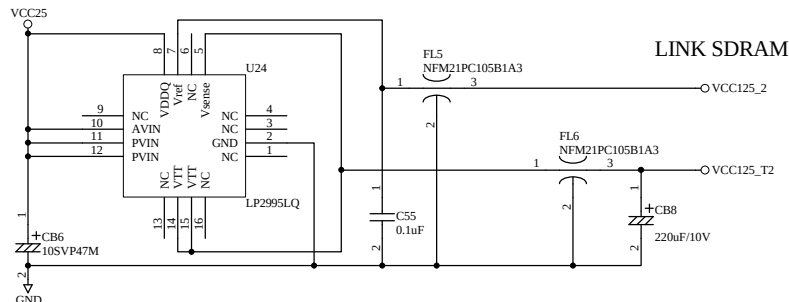
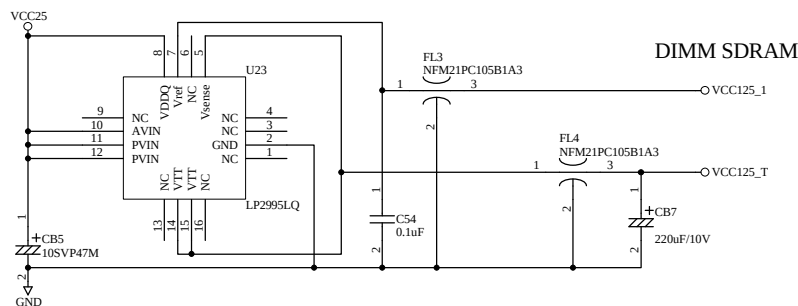


ソケット実装

2.5V OUTPUT

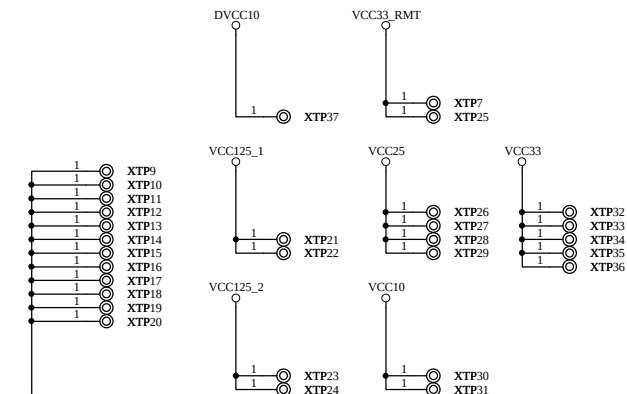
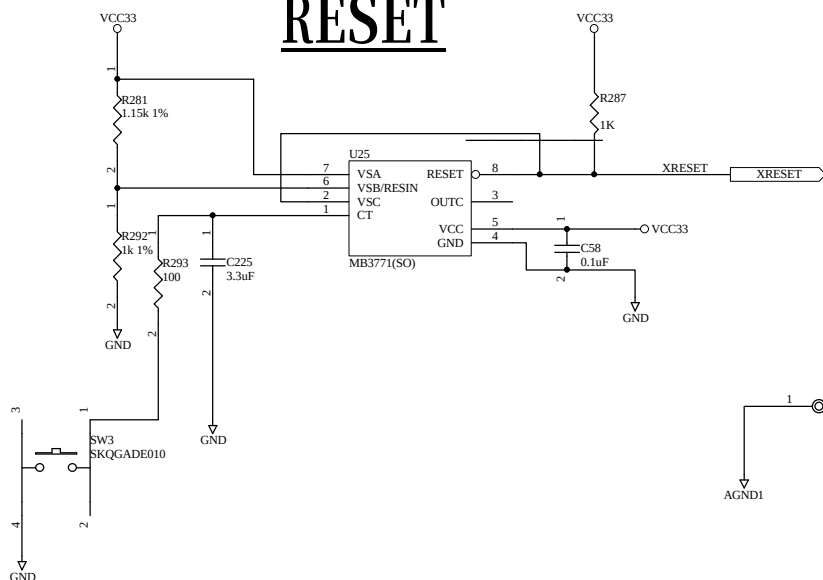


1.25V OUTPUT



M3のスペーサーを取り付け
下のSubBoardに電流を流し
ます。

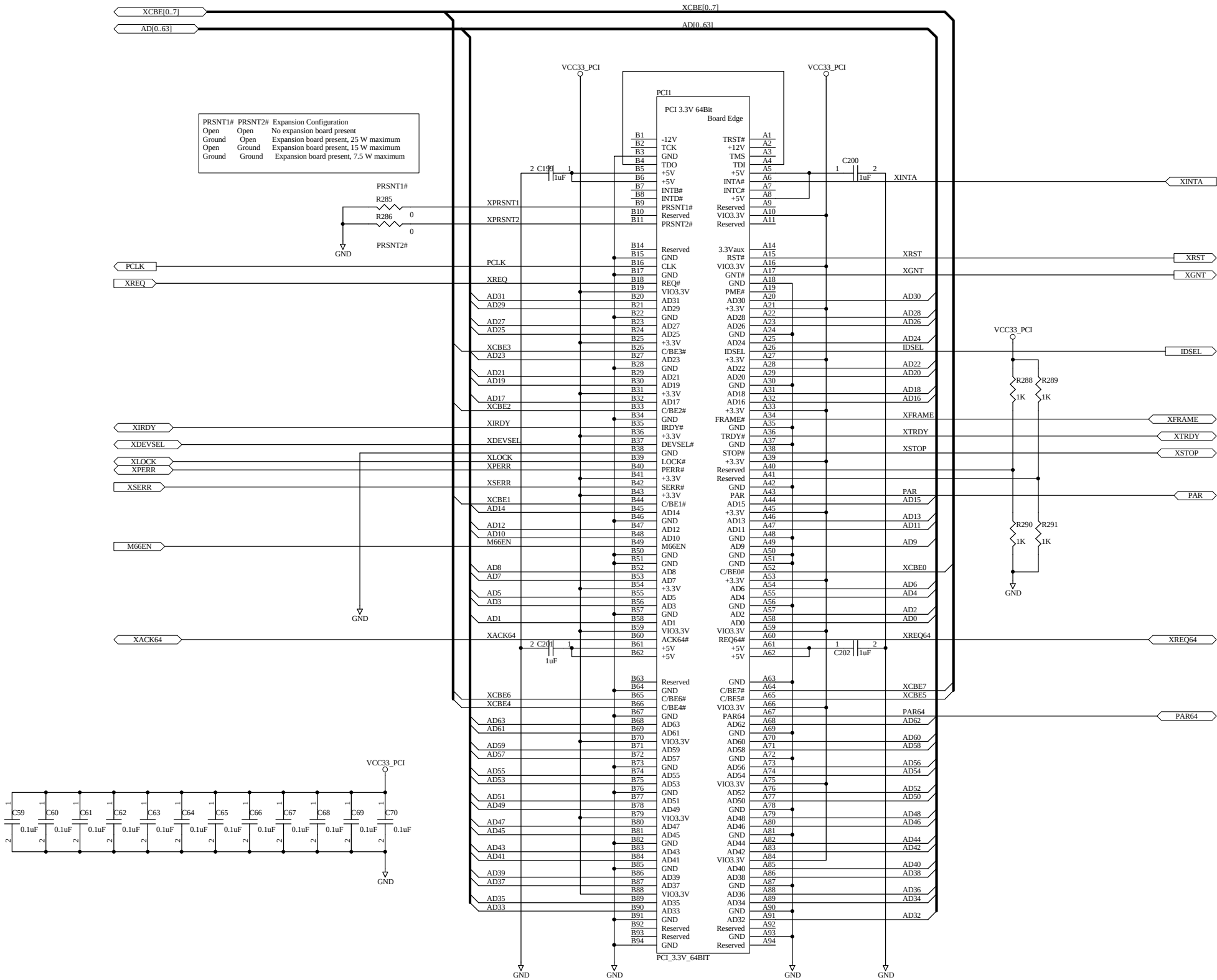
RESET

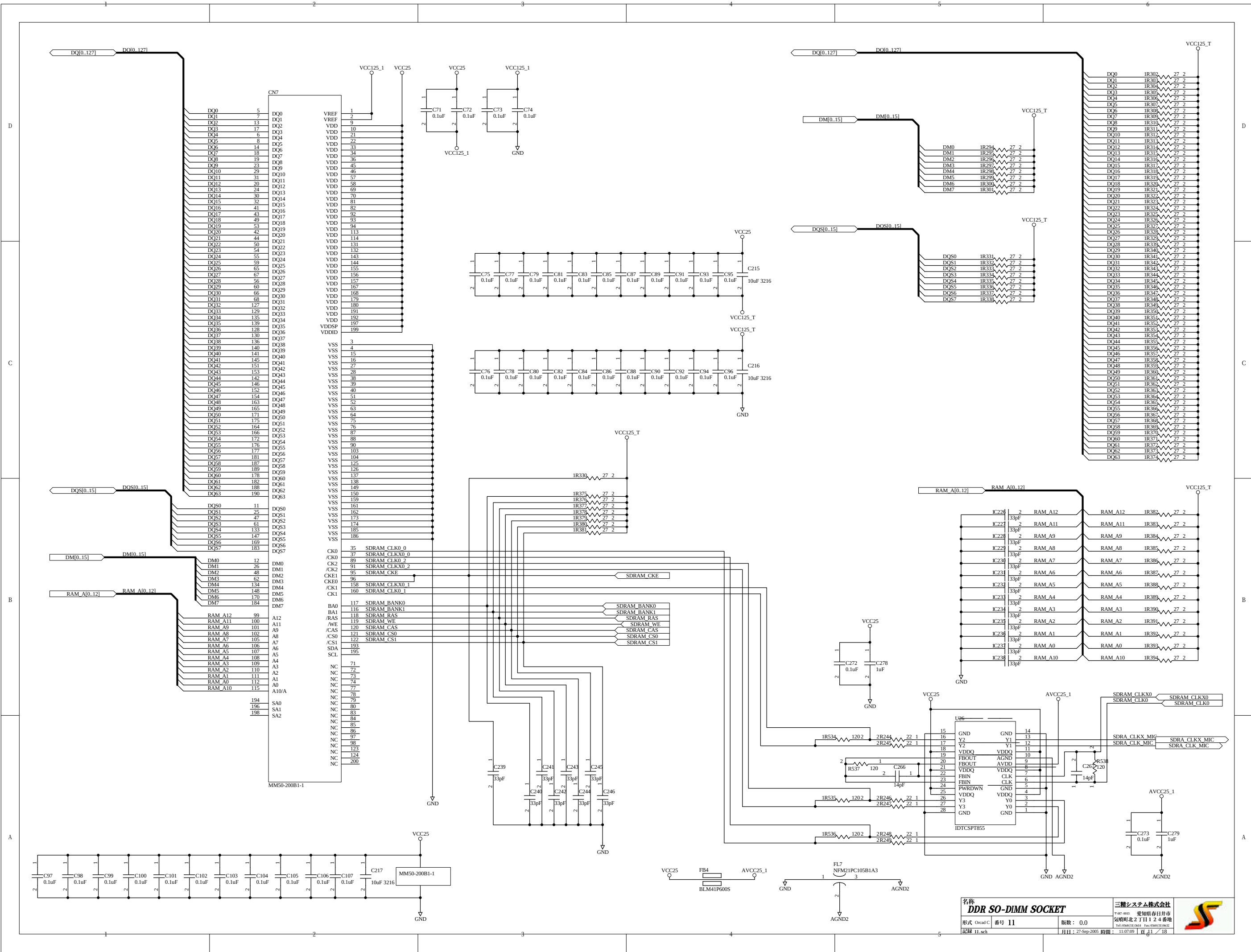


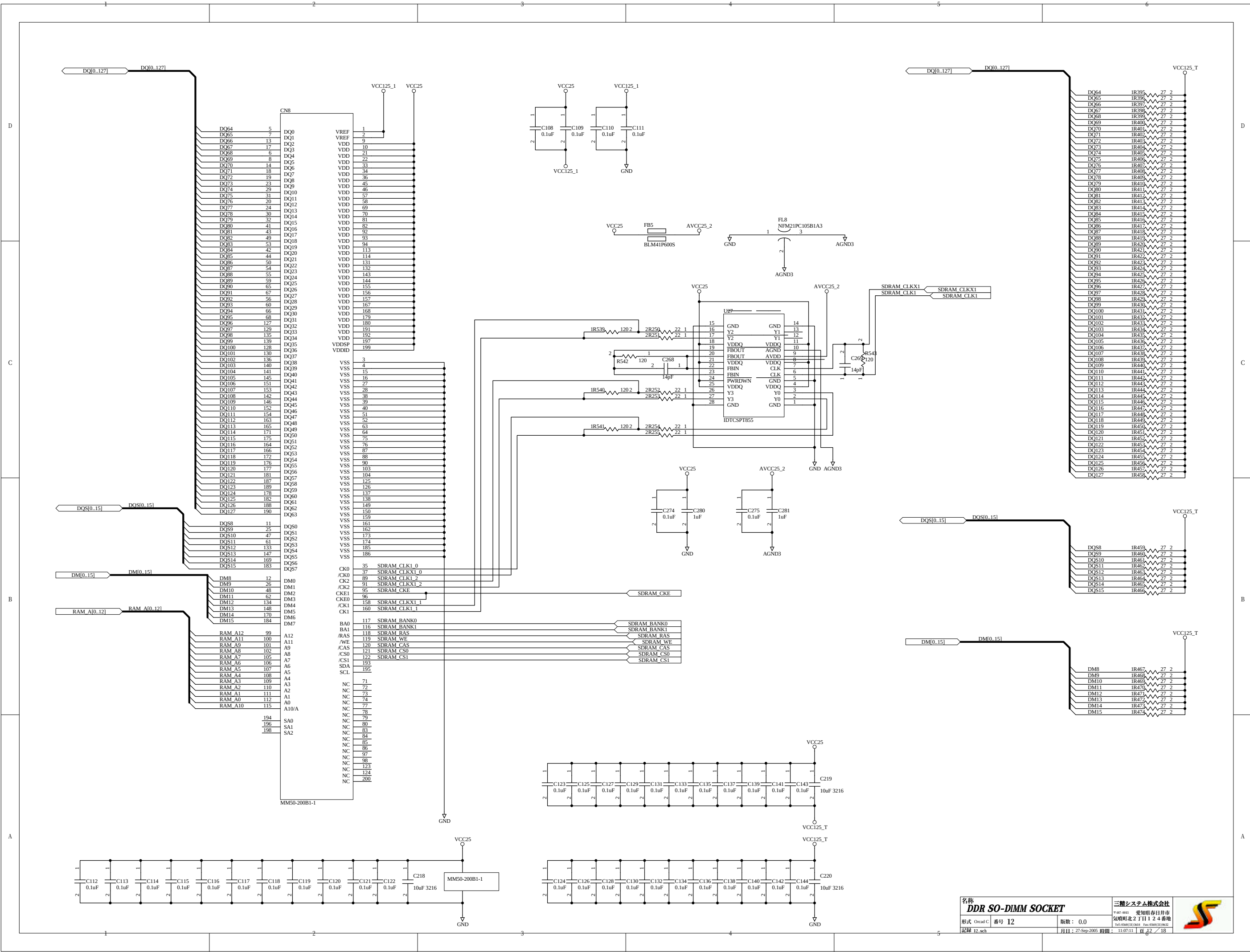
名称	Power & Reset	三精システム株式会社
形式	Orcad C	9487-0015 愛知県春日井市
番号	09	気頃町北2丁目124番地
版数	0.0	TEL:0563(3)3041 FAX:0563(3)3042
記録	09_sch	月日: 27-Sep-2005 前欄: 11:07:06 頁 9 / 18



PCI bus Edge







Link用 DDR SDRAM

D

D

C

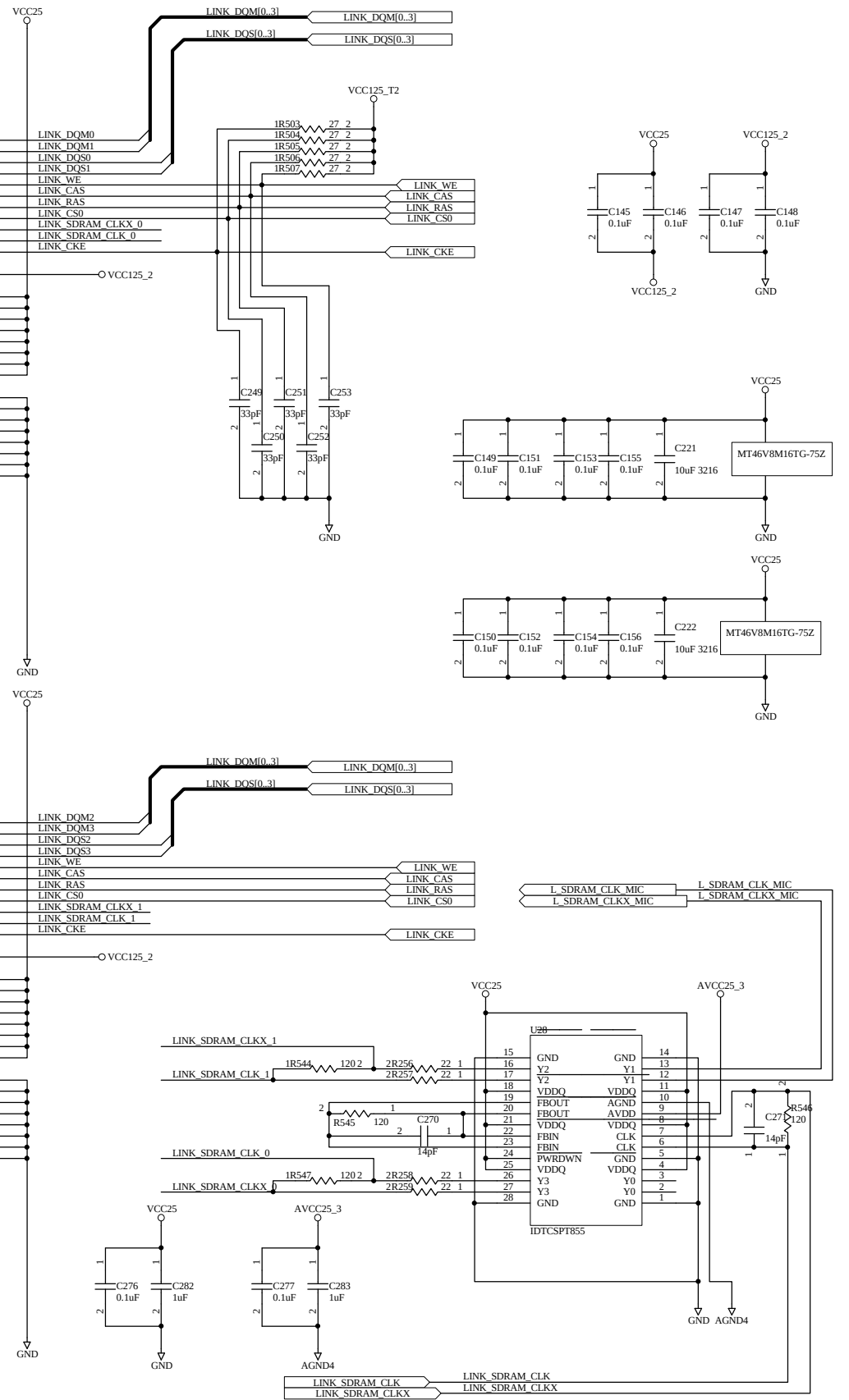
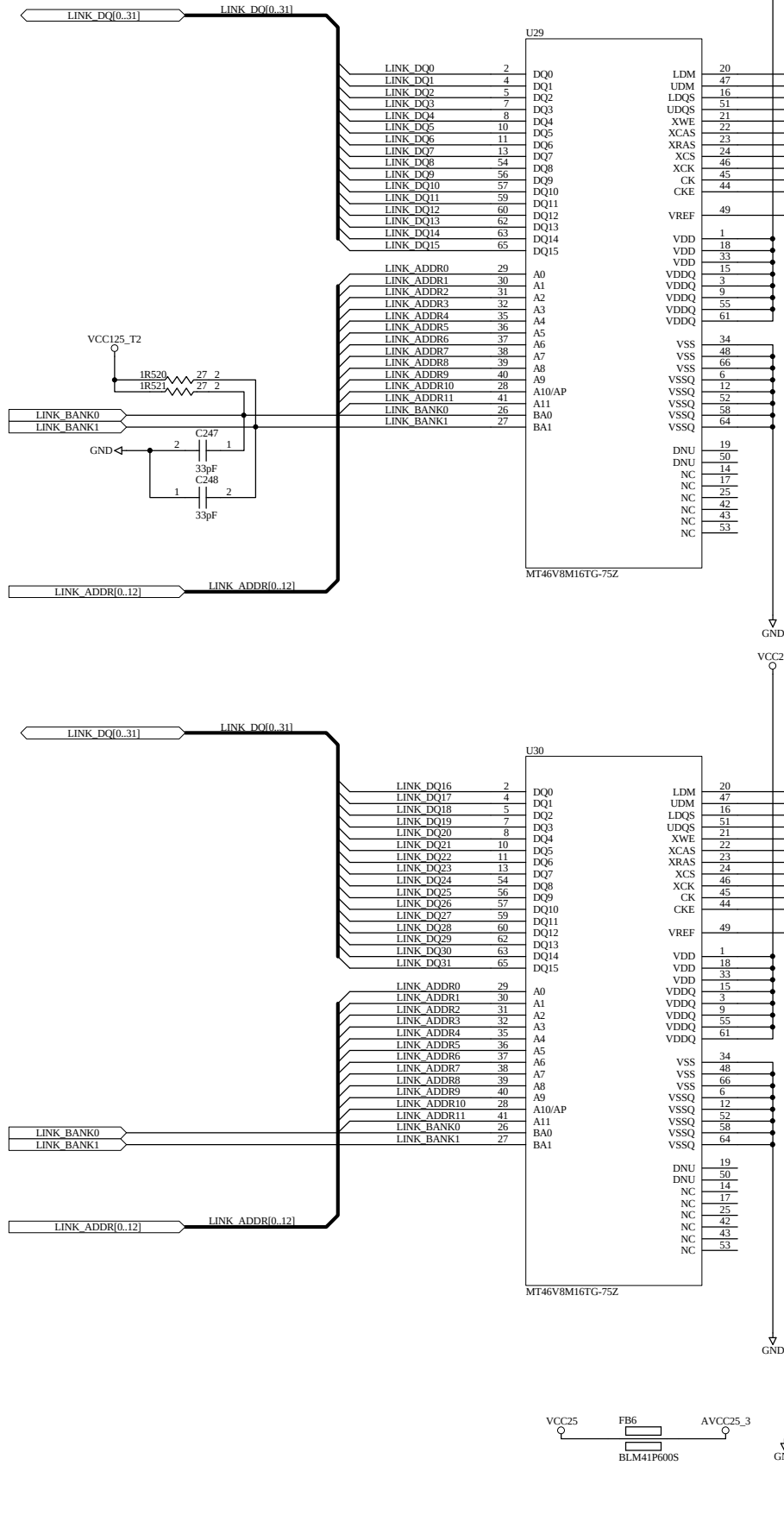
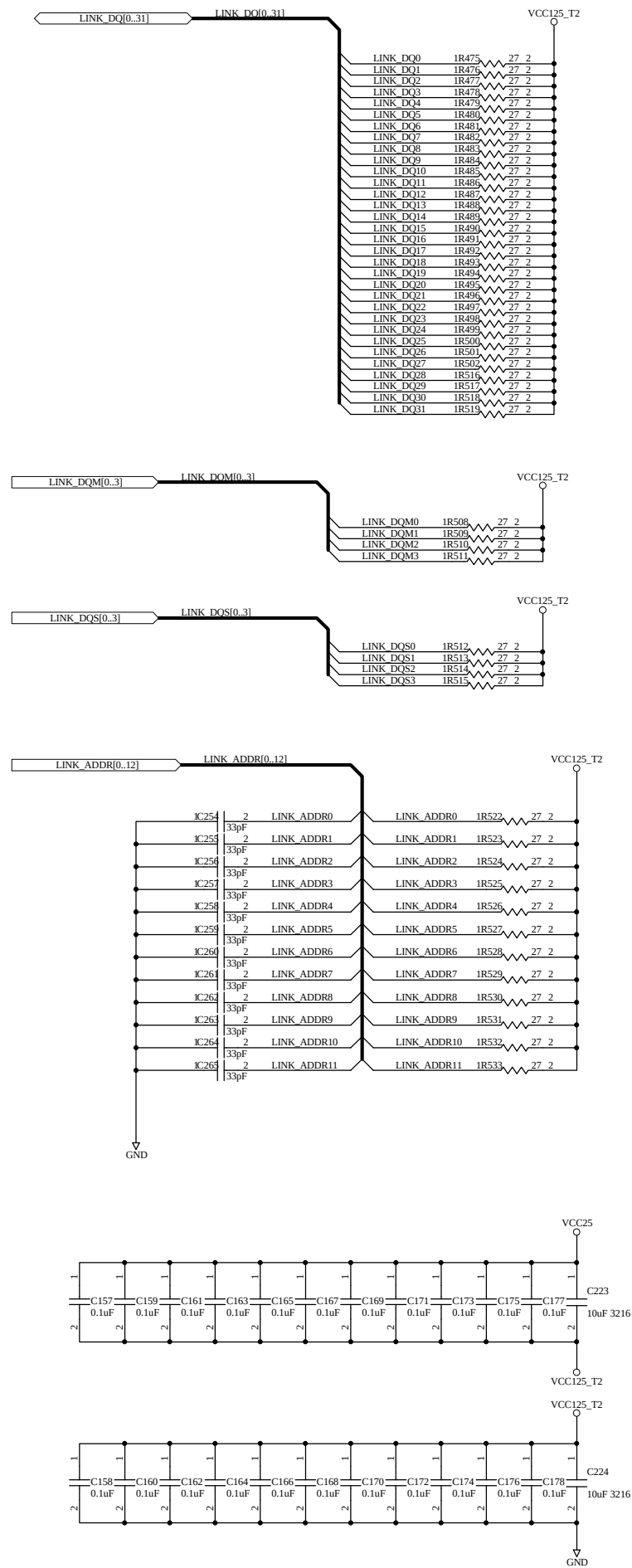
C

B

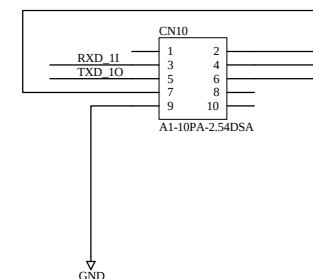
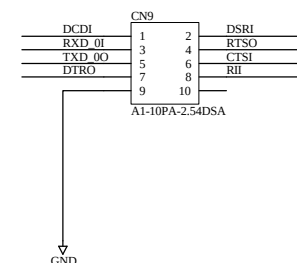
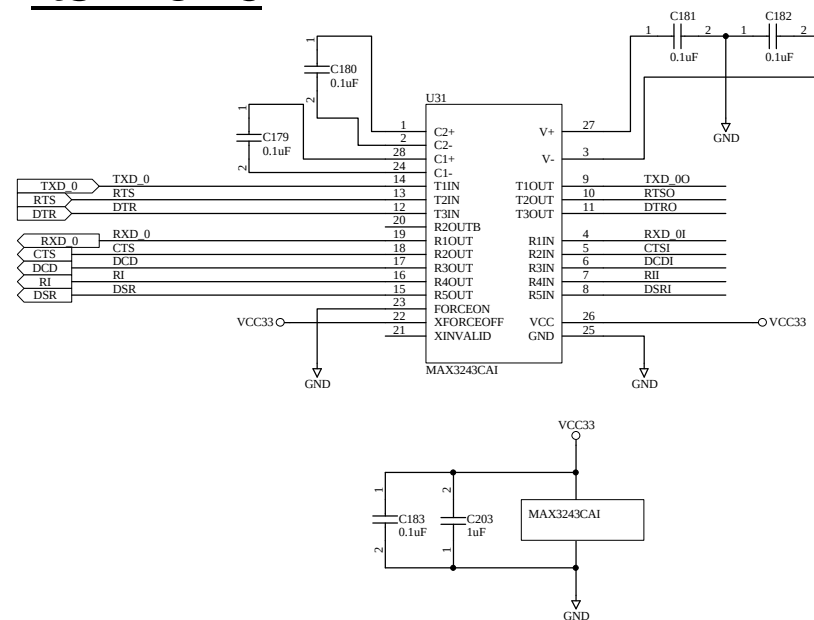
B

A

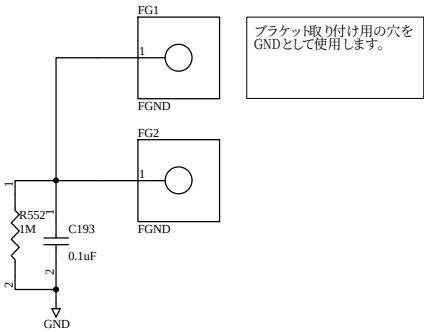
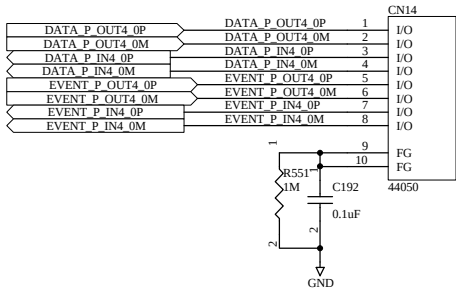
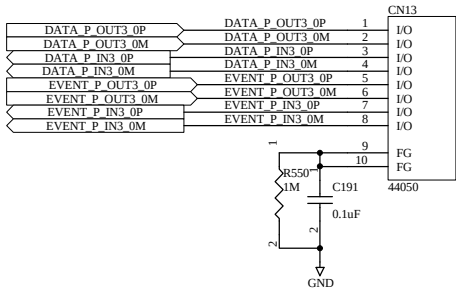
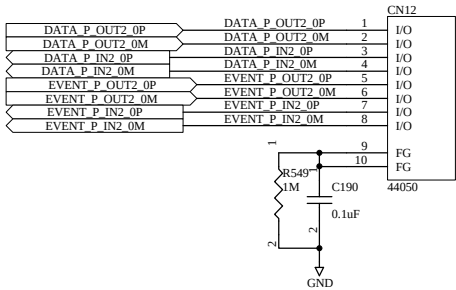
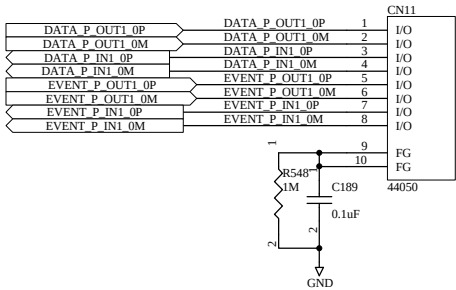
A



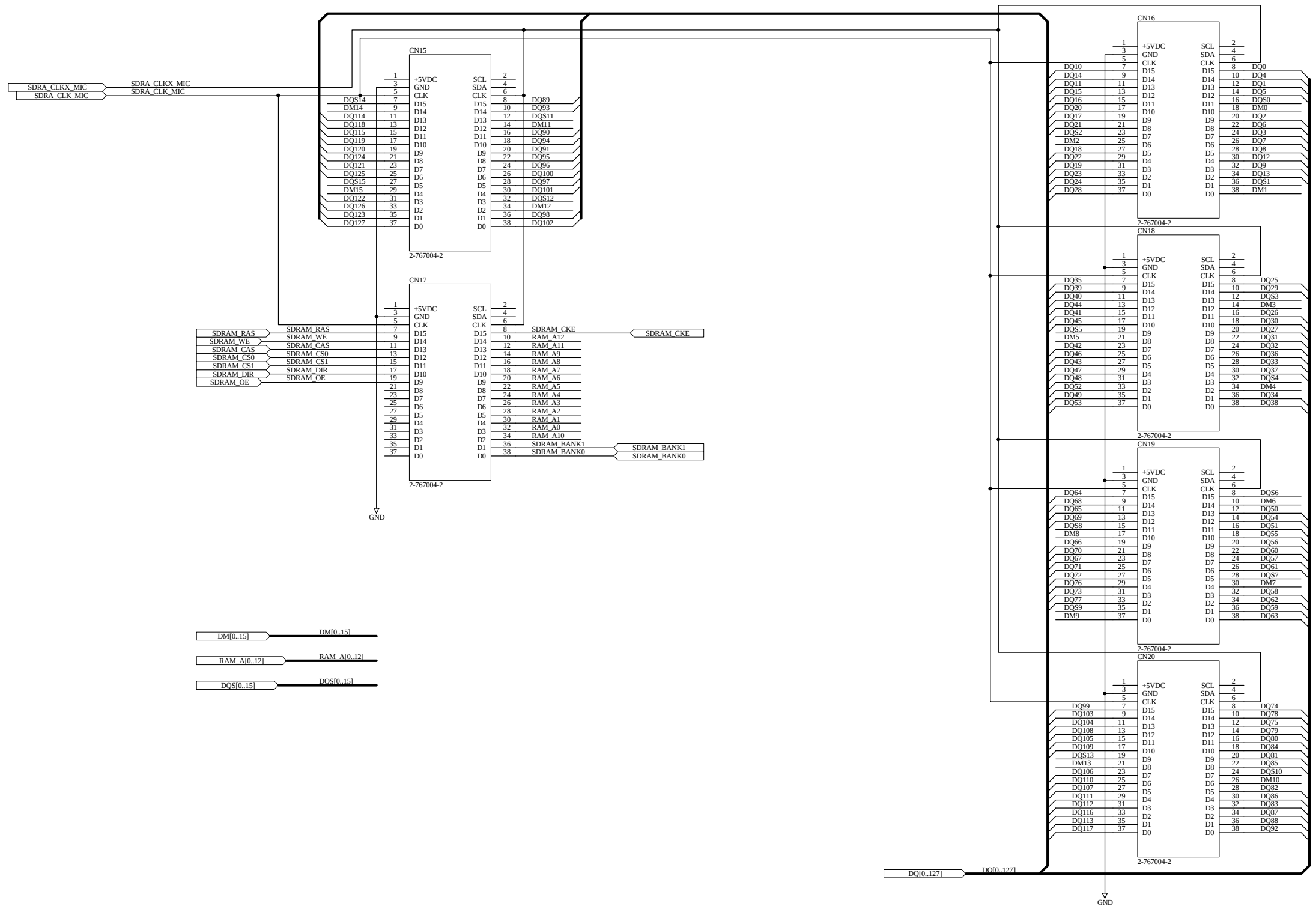
RS-232C



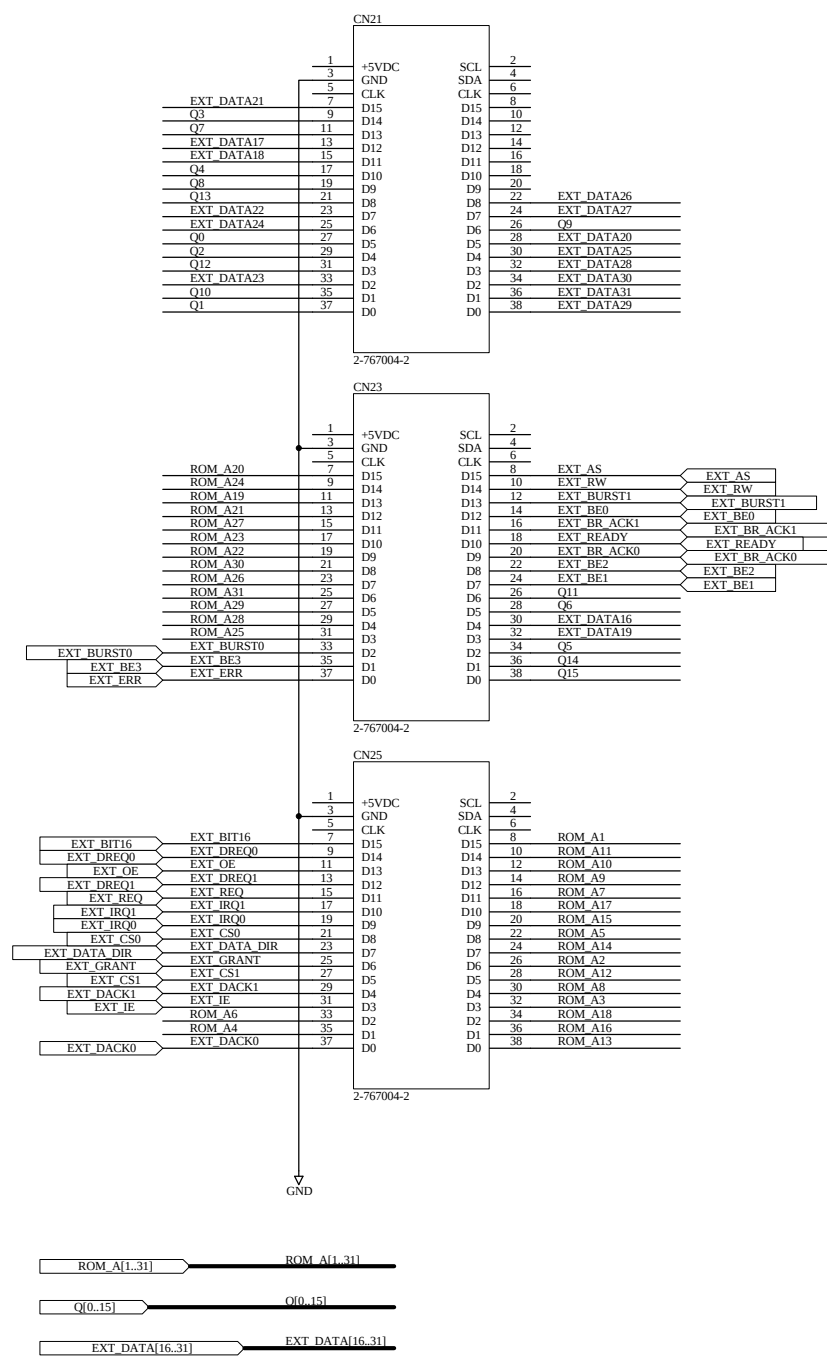
LINK Serial



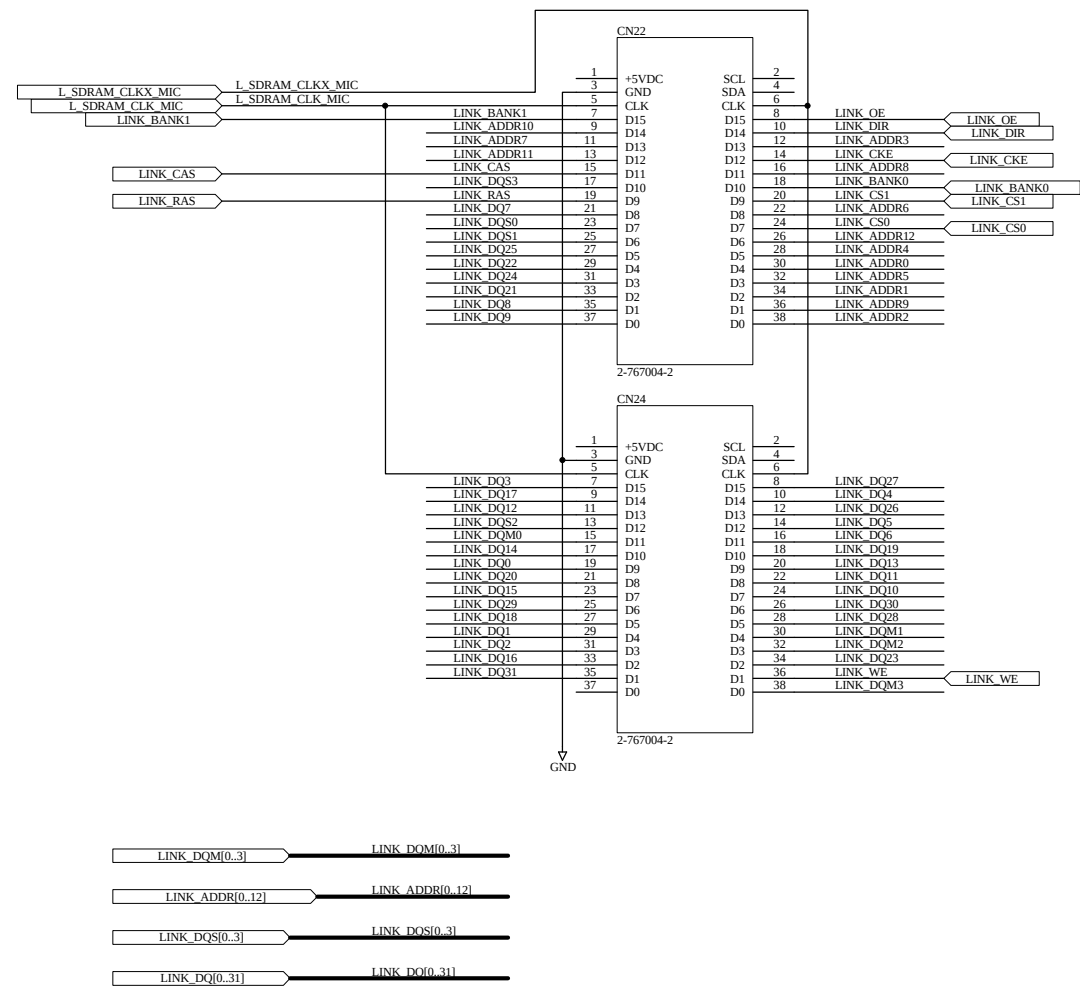
DDR SO-DIMMと接続



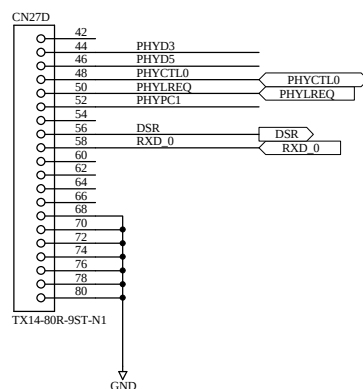
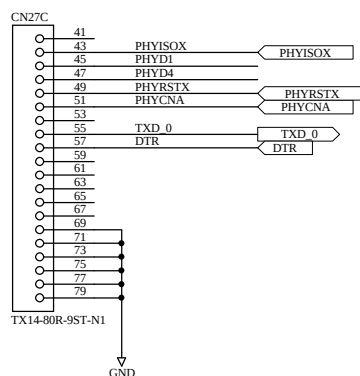
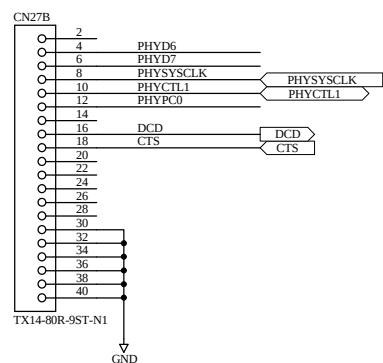
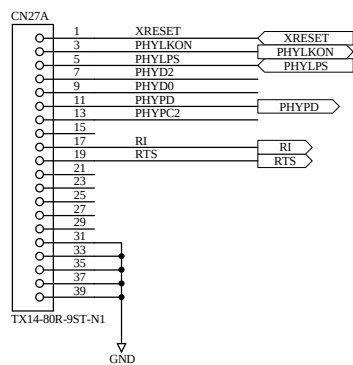
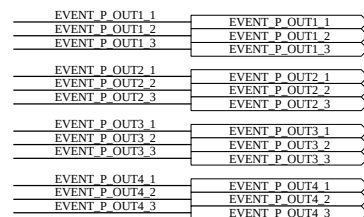
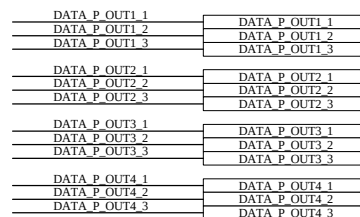
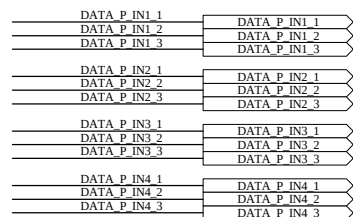
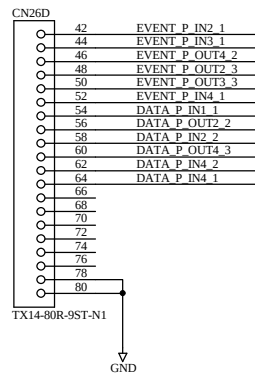
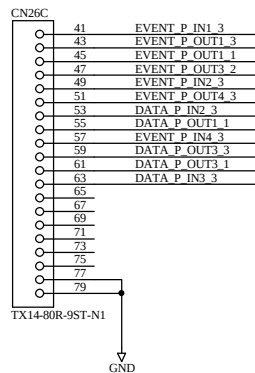
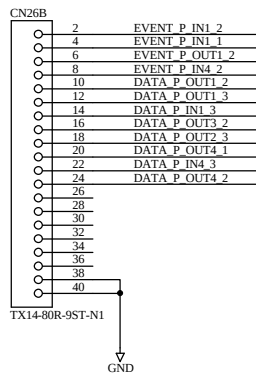
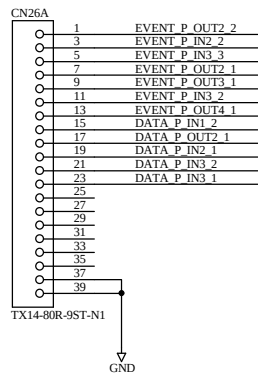
FLASH-ROM部と接続

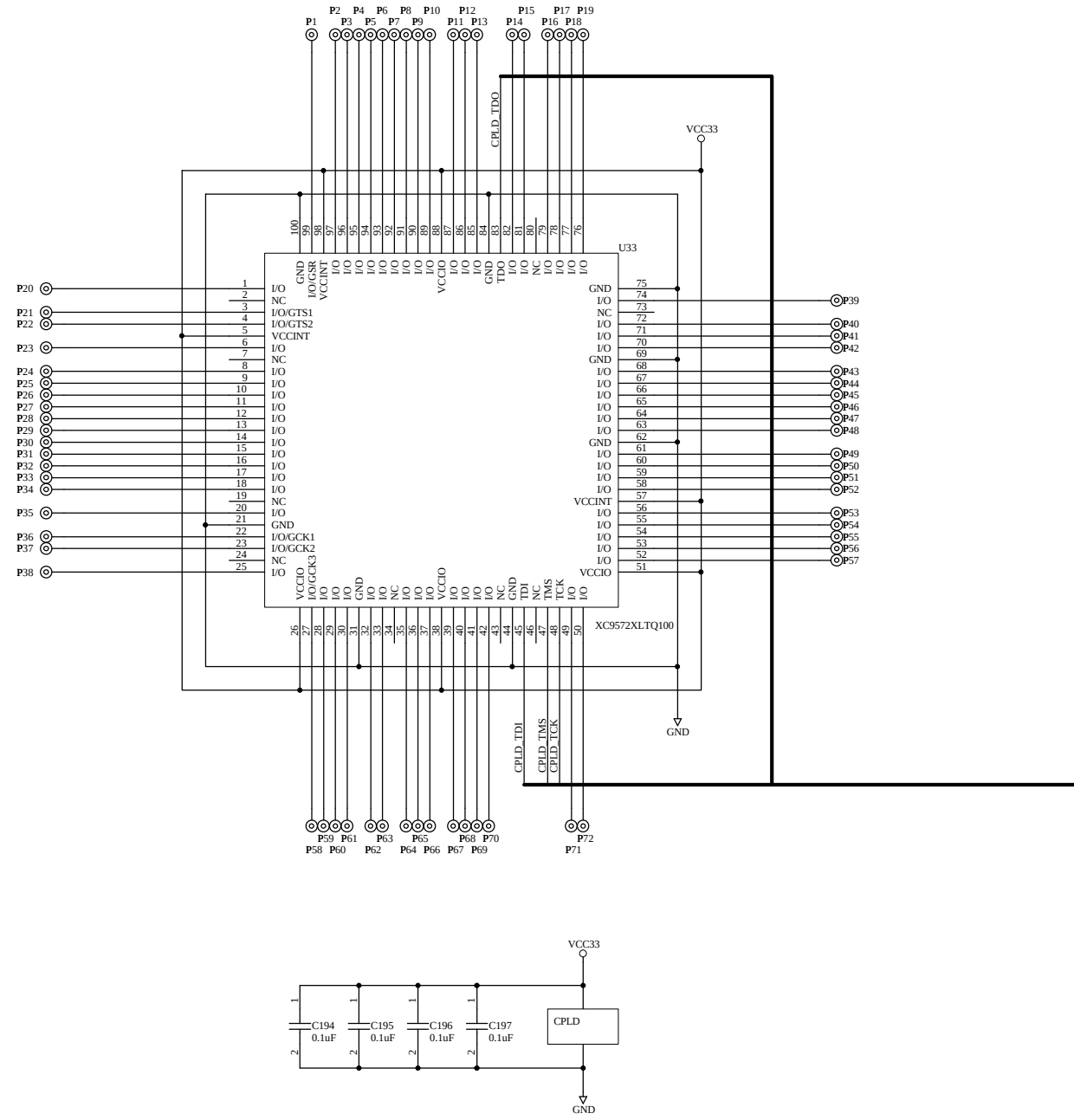


LINKSDRAM部と接続



2ボード間接続コネクタ





CPLD書き込みコネクタ