

RMT7。ロセッサPCI評価ボード Sub

P02
P02.sch

2:USB

P03
P03.sch

3:IEEE1394

P04
P04_sch

4:To PCI EVA Board main Connector

P05
P05.sch

5:LVDS INPUT

P06
P06.sch

6:LVDS OUTPUT

P07
P07.sch

7:RJ45 (Parallel-Input)

P08
P08.sch

8:RJ45 (Parallel-Output)

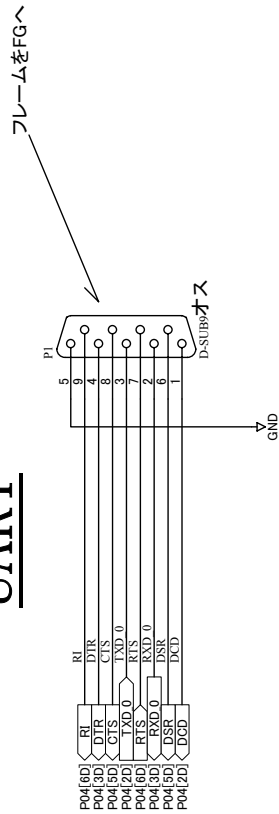
P09
P09.sch

9:Logic Analyzer Connector(USB&LINK&1394)

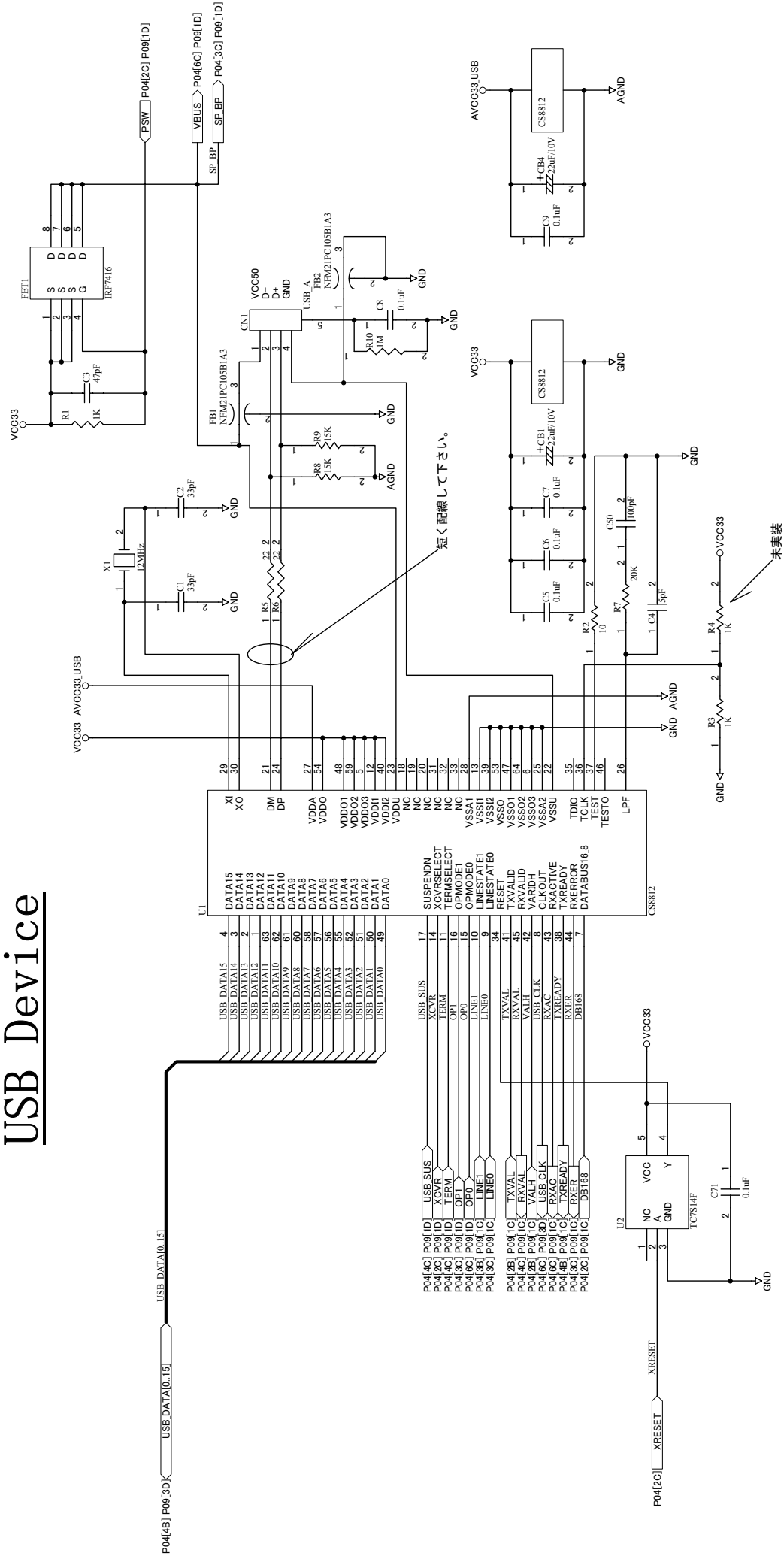
P10
P10.sch

10:PCI EDGE (GND)

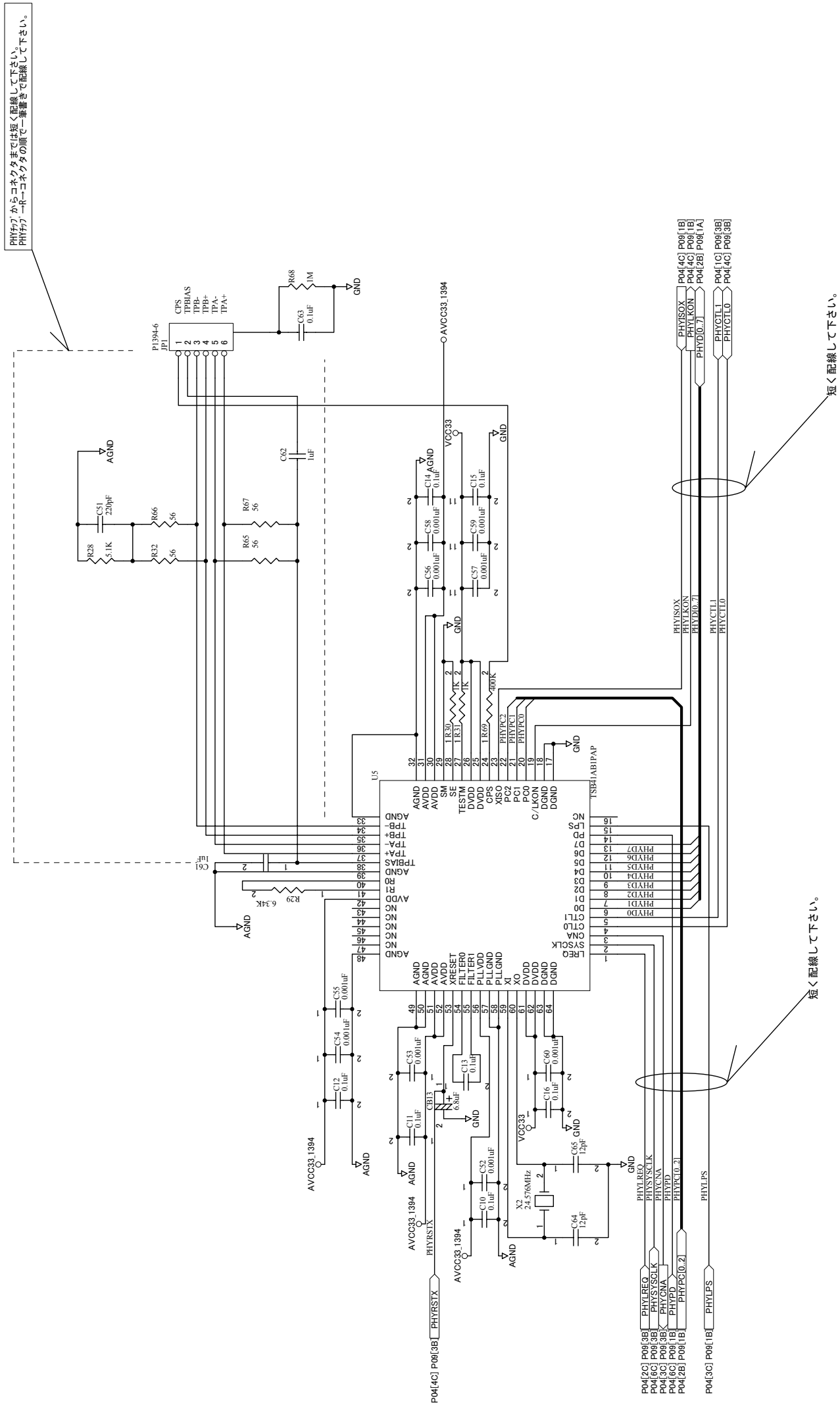
UART



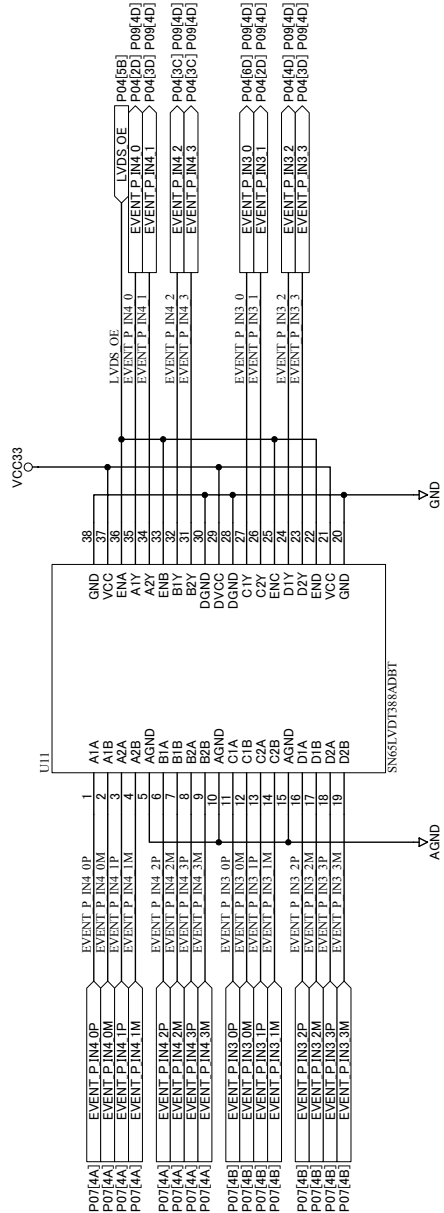
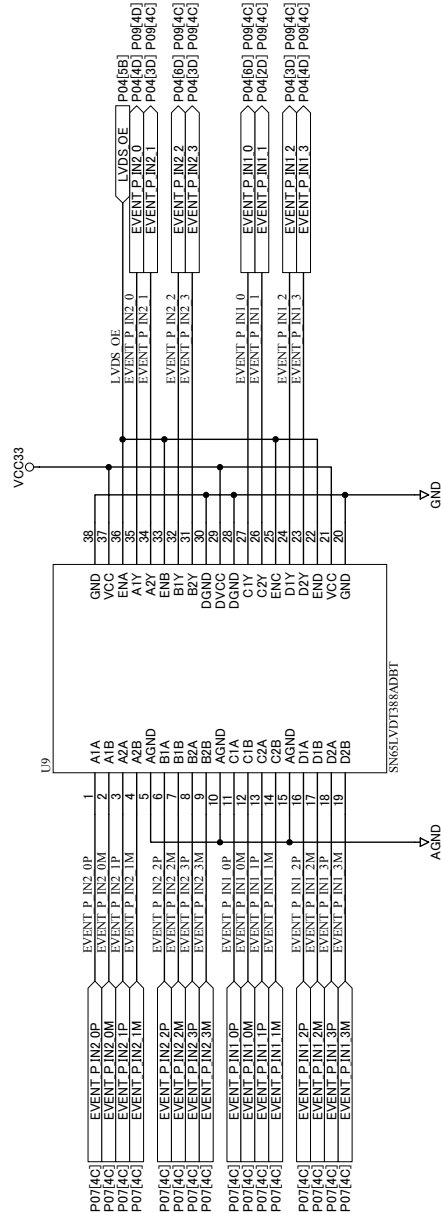
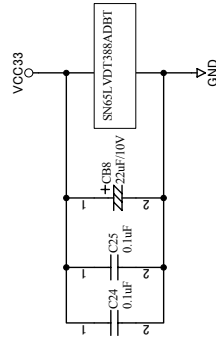
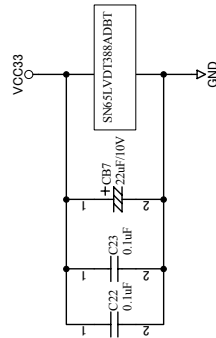
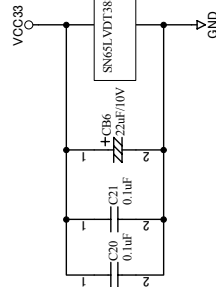
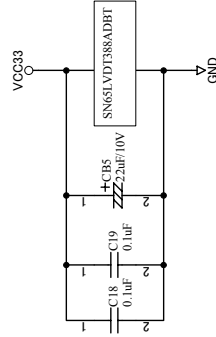
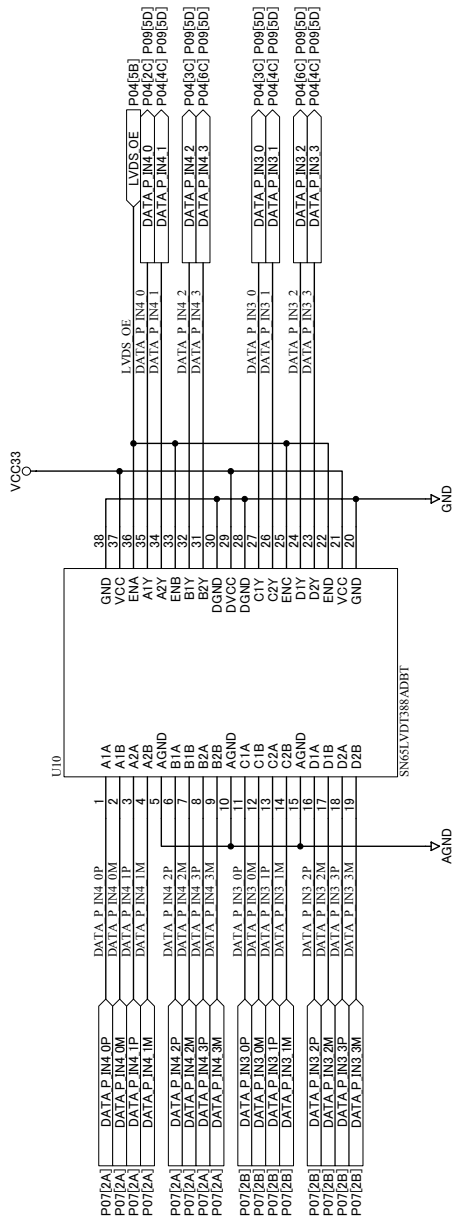
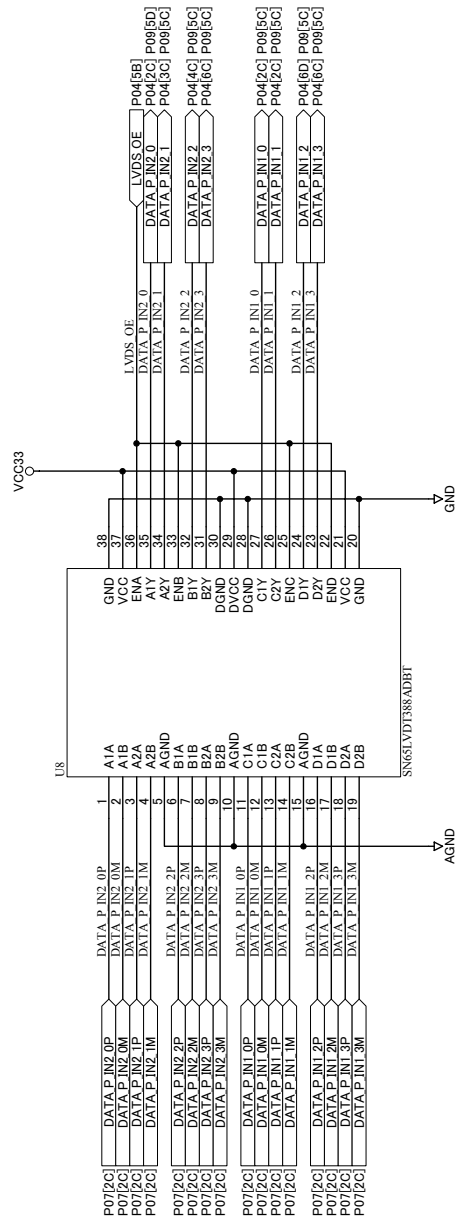
USB Device



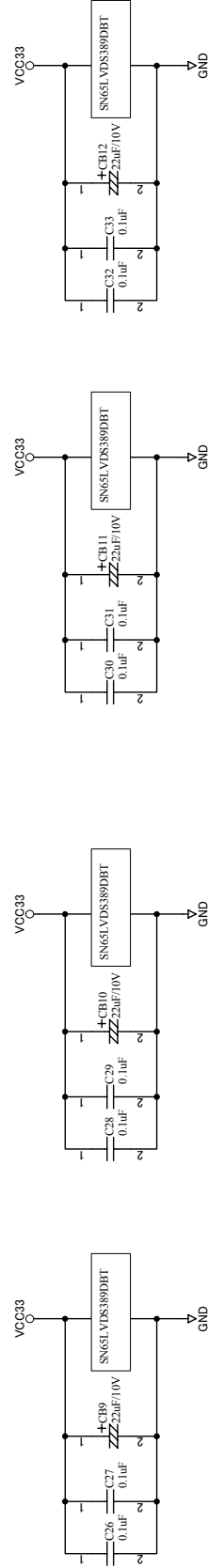
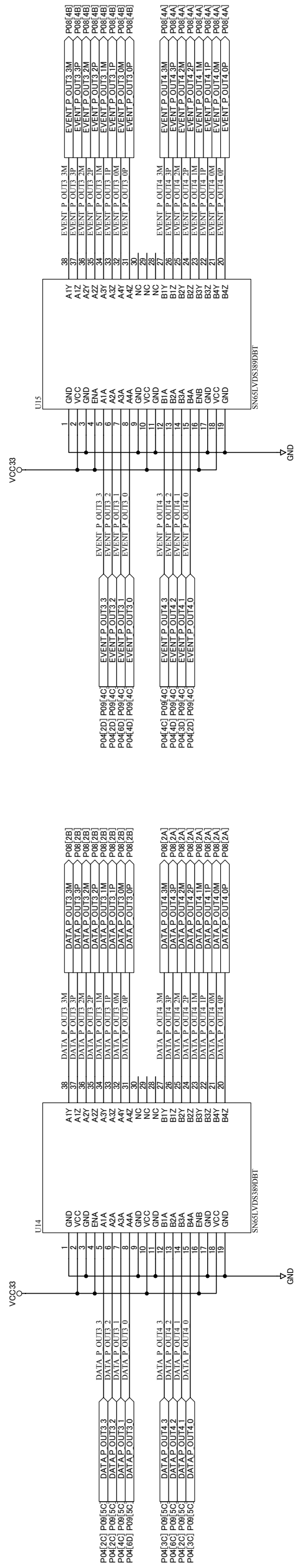
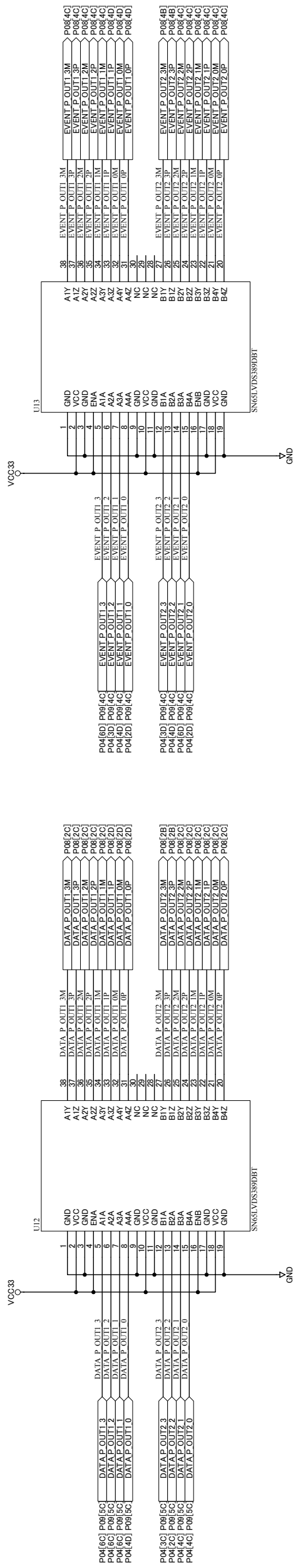
1394 PHY



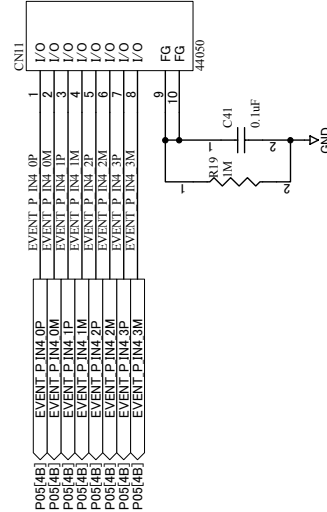
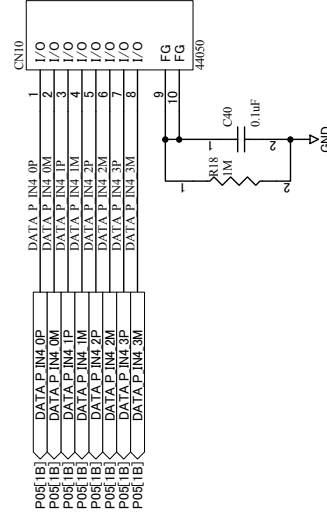
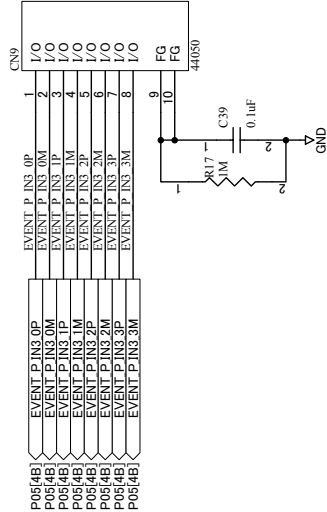
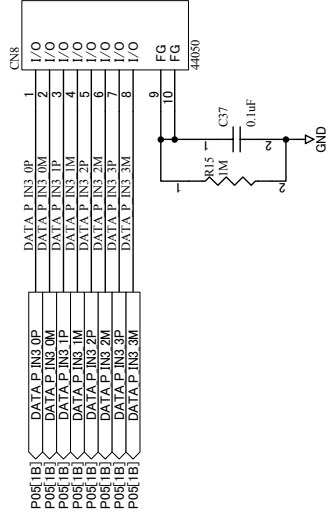
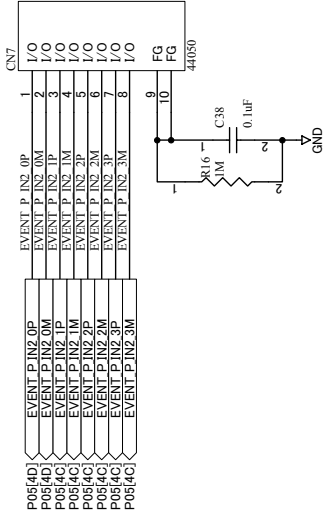
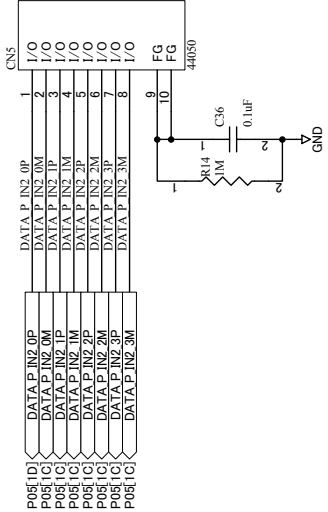
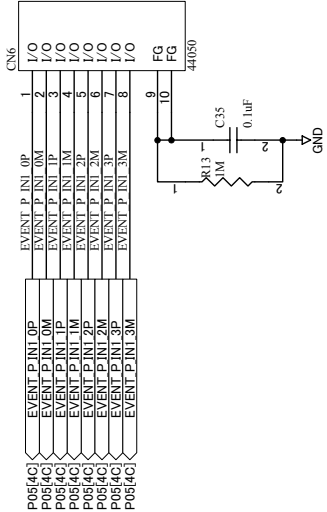
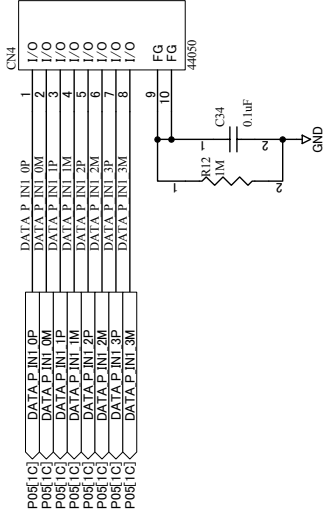
LVDS INPUT



LVDS OUTPUT

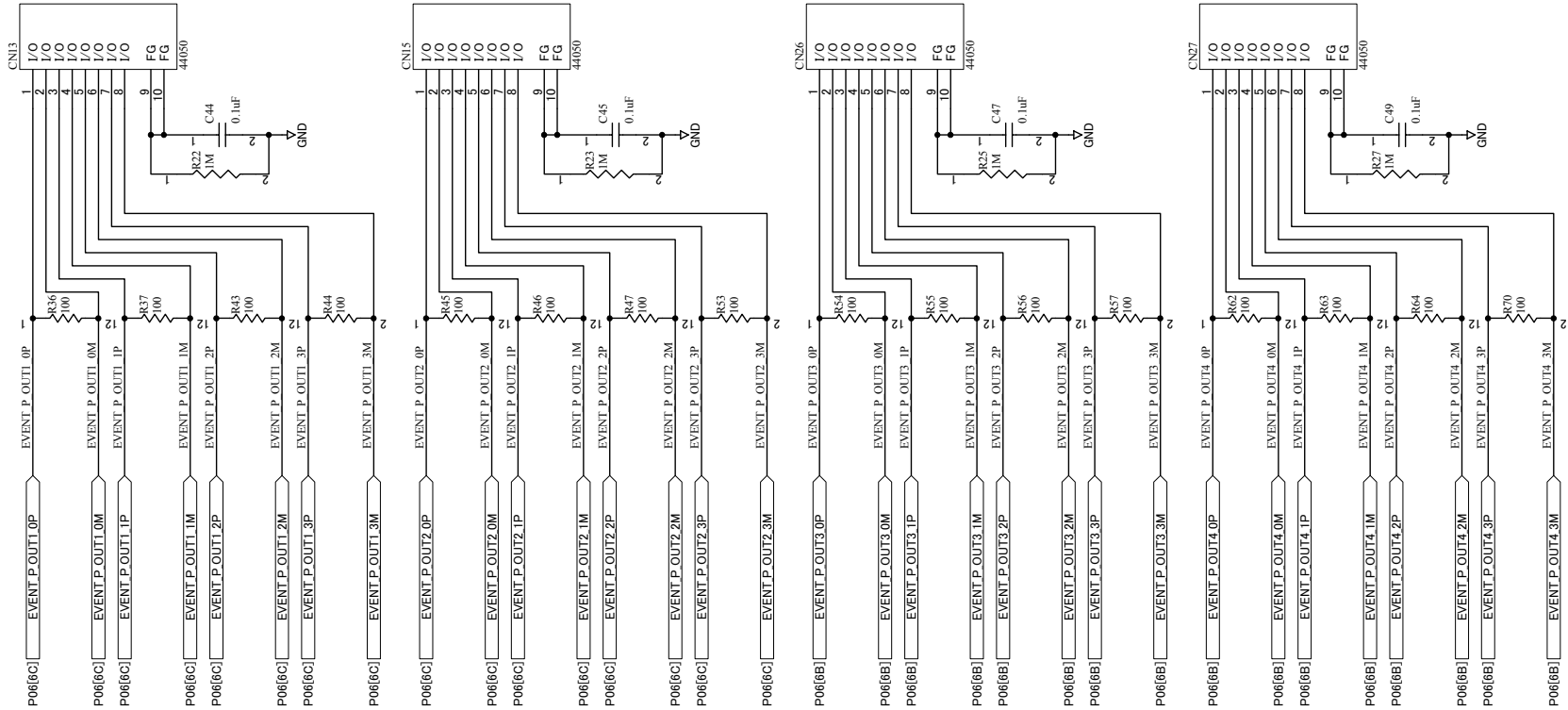
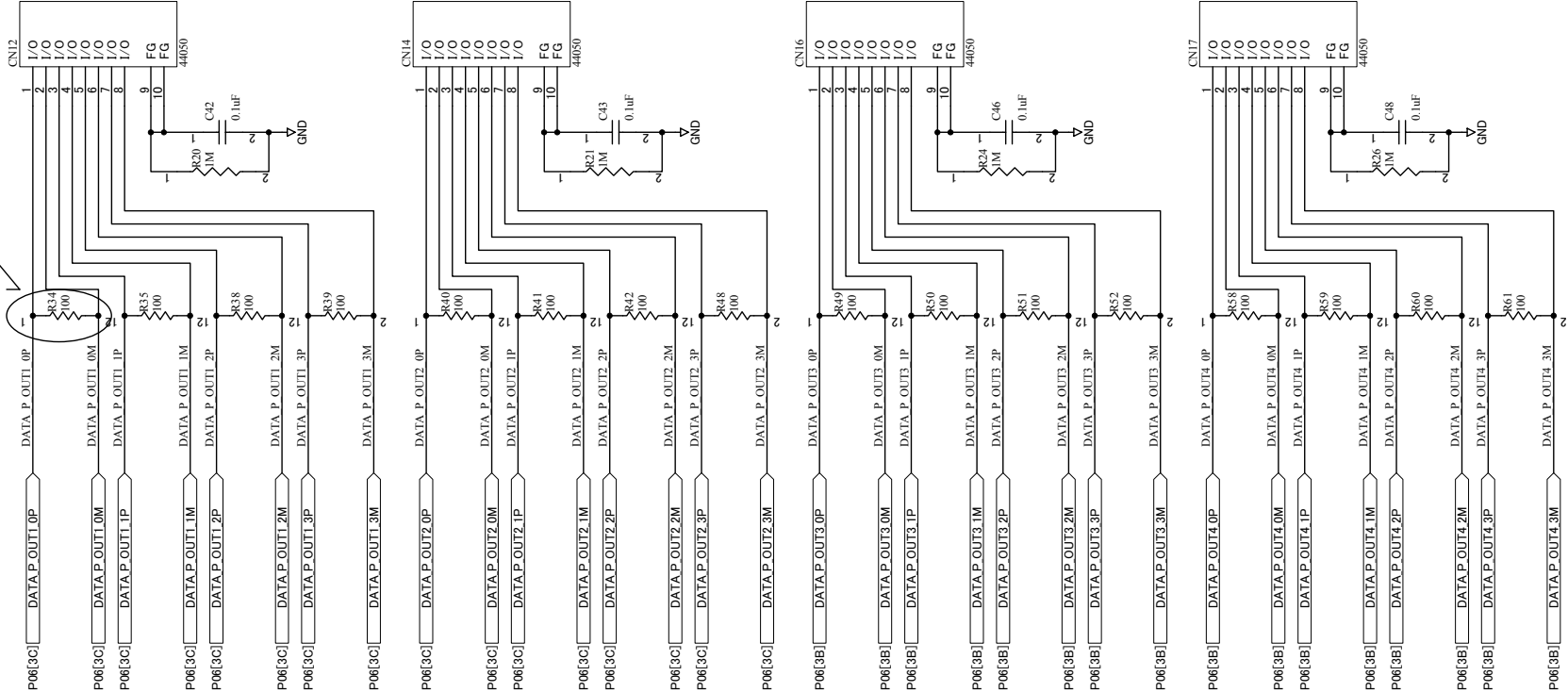


RJ45 Connector (Parallel-Input)



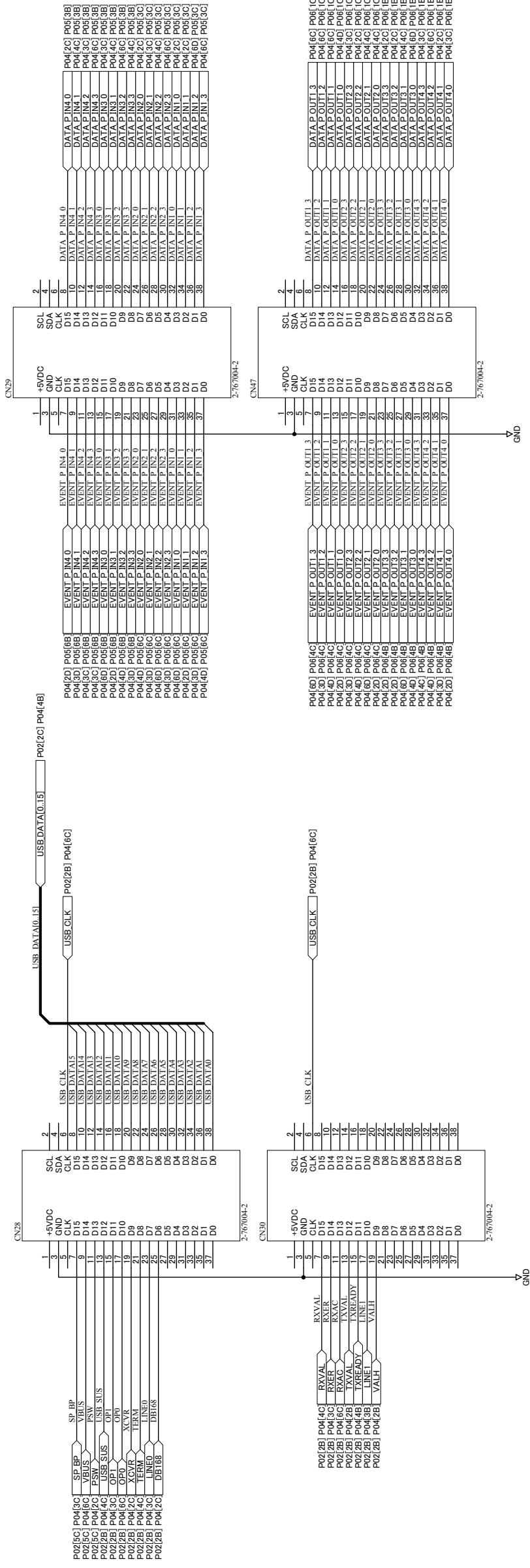
RJ45 Connector (Parallel-Output)

終端はコネクタのピンの近くに配線してください

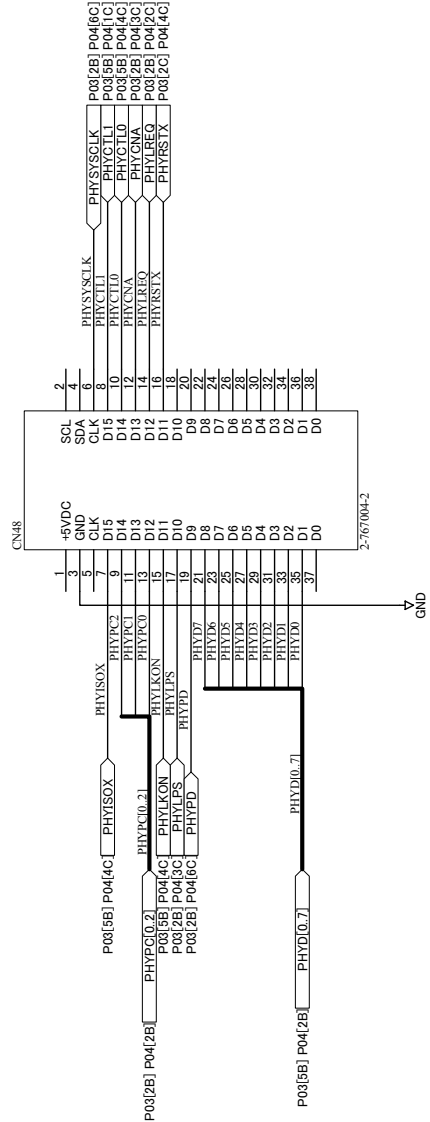


USB

LINK



1394



PCI bus Edge

