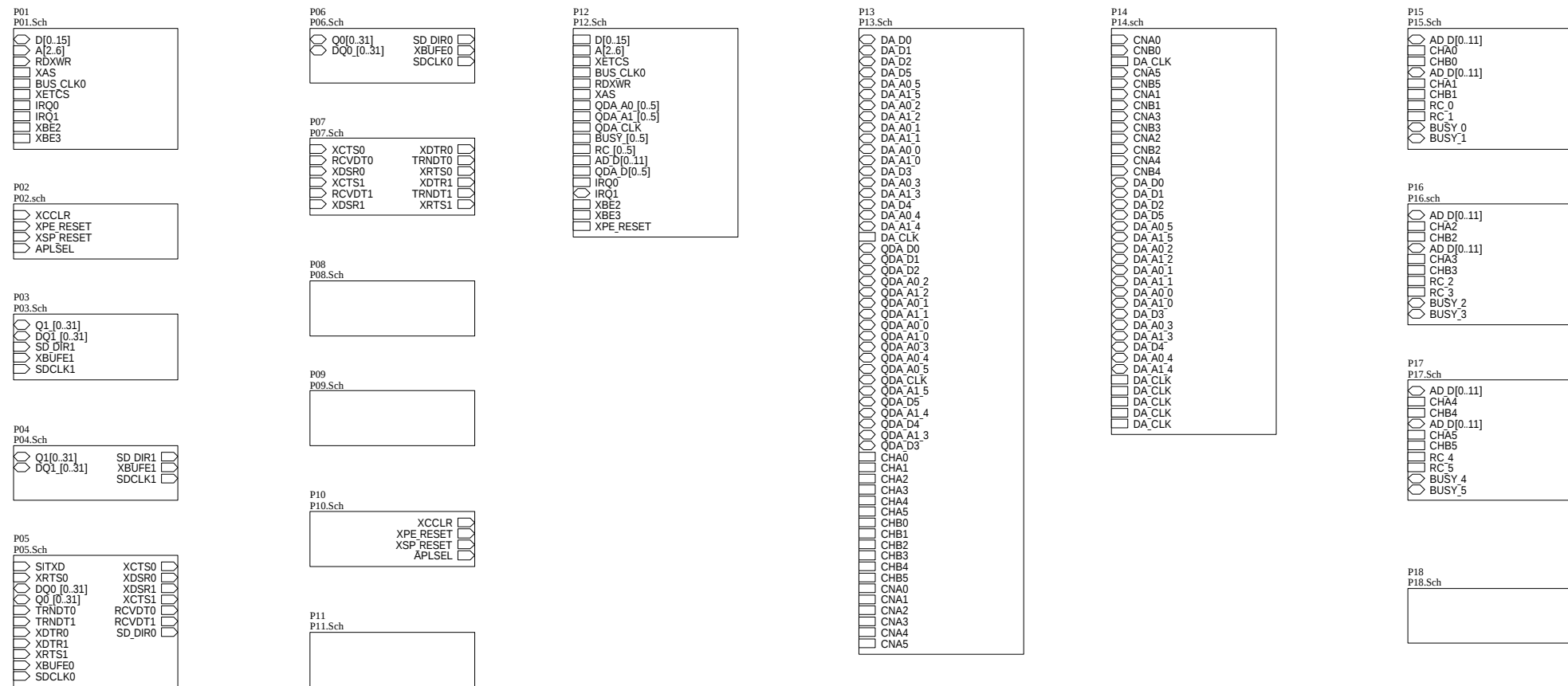
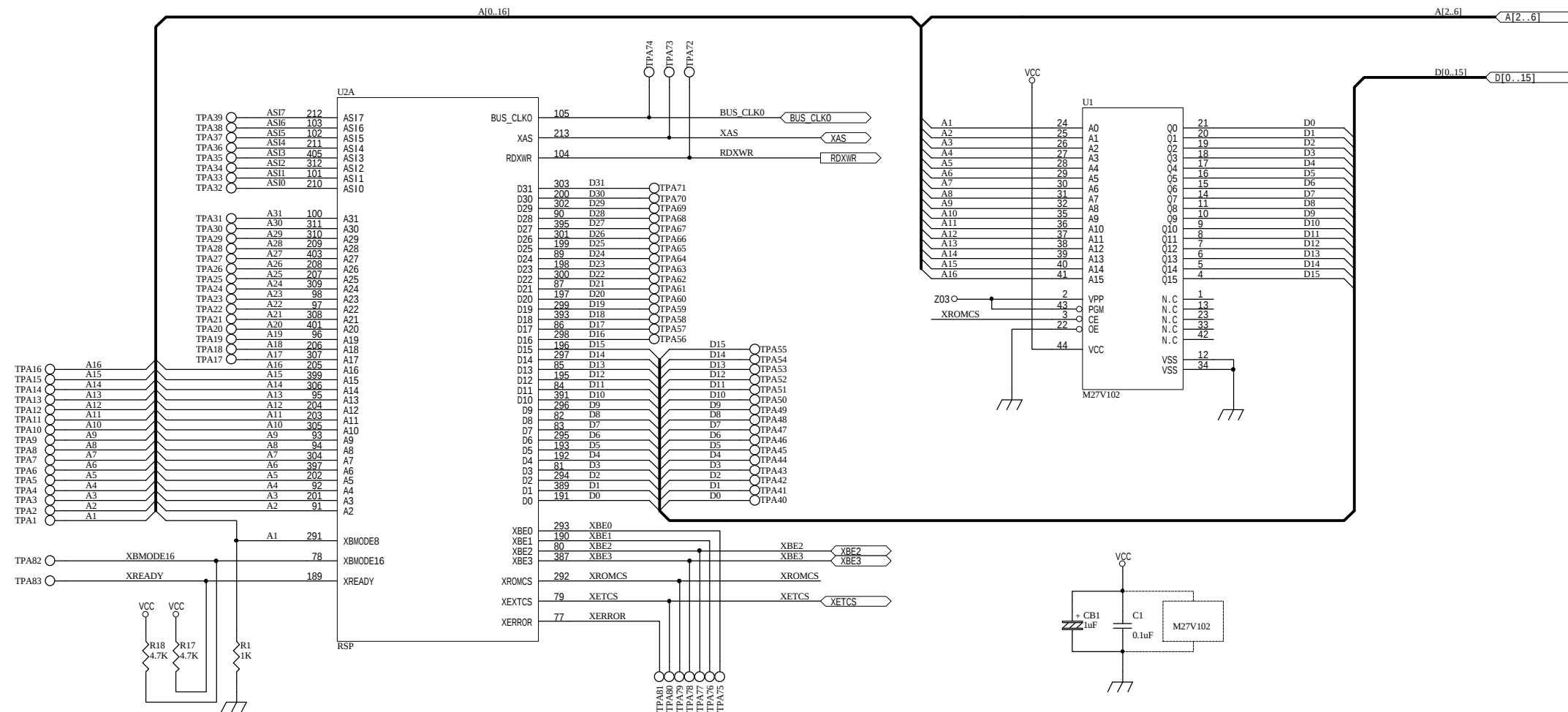


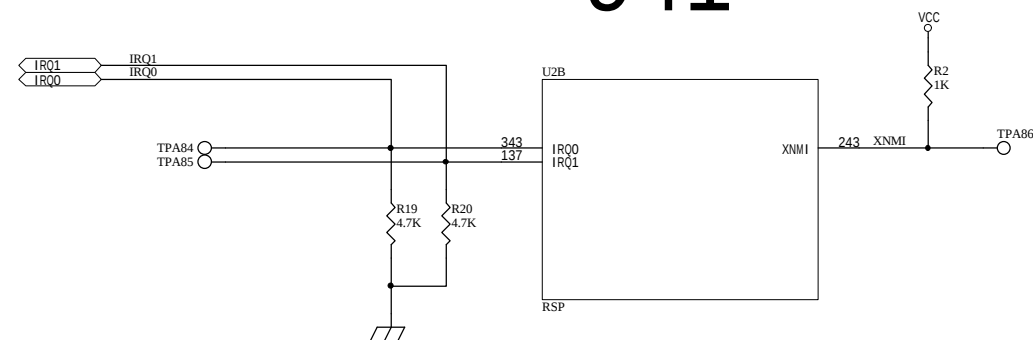
Responsive Processor Ver.K0



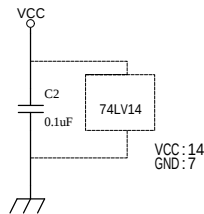
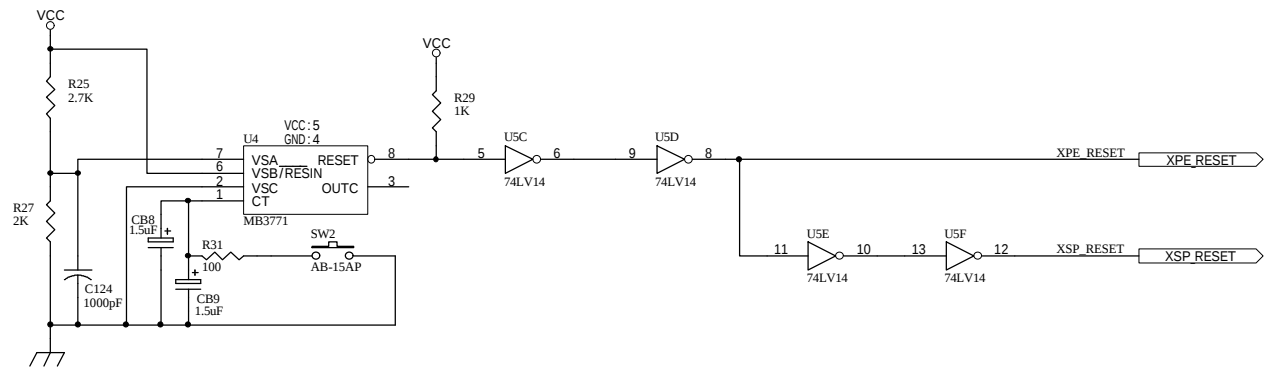
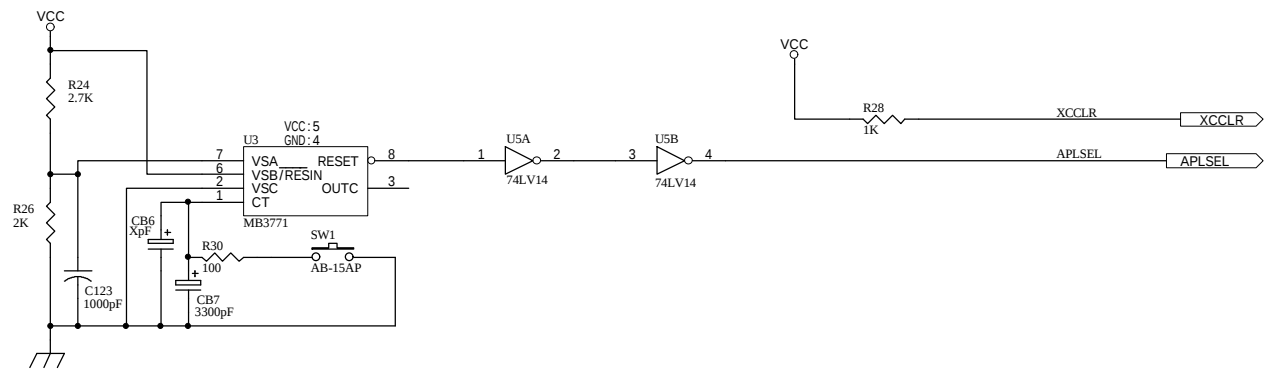
CPU_CORE



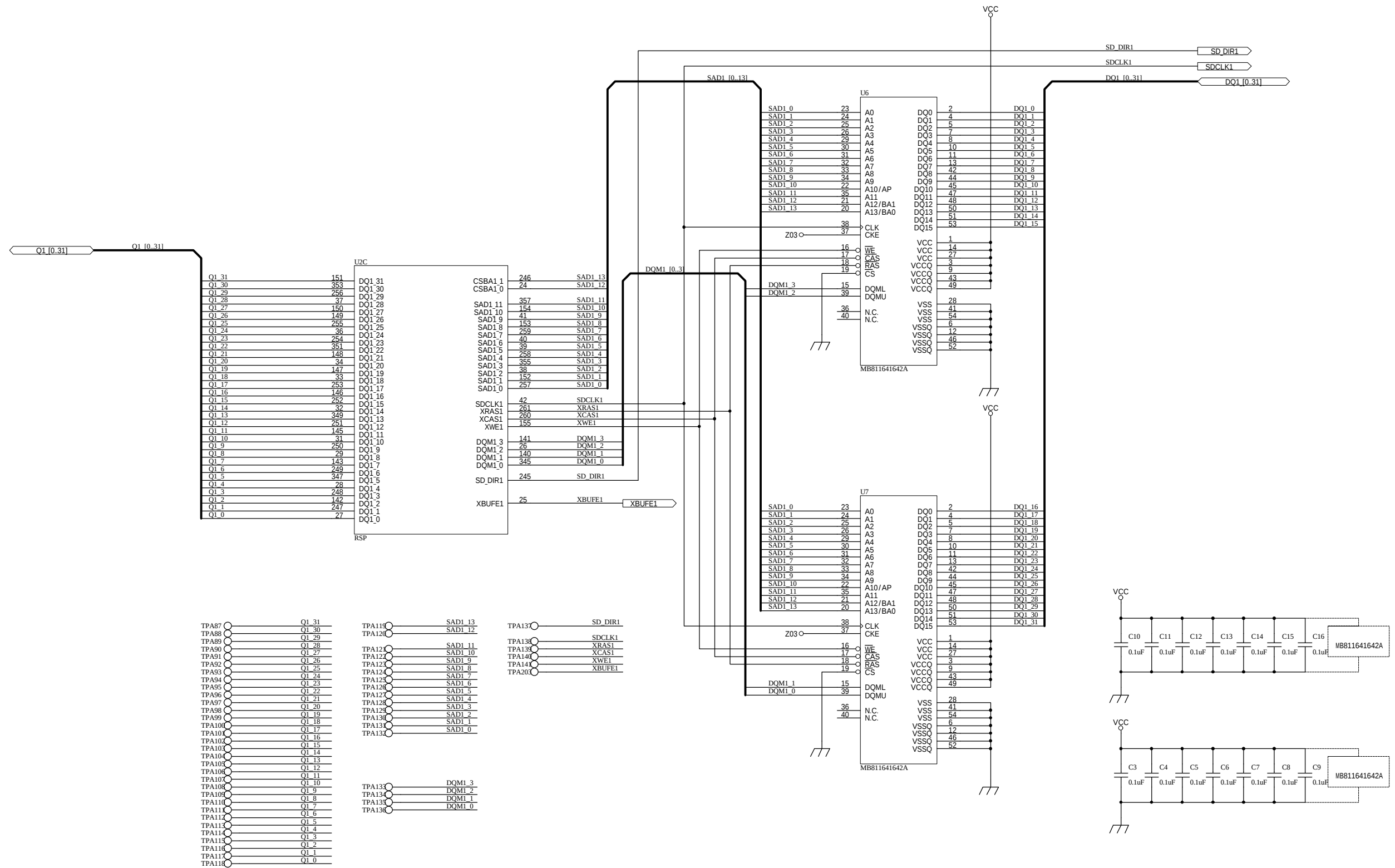
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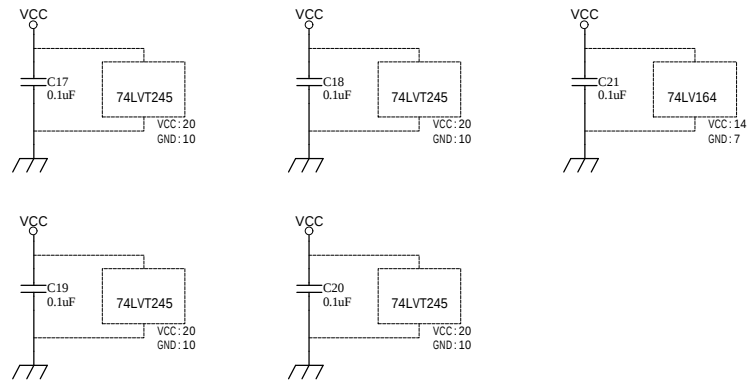
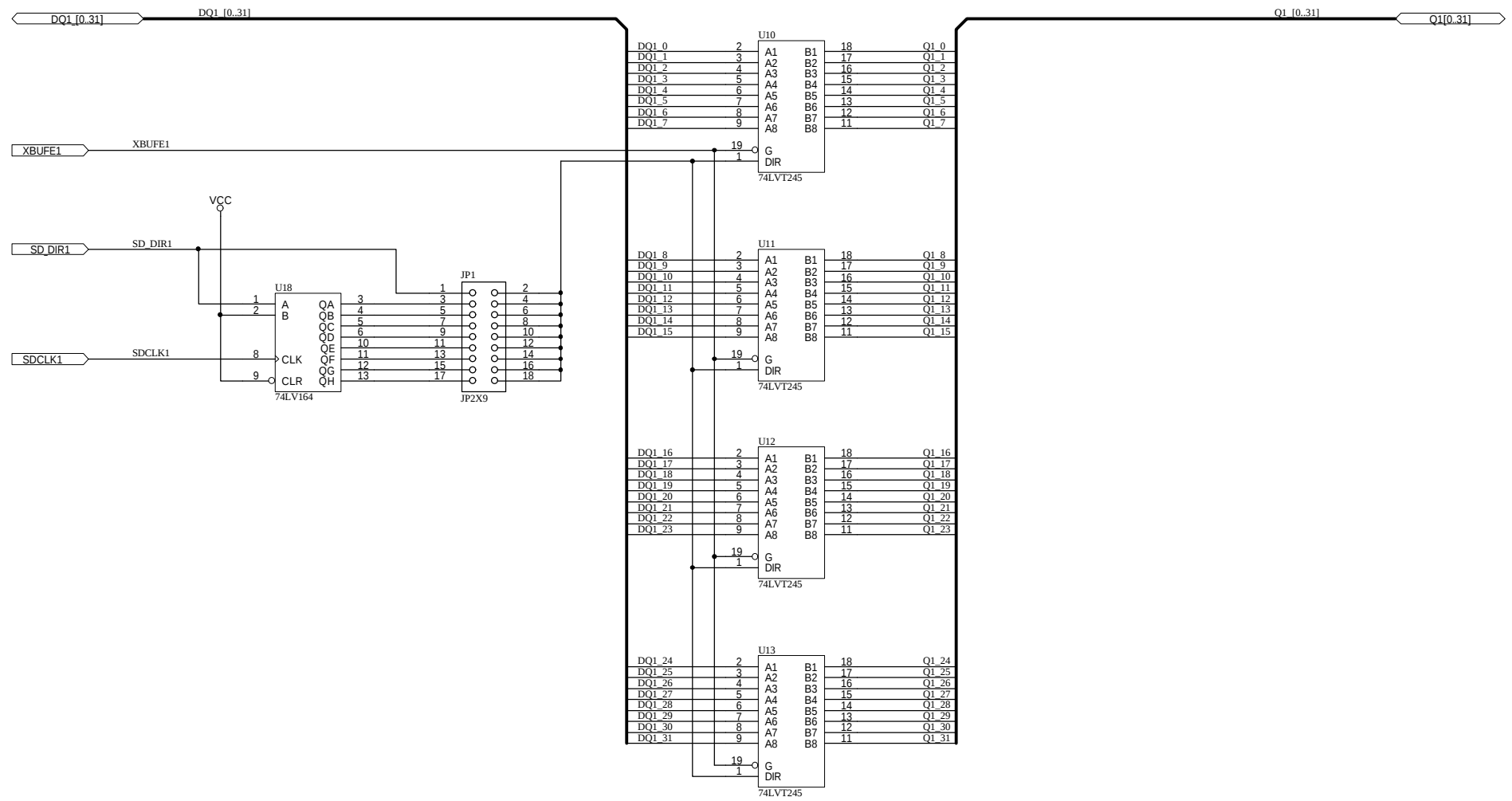
RESET



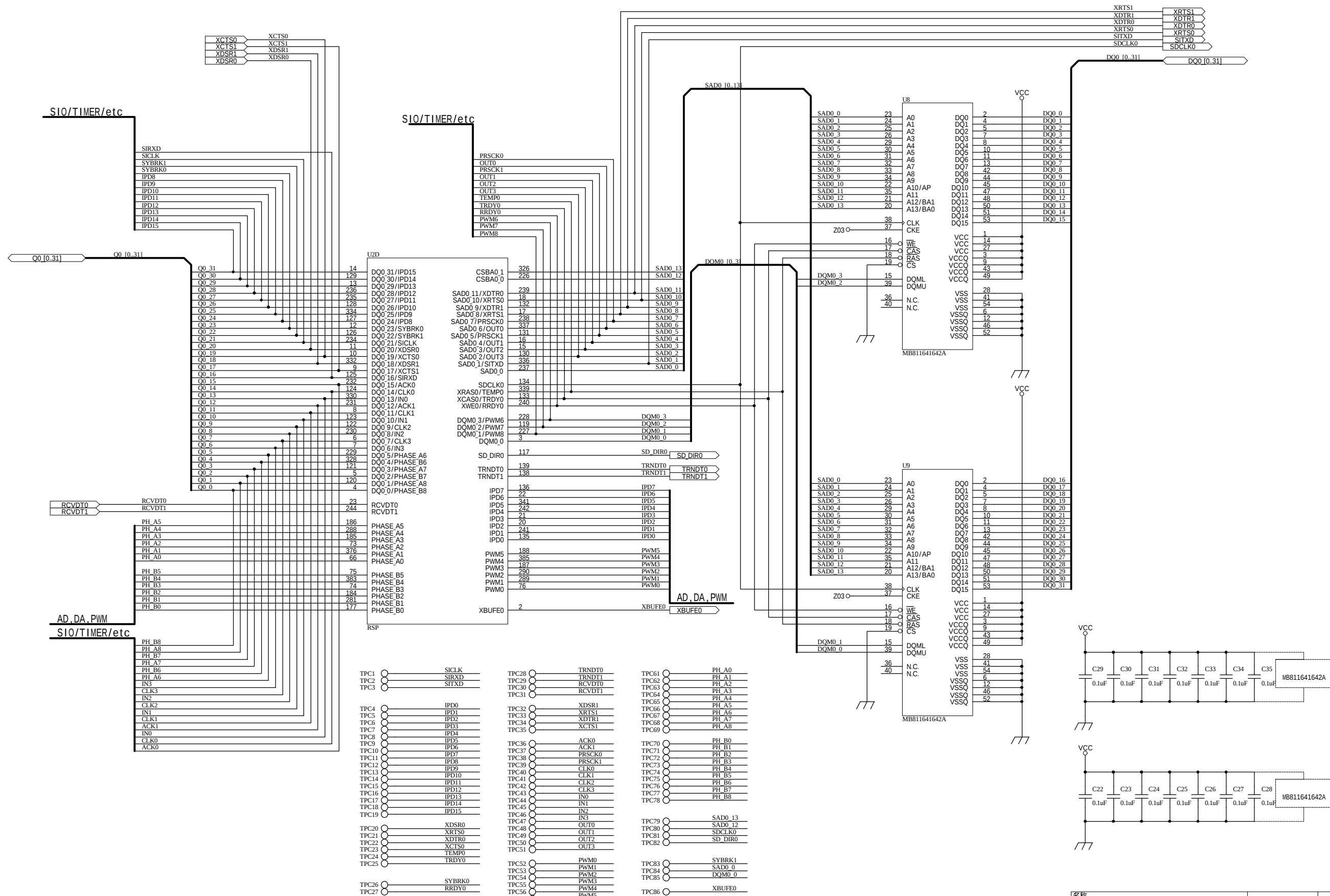
SDRAM1



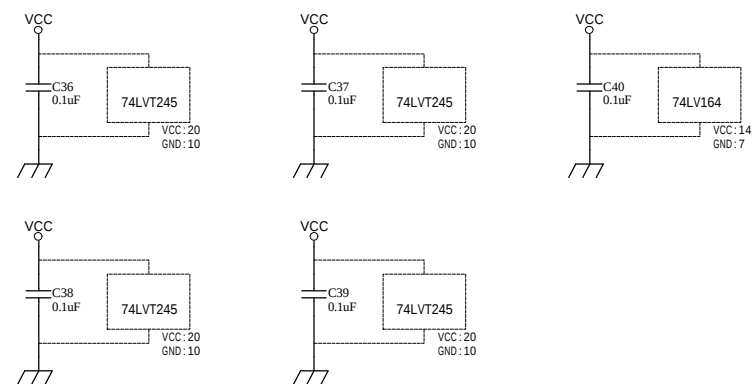
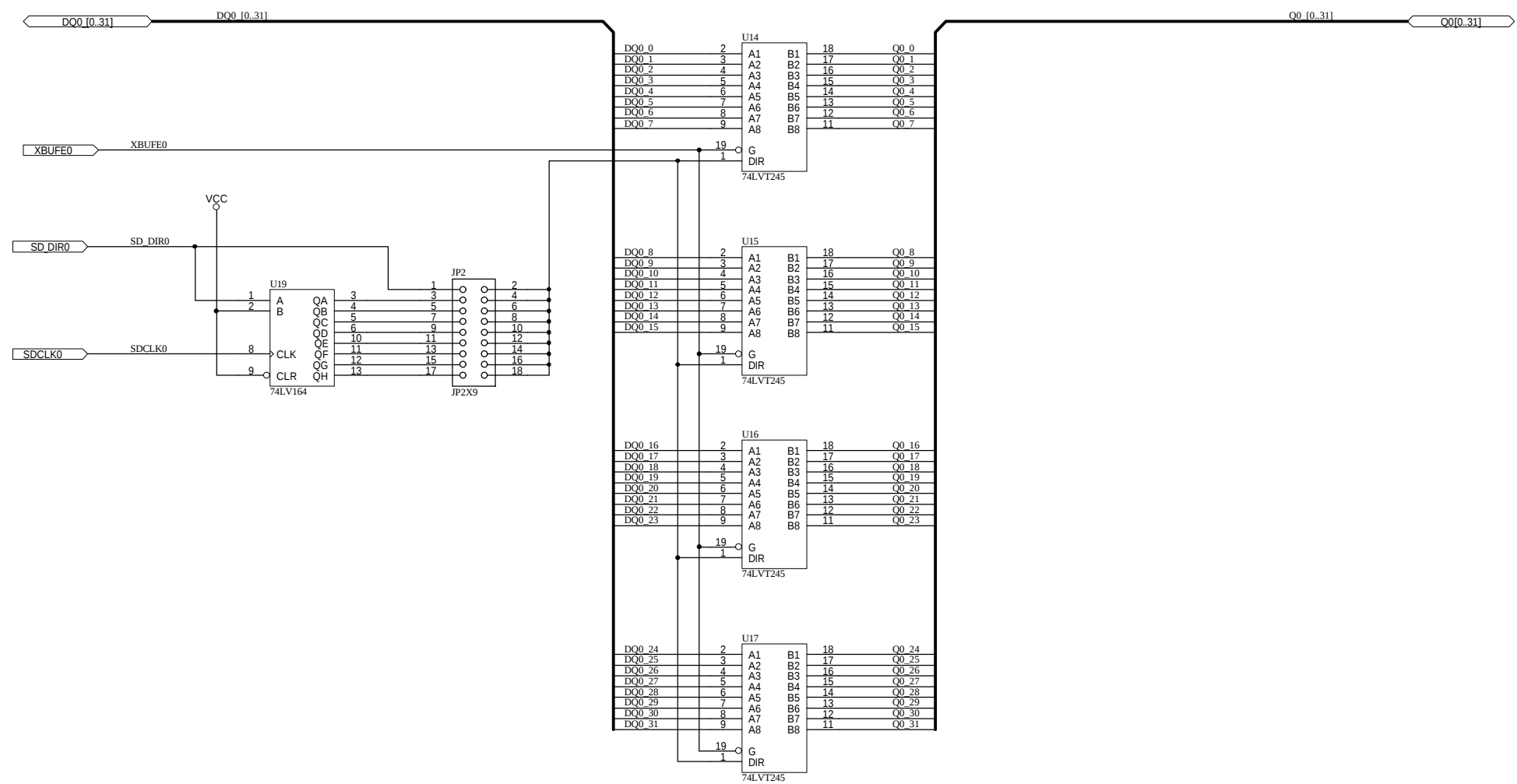
SDRAM1



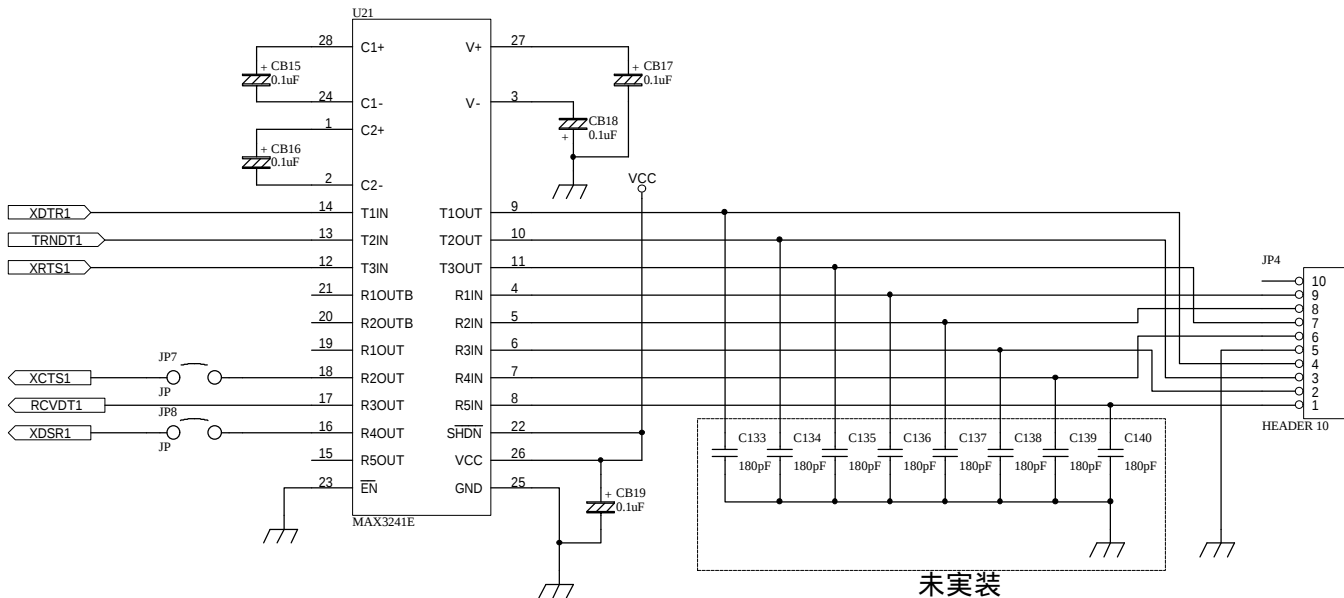
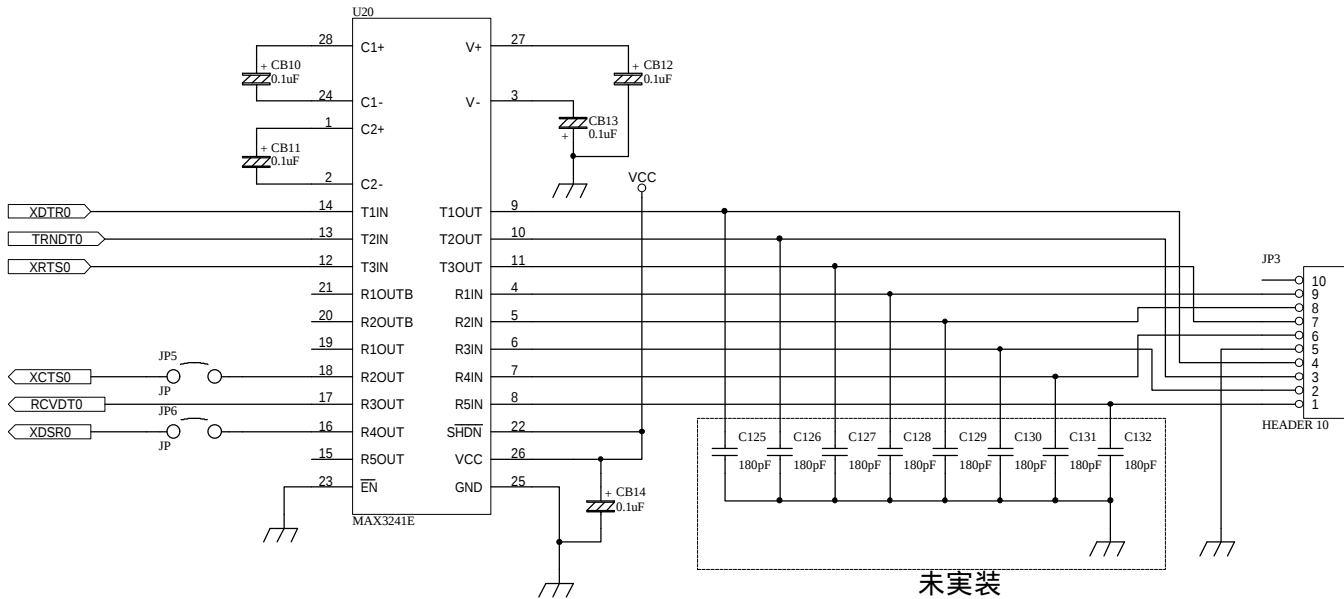
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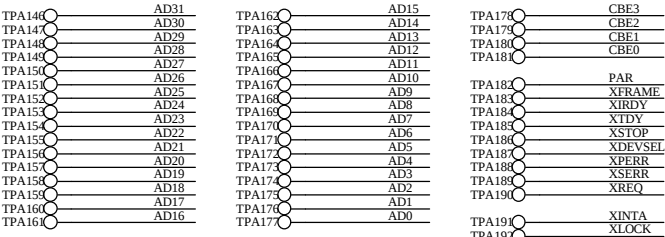
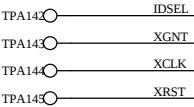
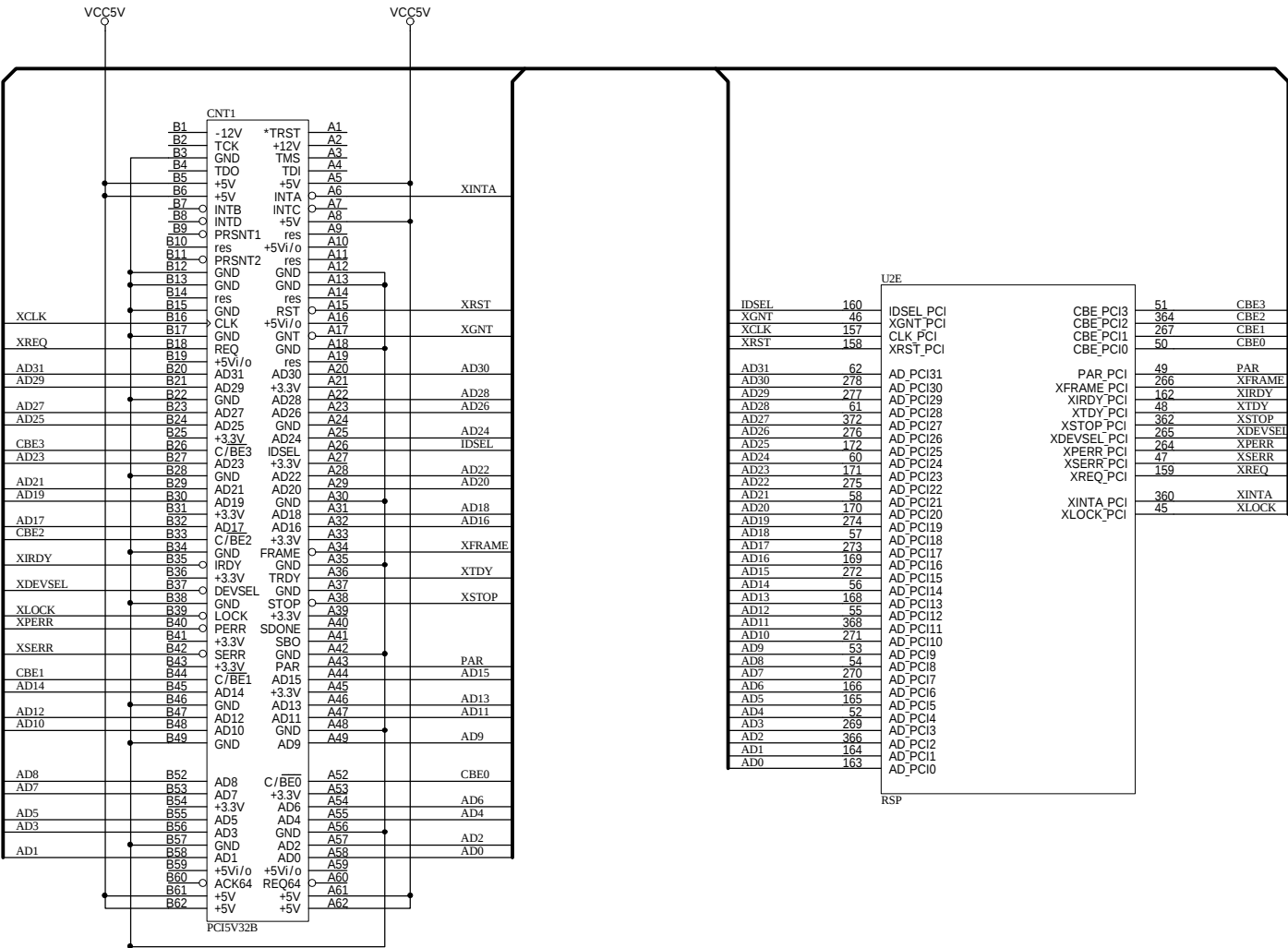
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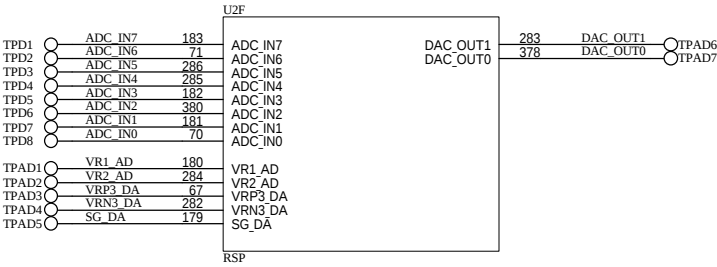
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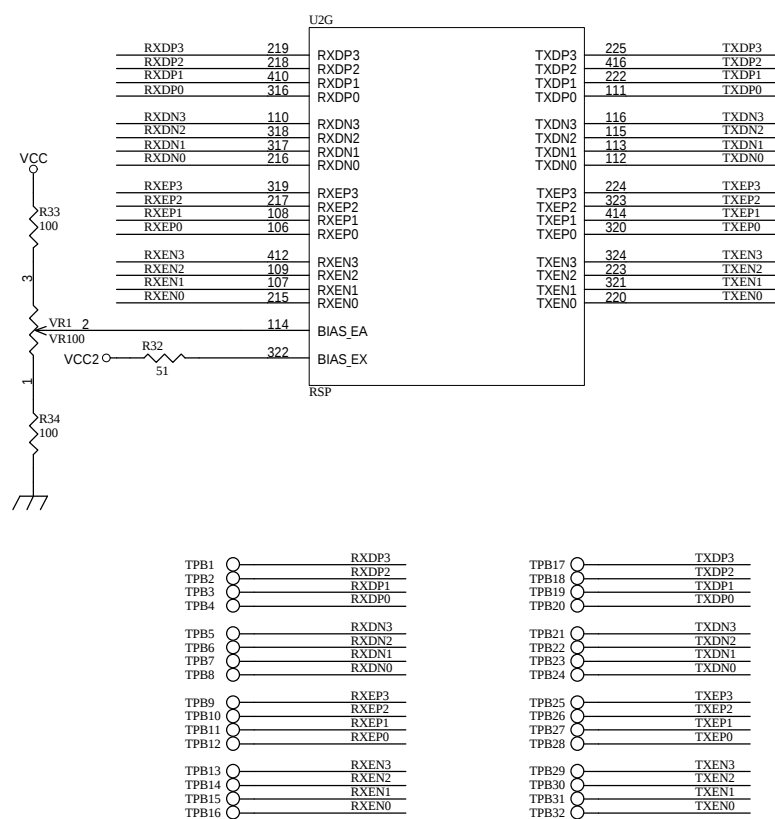
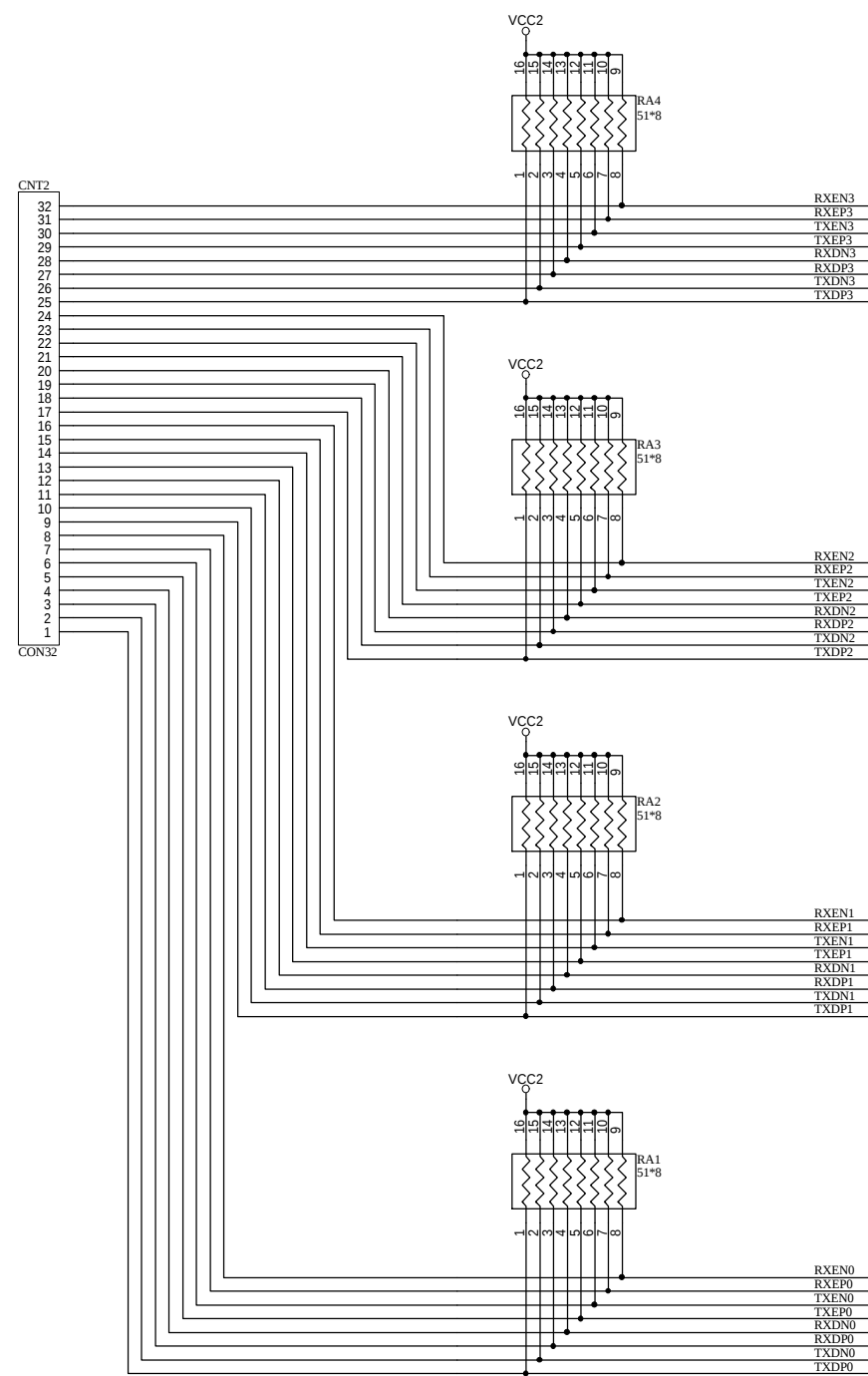
PCI



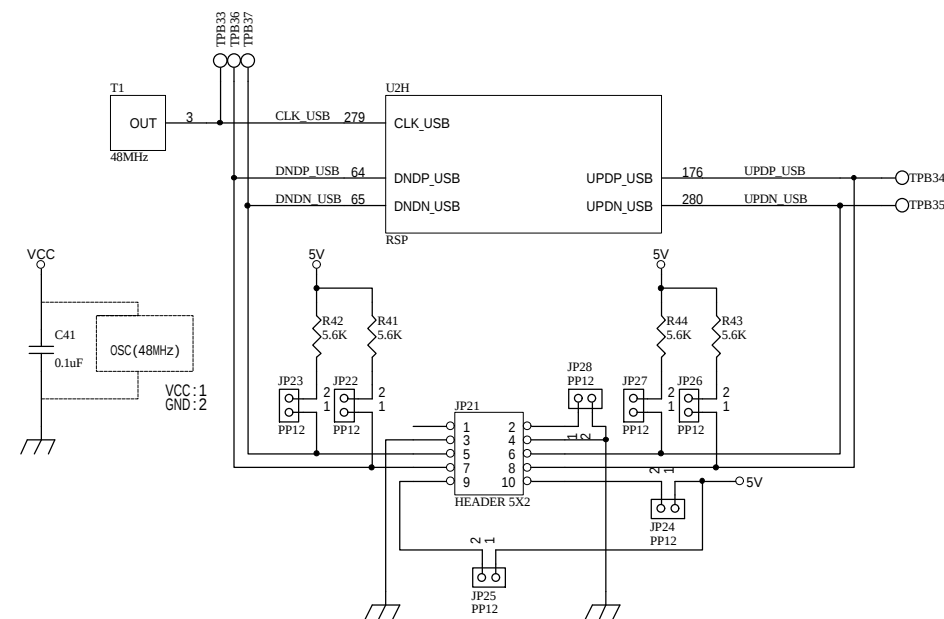
AD_DA_PWM



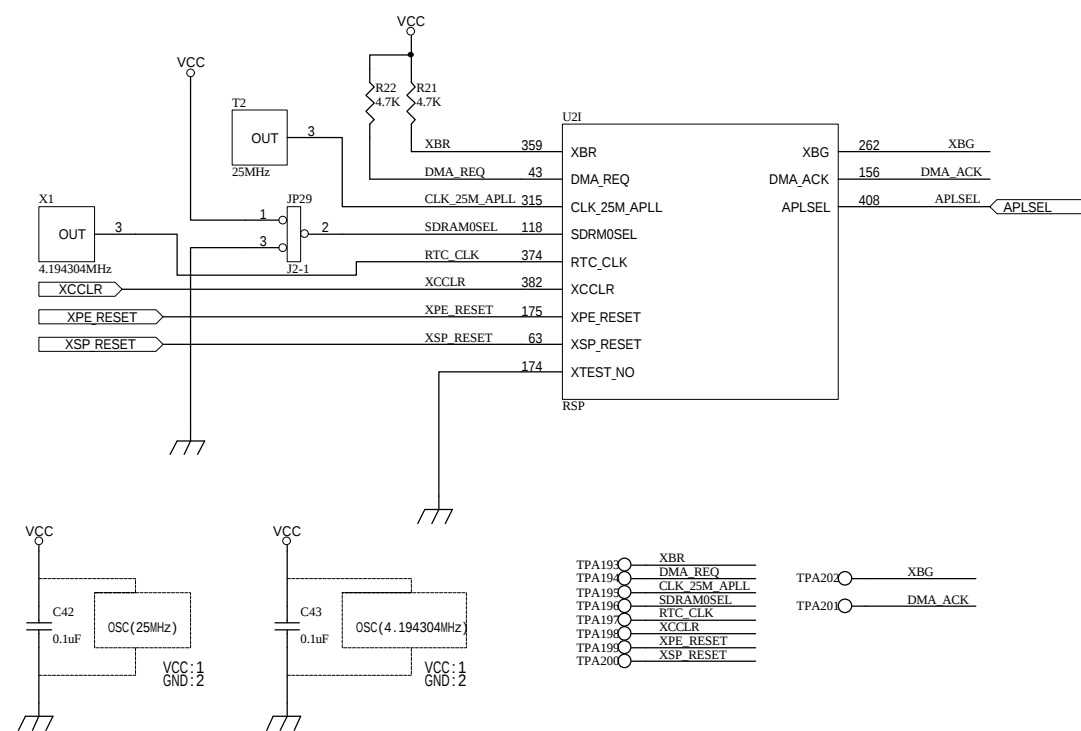
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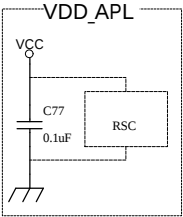
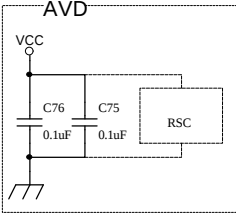
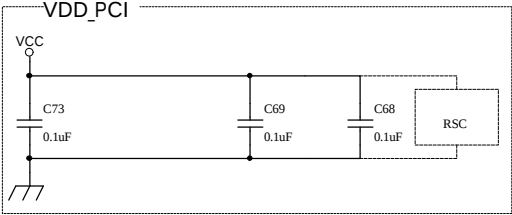
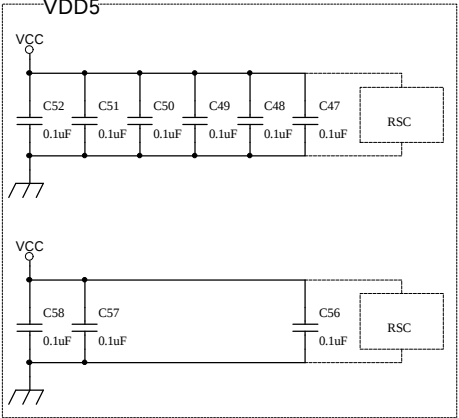
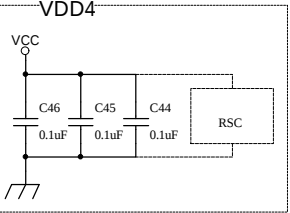
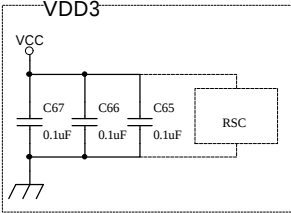
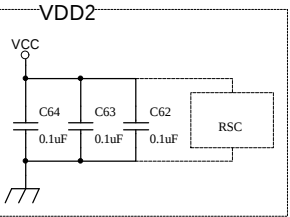
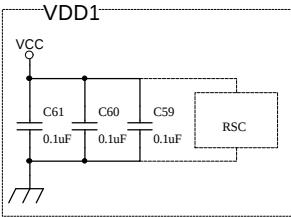
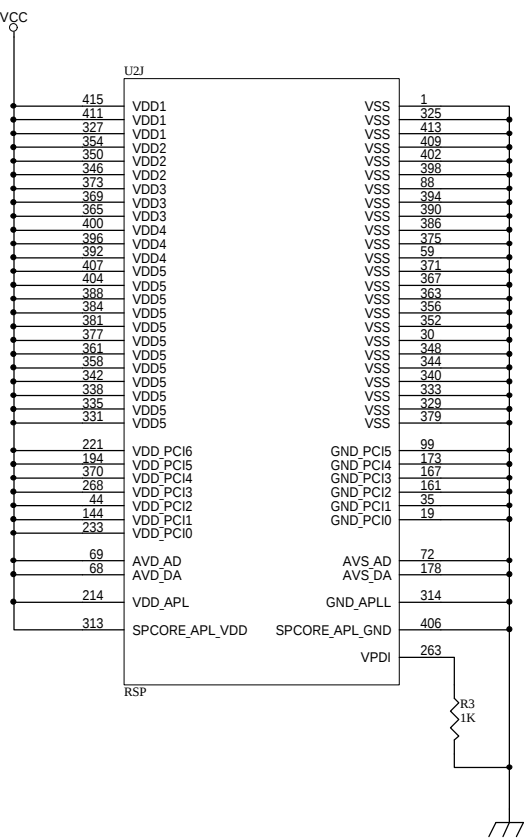
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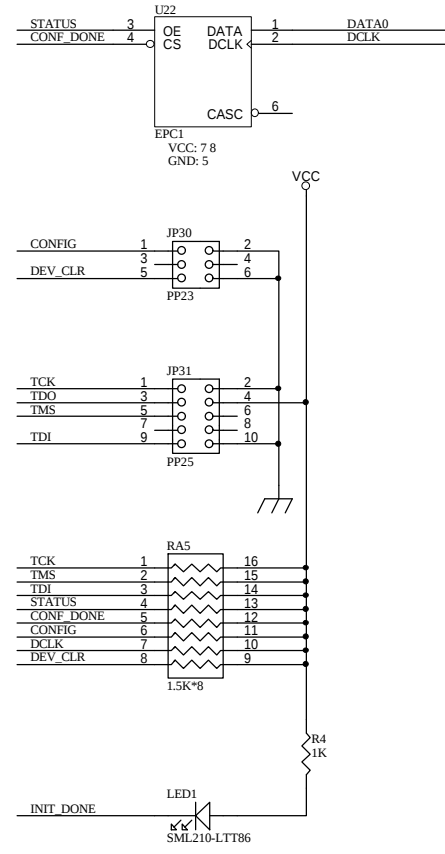
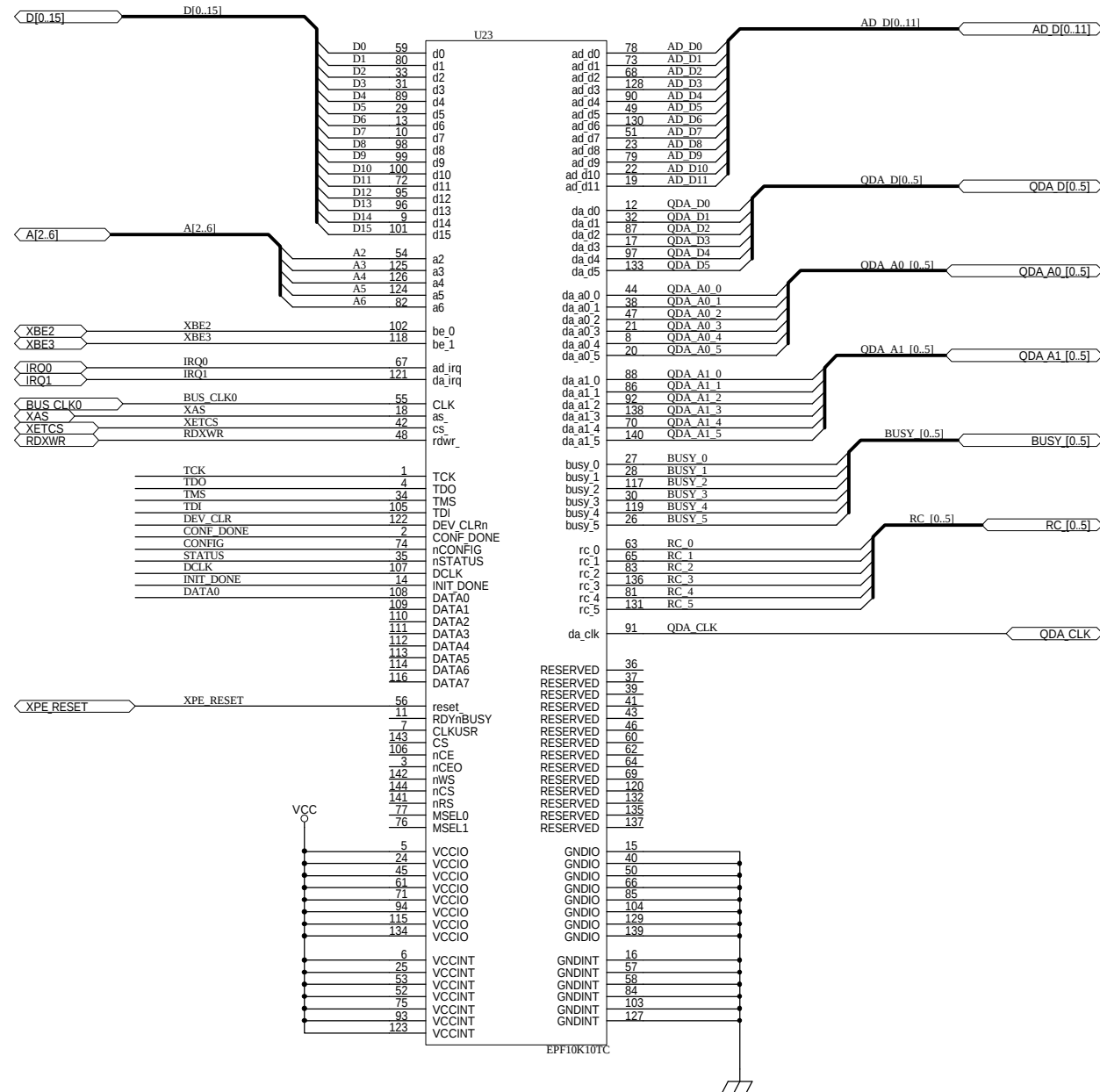
TESTetc...



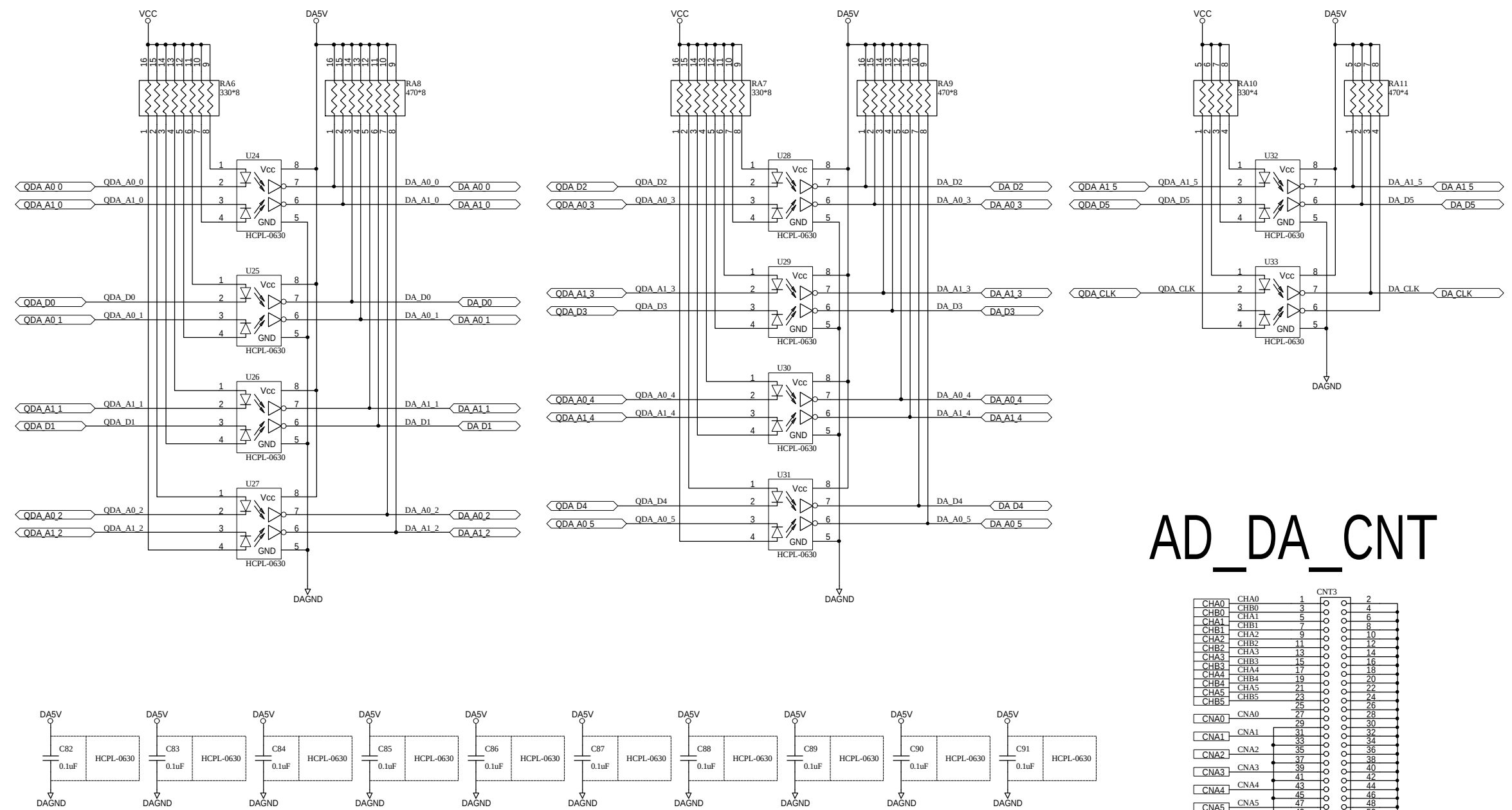
POWER



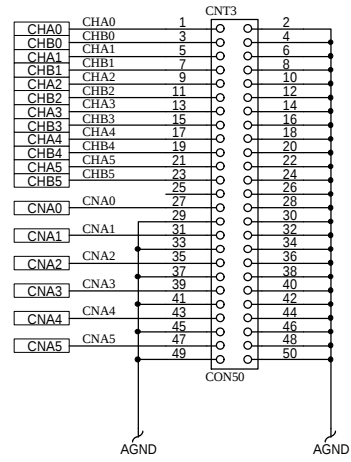
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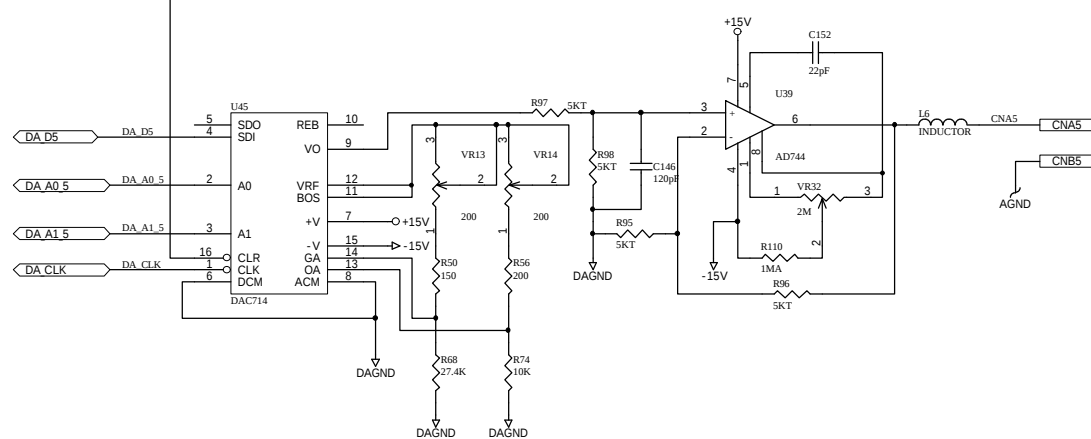
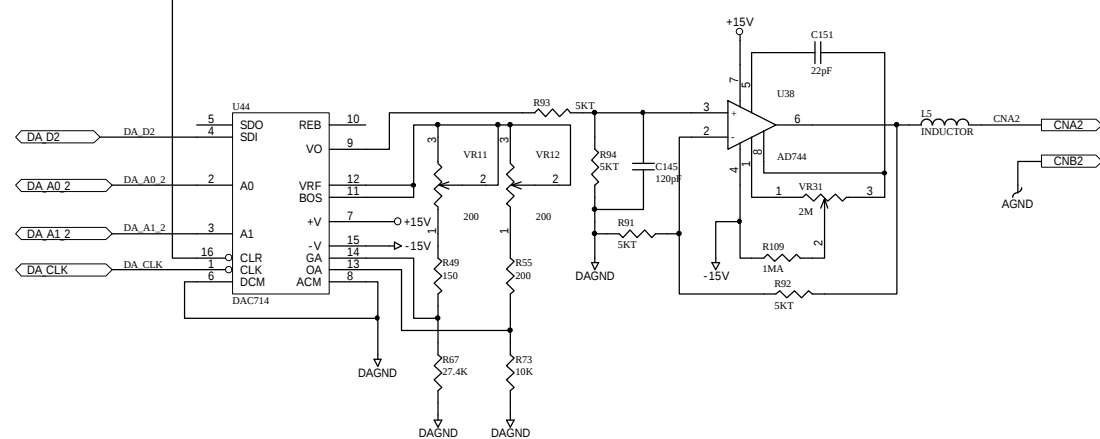
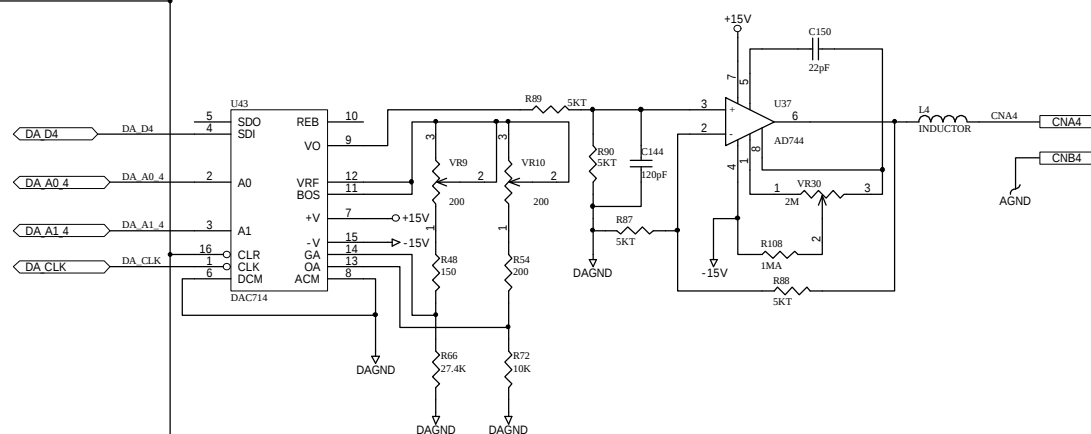
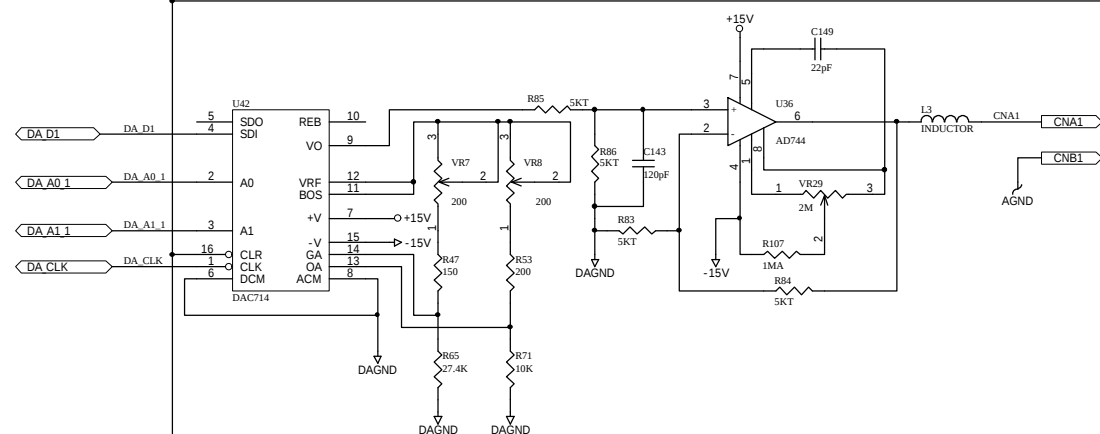
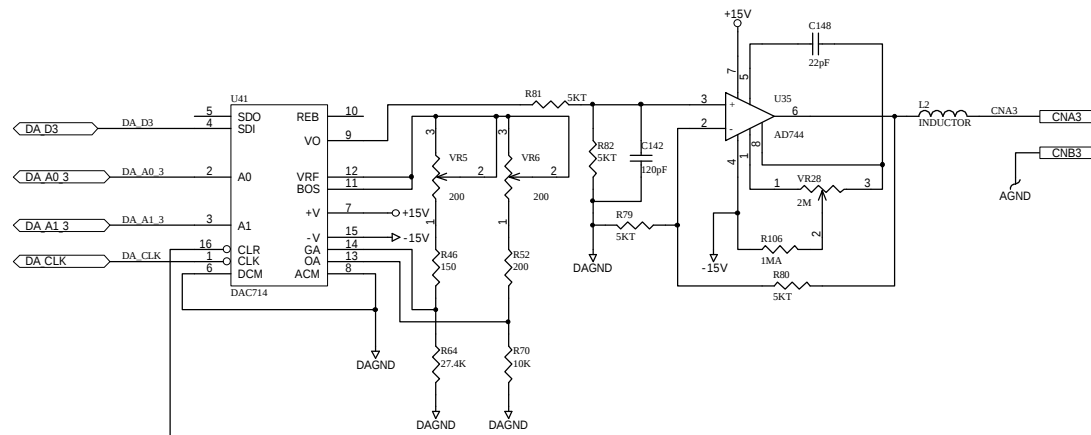
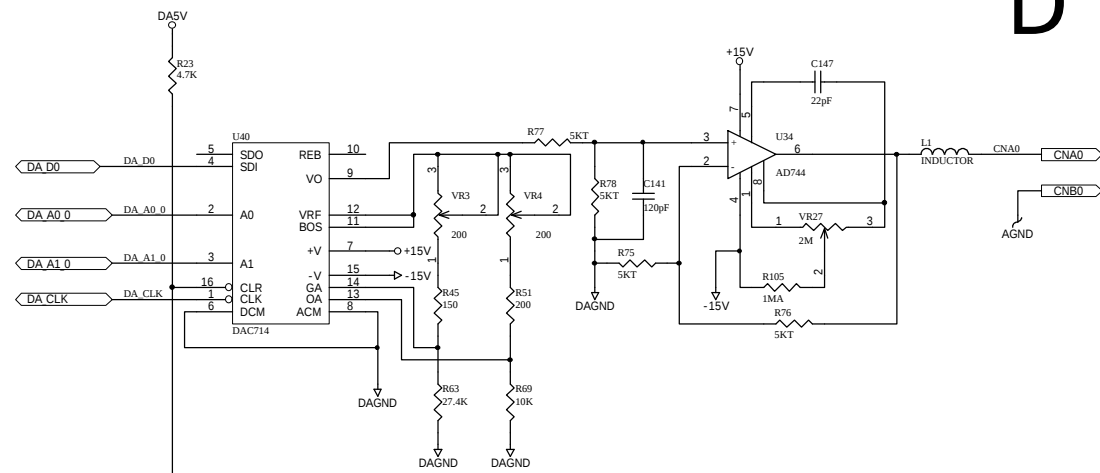
D/A フォトカプラ



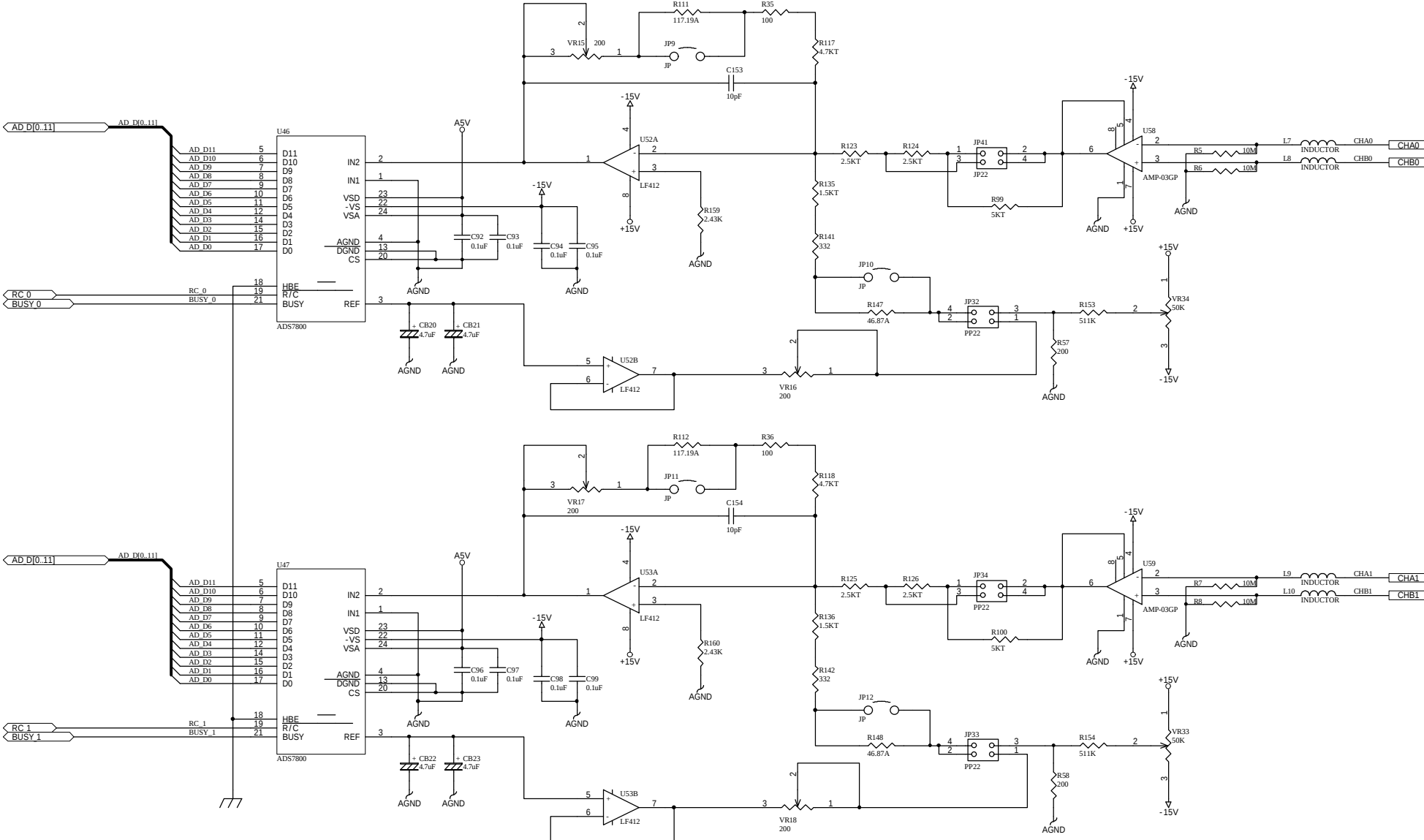
AD_DA_CNT



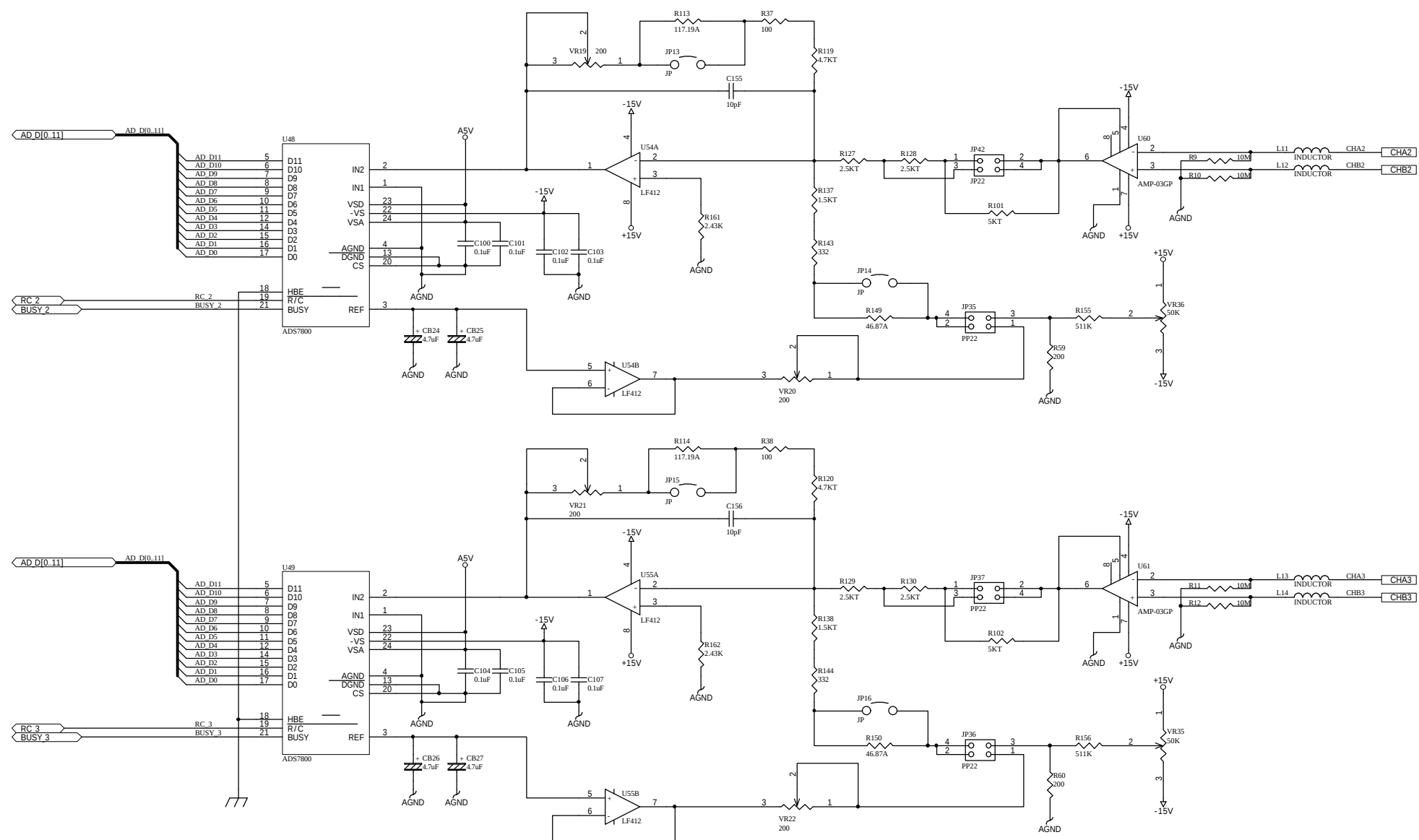
D / A



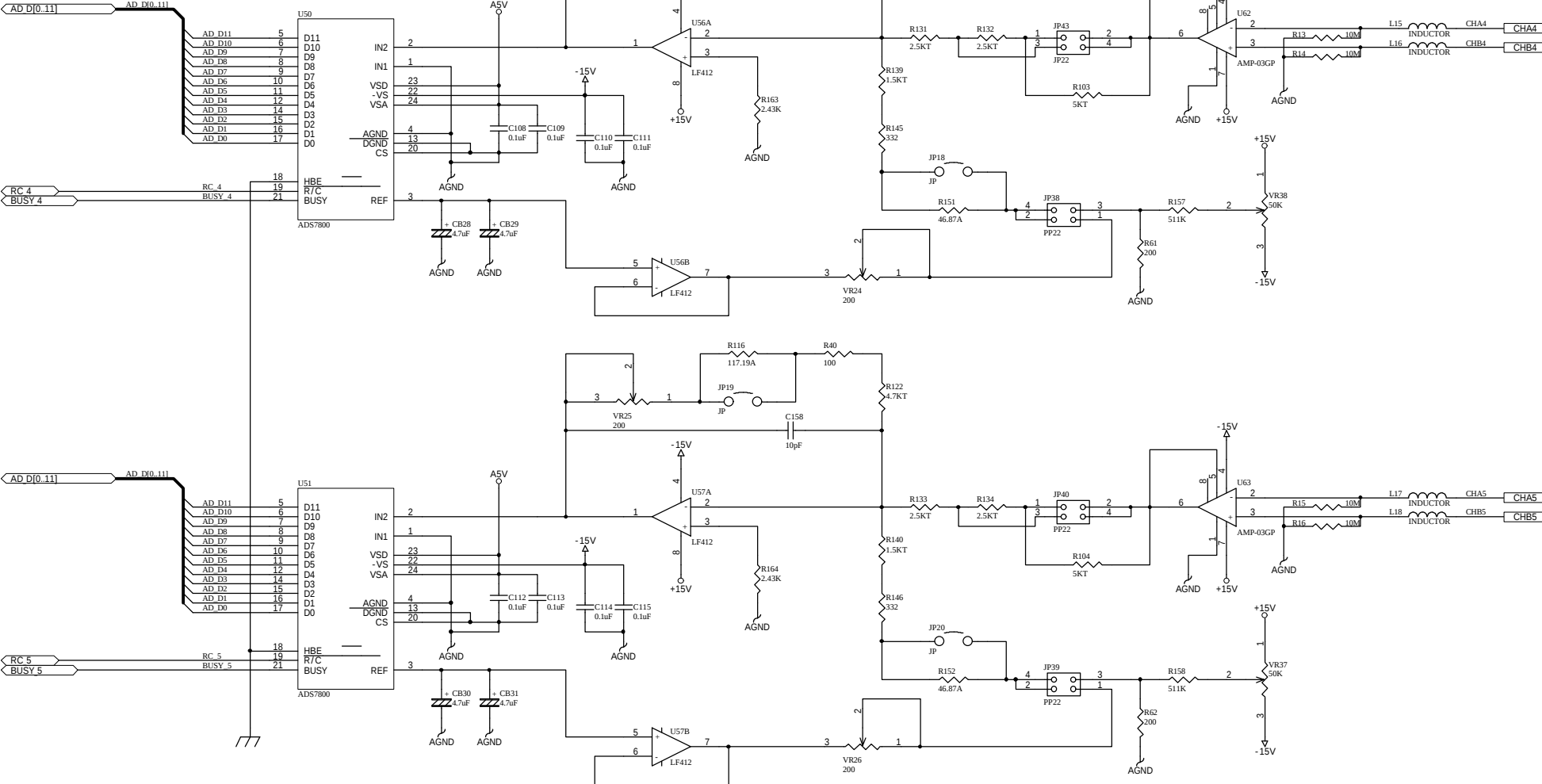
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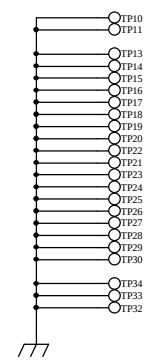
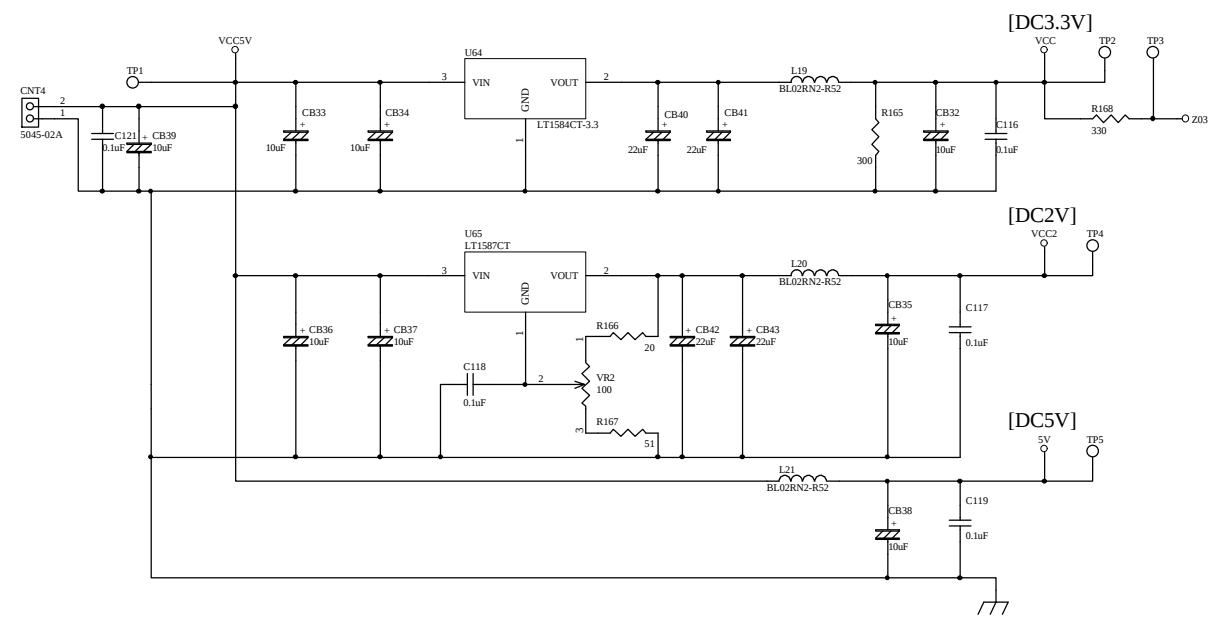
A / D 2



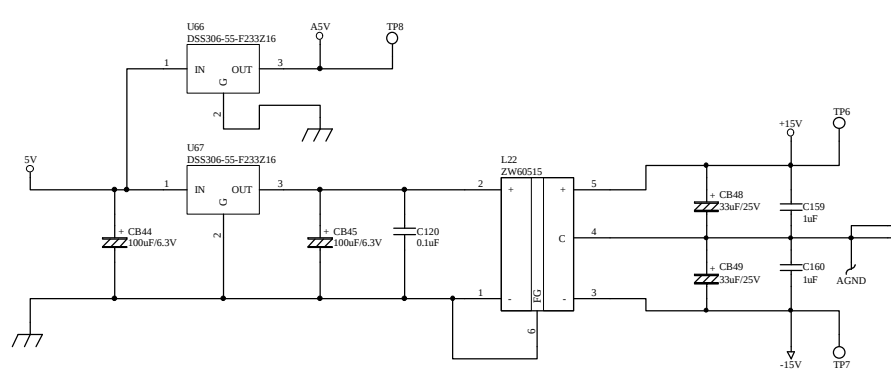
A / D 3



POWER LOGIC



A/D POWER



D/A POWER

