

D

C

8

A

9

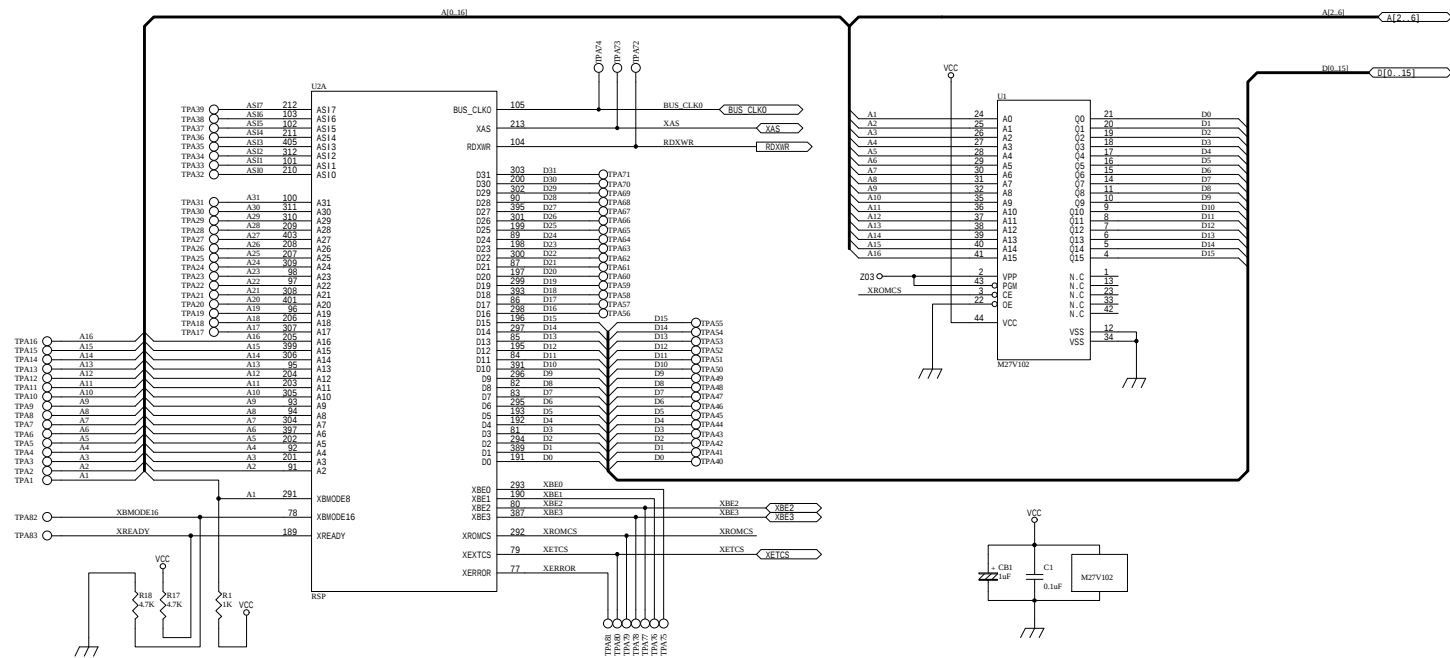
C

1

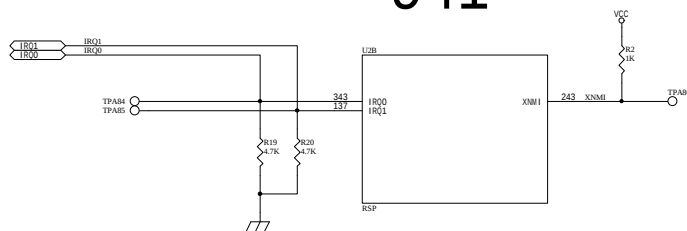
A



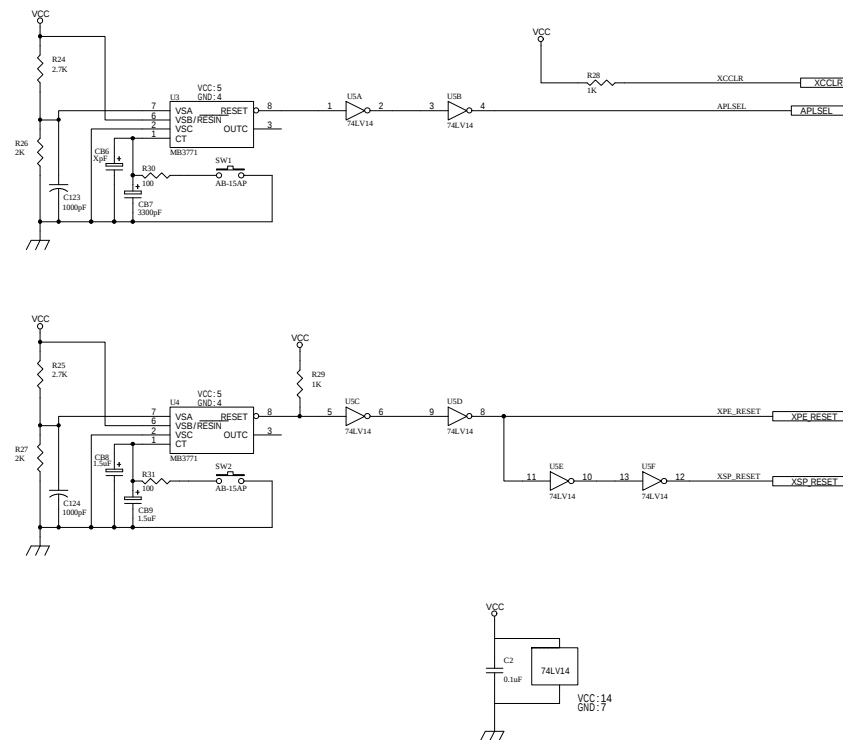
CPU_CORE



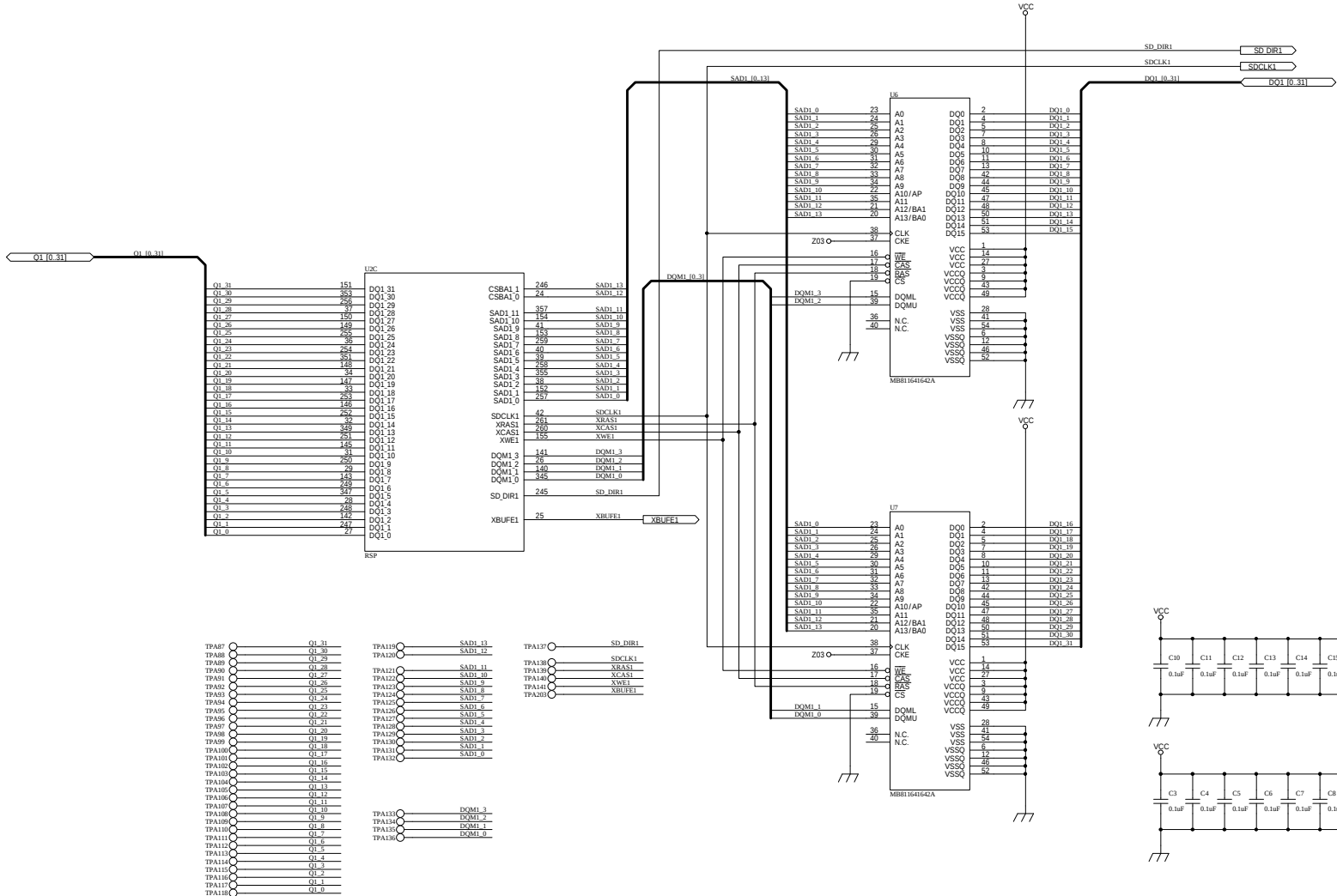
941



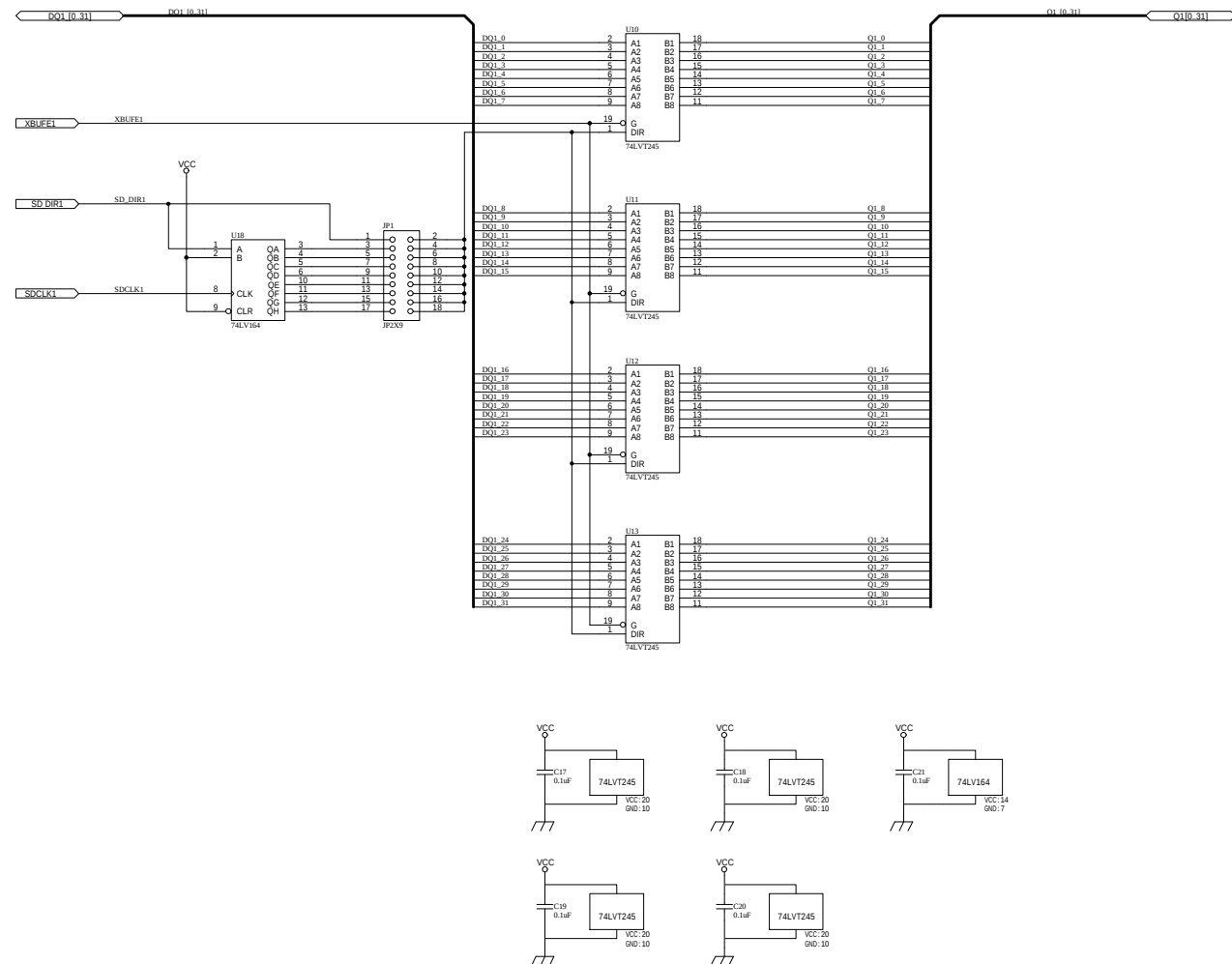
RESET



SDRAM1

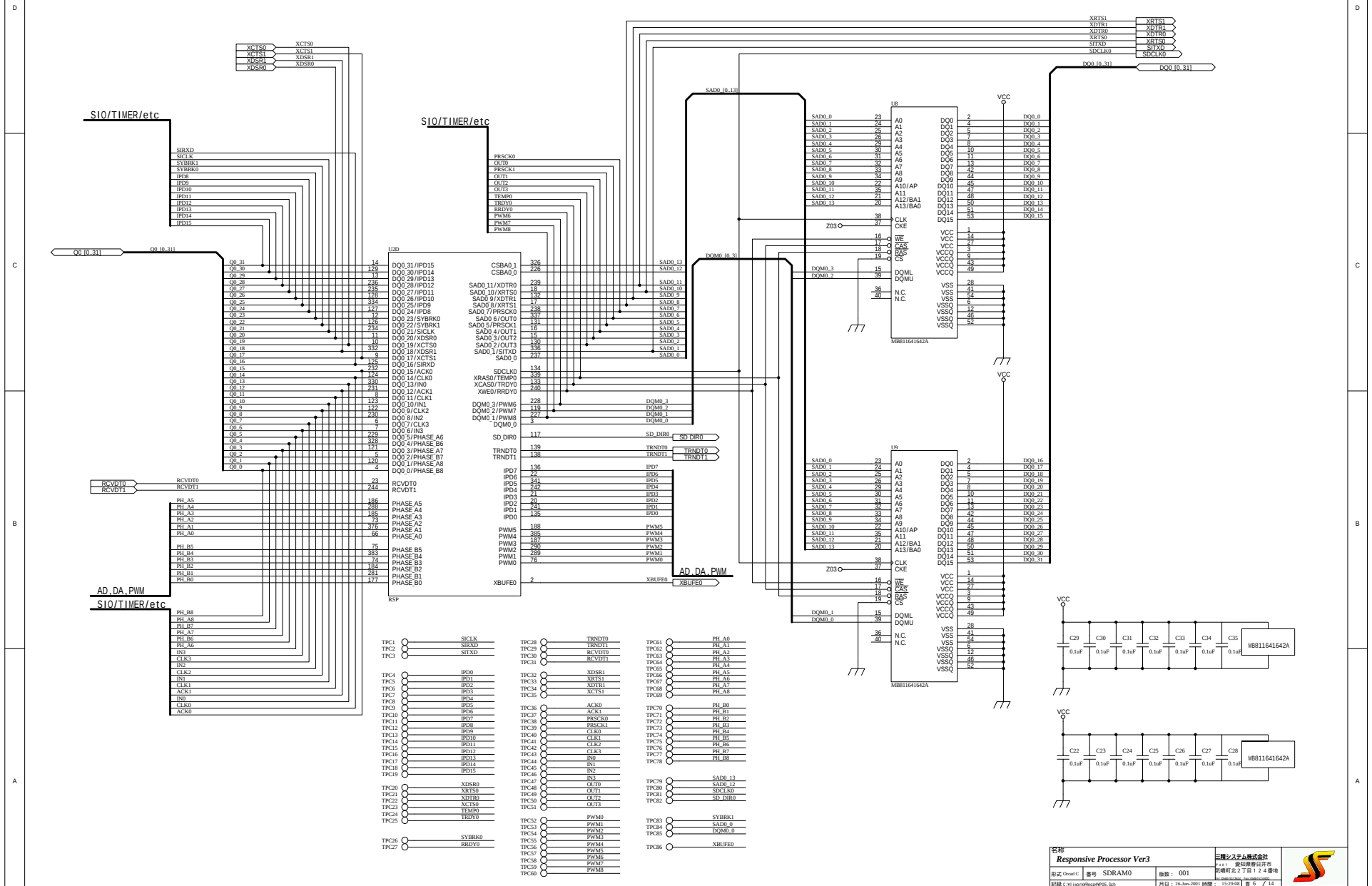


SDRAM1

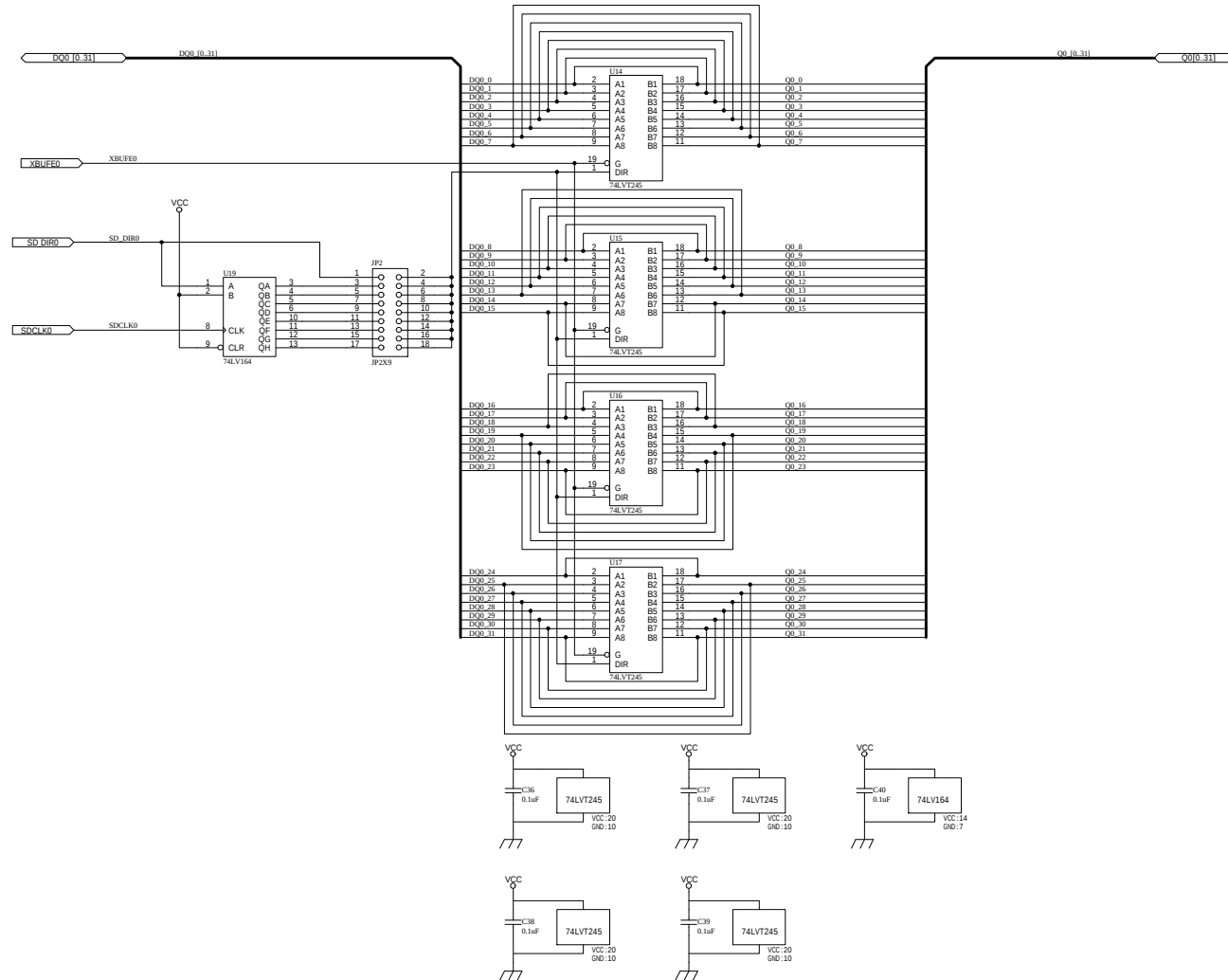


名称 Responsive Processor Ver3		三積システム株式会社 〒483 愛知県春日井市 筑崎町北2丁目12-4番地		
形式 Ocrad C	番号 SDRAM11	版数: 001		
記録 C:\rsrc\h\ocp\c004_Sch		月日: 26-Jan-2001 時間: 15:28:57	頁 5 / 14	

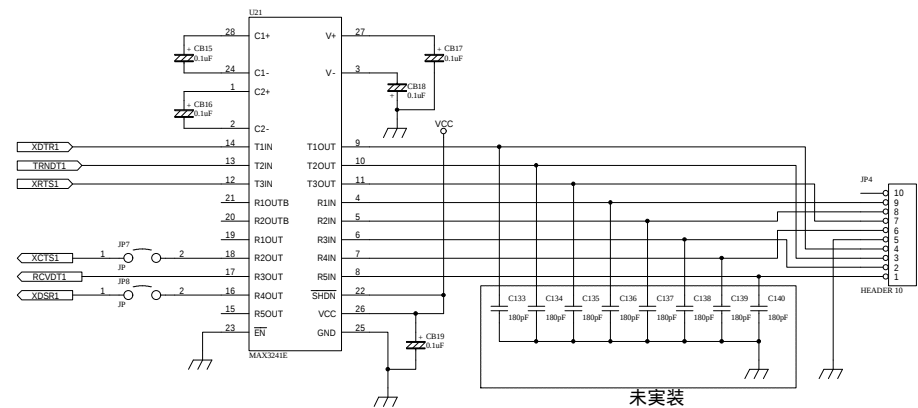
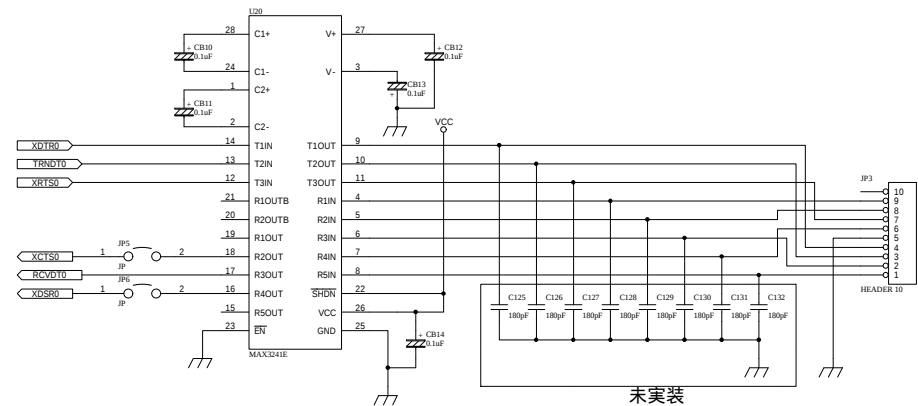
SDRAM0



SDRAM0

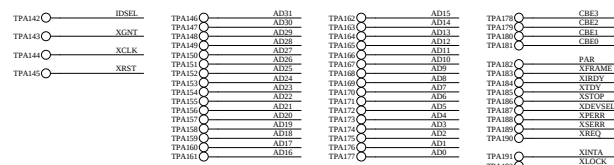
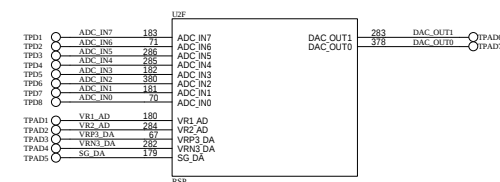
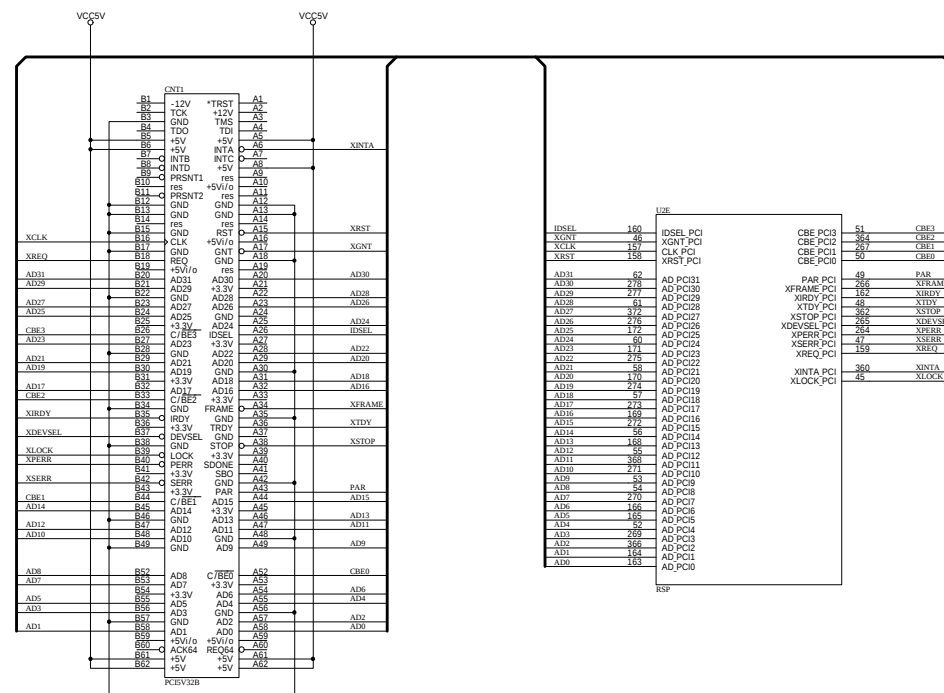


MAX3241E

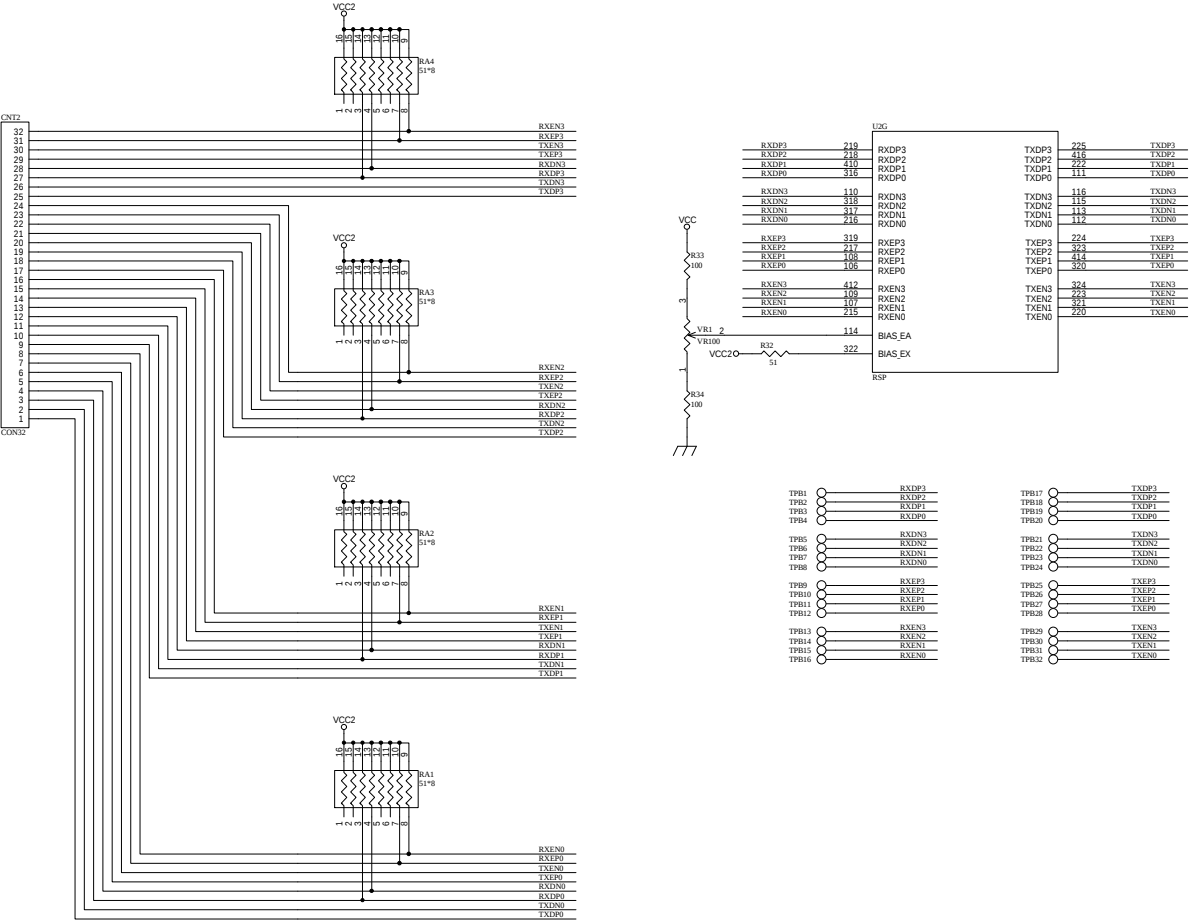


PCI

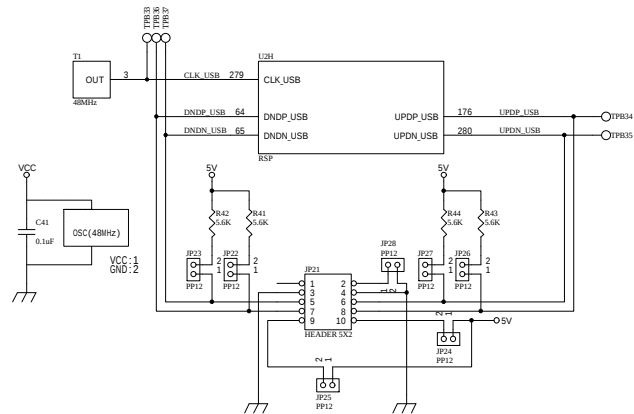
AD DA PWM



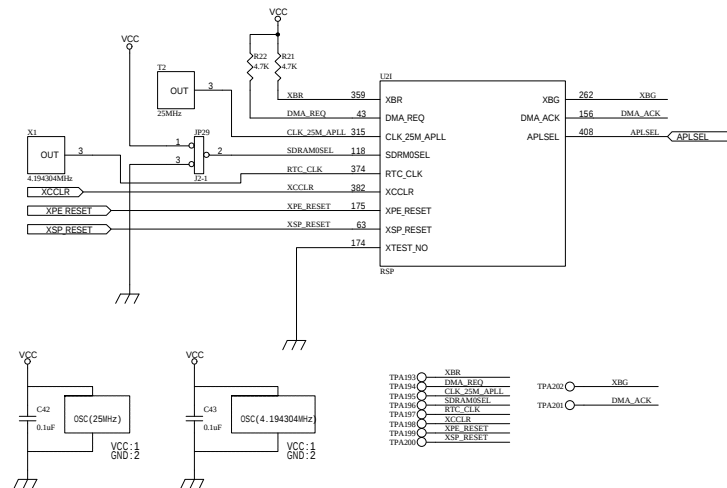
LINK



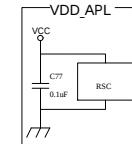
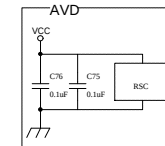
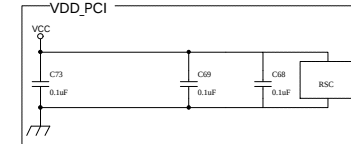
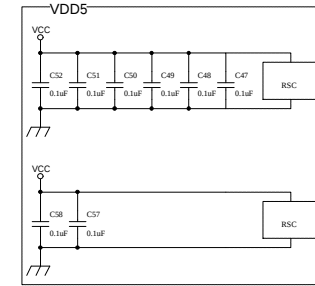
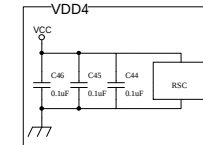
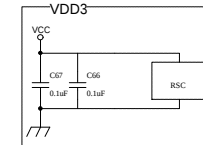
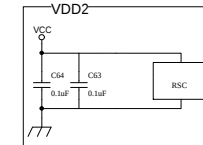
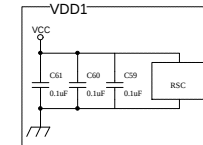
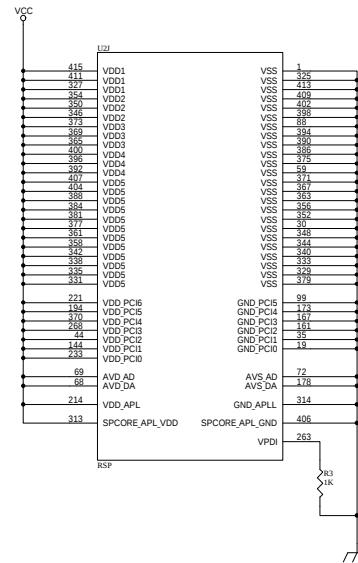
USB



TESTetc...



POWER



A	B	C	D
---	---	---	---

