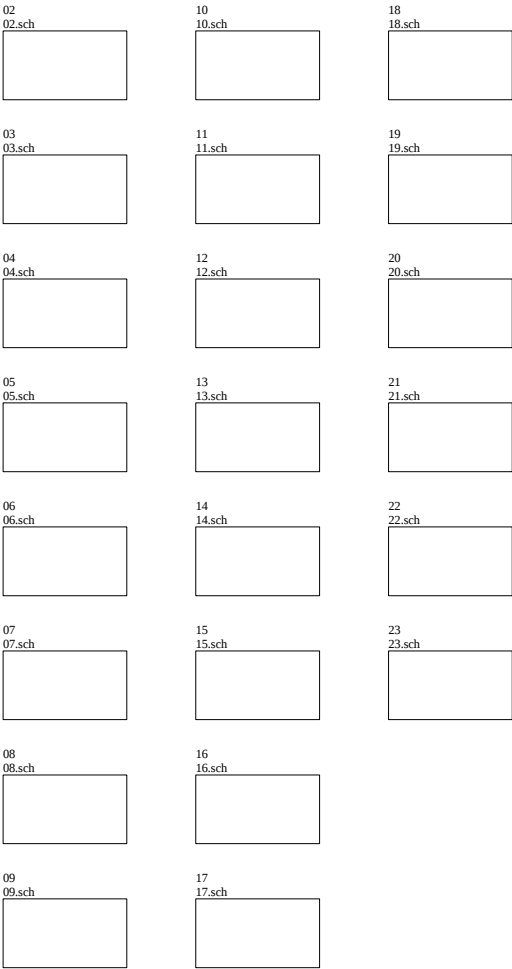
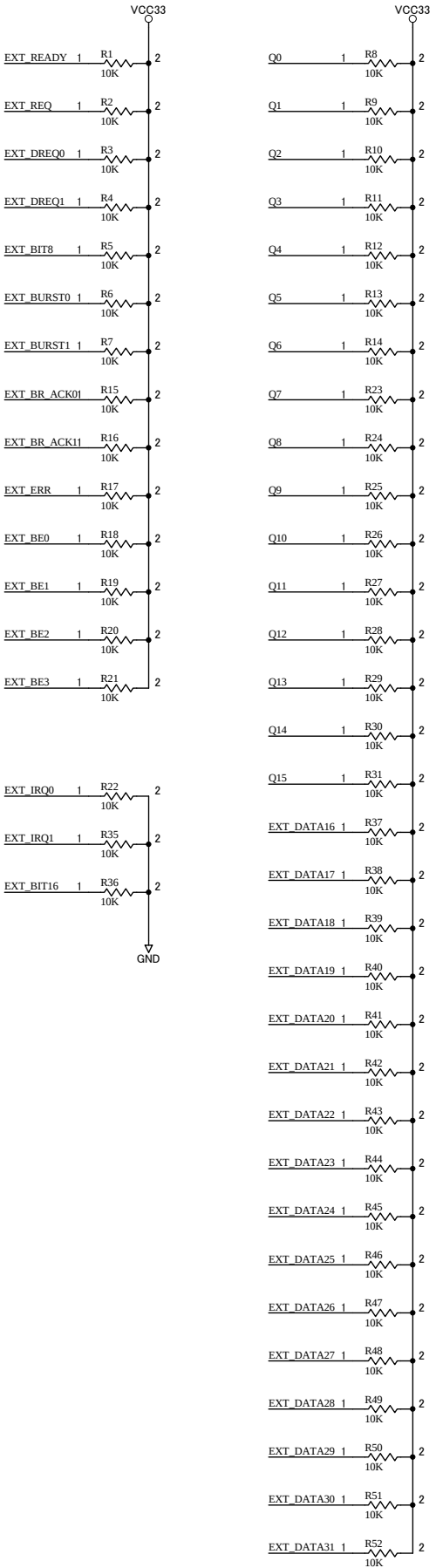
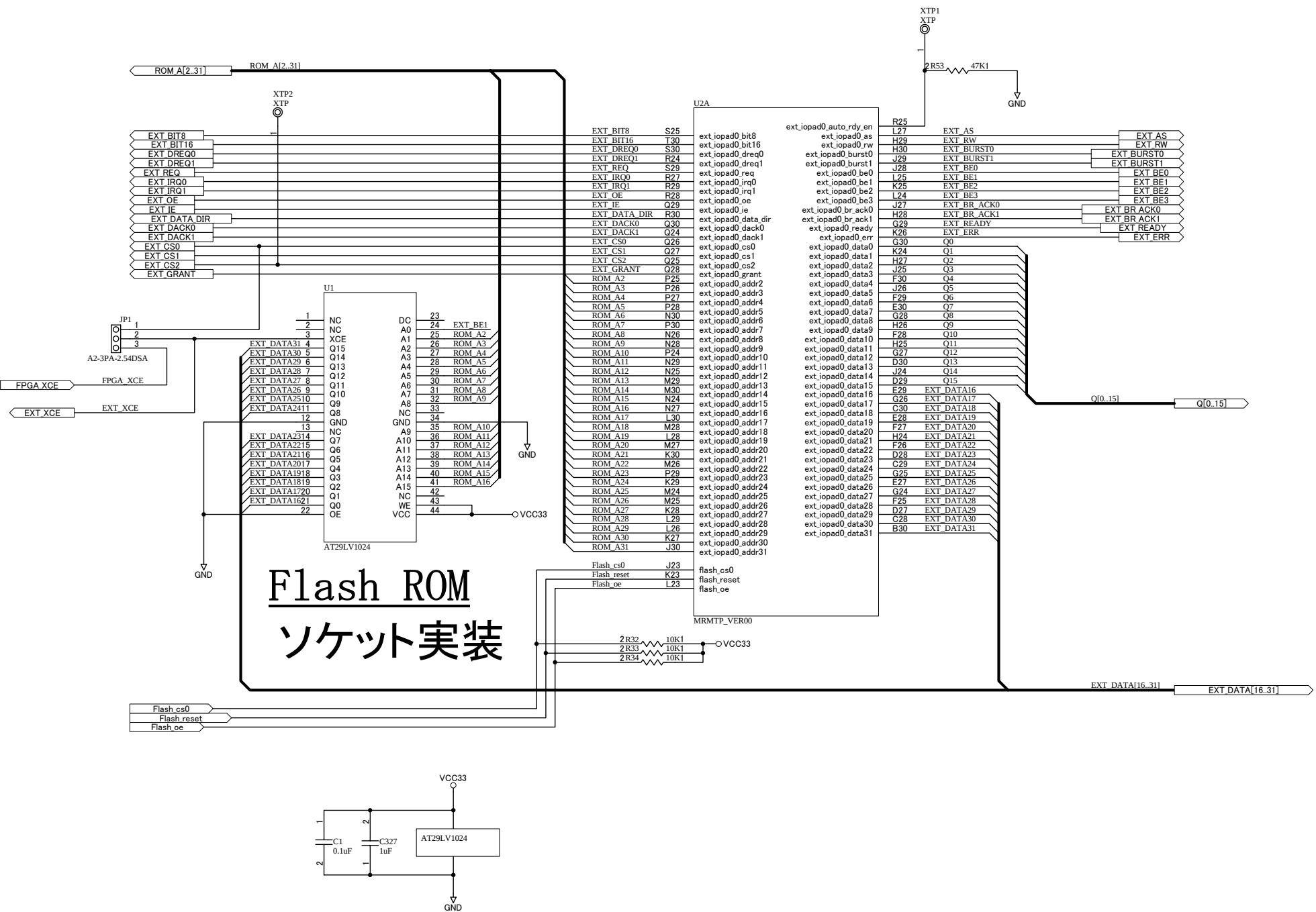


MRMTP Ver1.0 PCI評価ボード Main



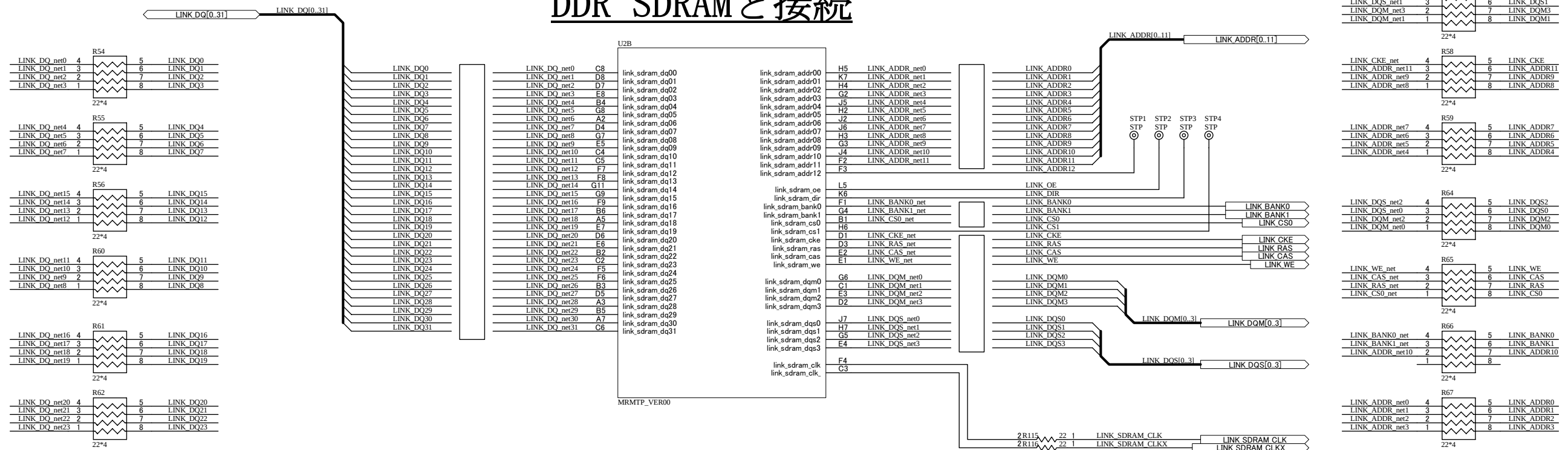
- 2:RMTP to FlashROM
- 3:RMTP to Link DDR SDRAM & PCI
- 4:RMTP to DDR SO-DIMM
- 5:Series & Bus Termination 1
- 6:Series & Bus Termination 2
- 7:RMTP to IEEE1394 & UART
- 8:RMTP to PLL & PWM
- 9:RMTP Power
- 10:RMTP to LVDS
- 11:LVDS Buff
- 12:Power & Reset
- 13:PCI BUS EDGE
- 14:DDR SO-DIMM SOCKET 1
- 15:DDR SO-DIMM SOCKET 2
- 16:LINK DDR SDRAM
- 17:UART
- 18:RJ45(LINK Serial)
- 19:Logic Analyzer Connector
- 20:To PCI SubBoard Connector
- 21:FPGA
- 22:Configuration
- 23:Ether

RMTプロセッサ

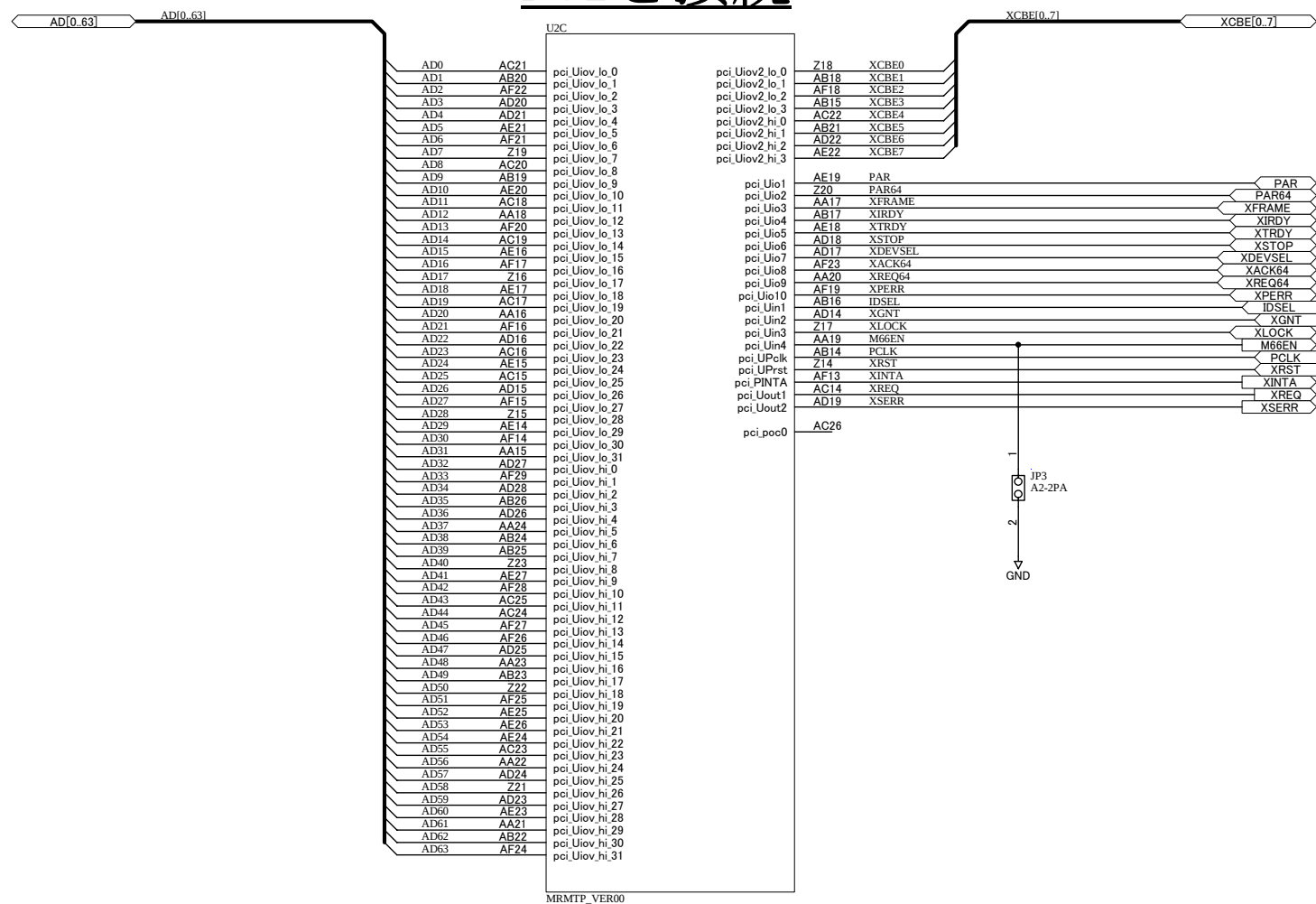


RMTプロセッサ

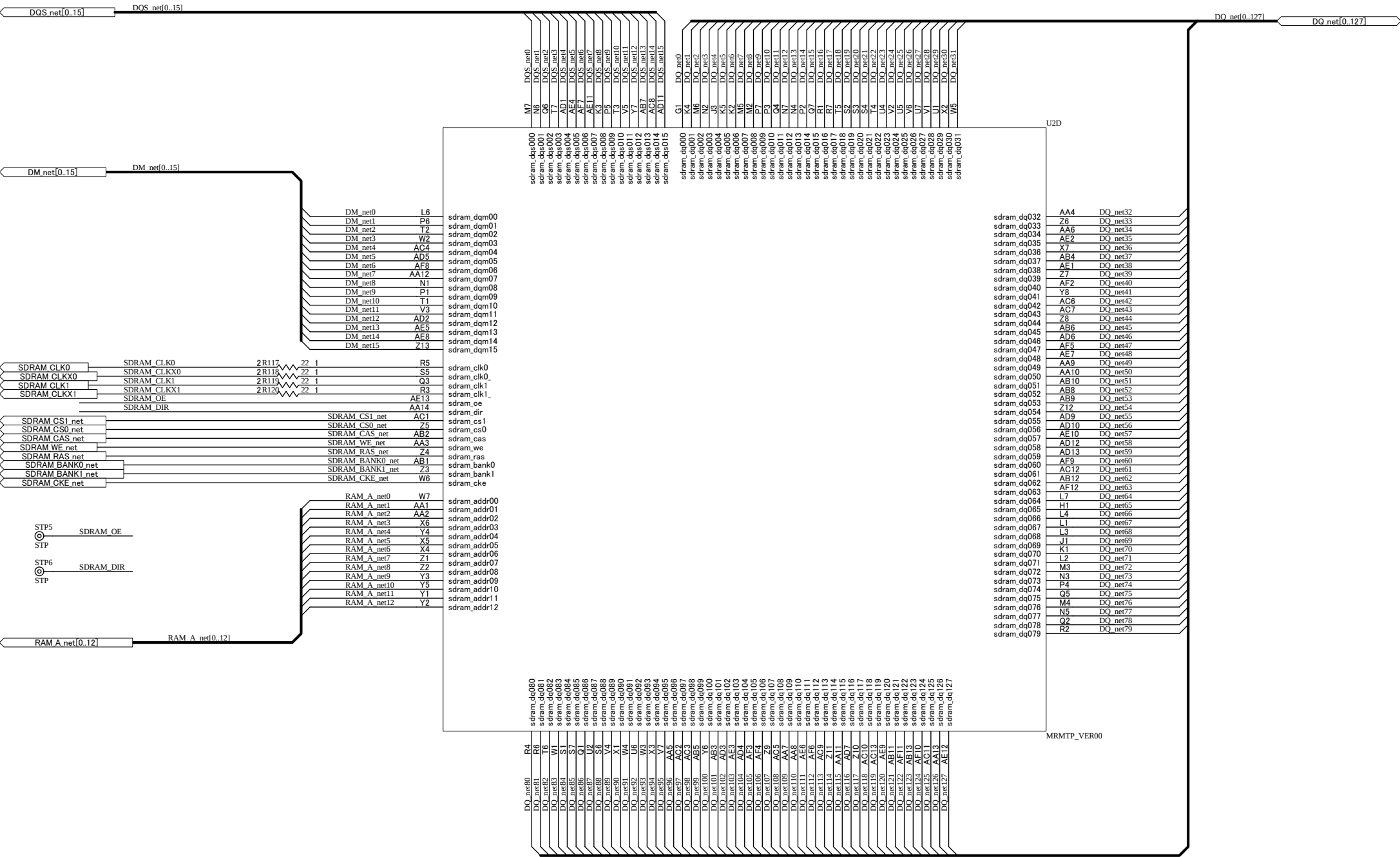
DDR SDRAMと接続



PCIと接続

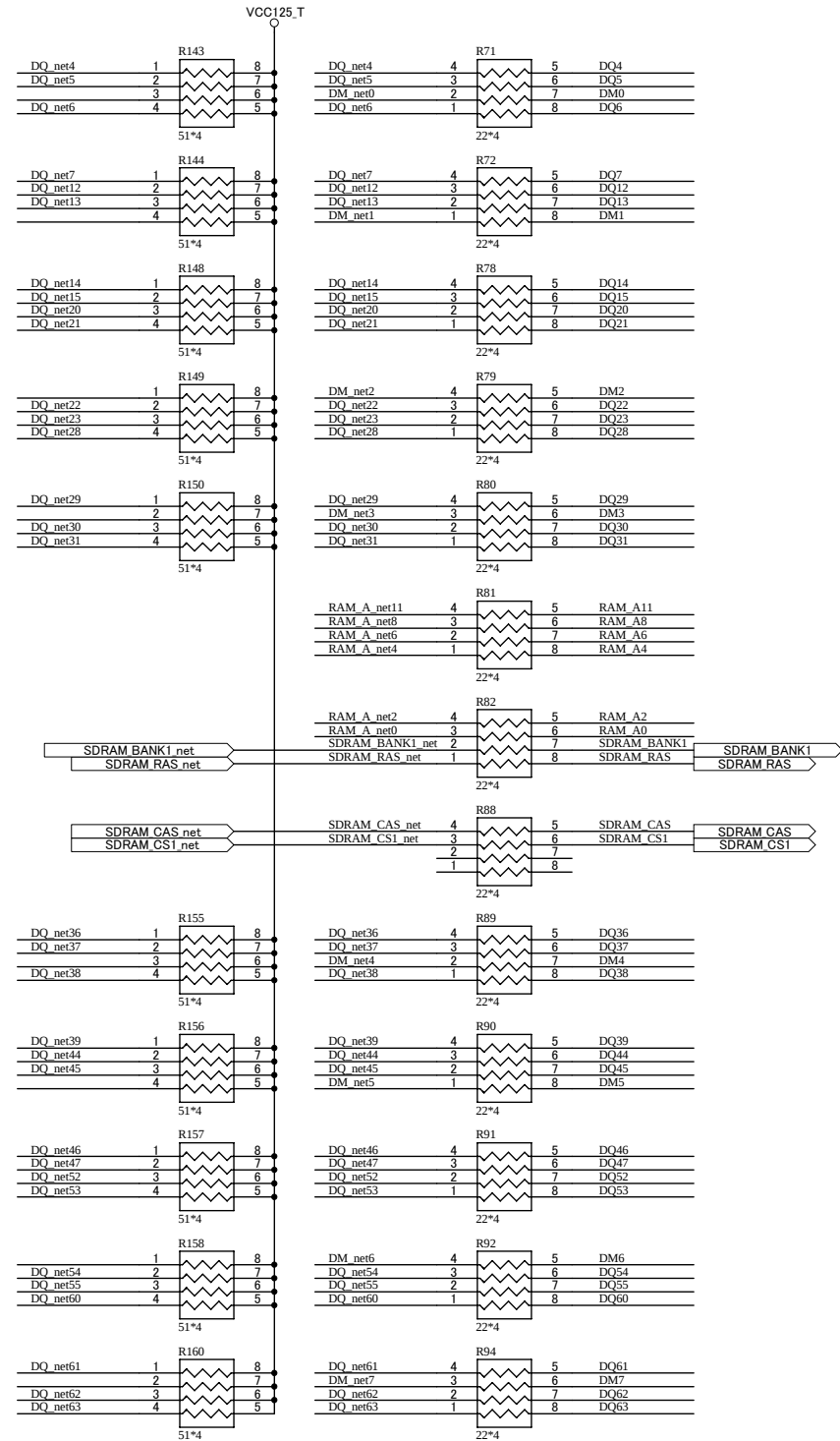
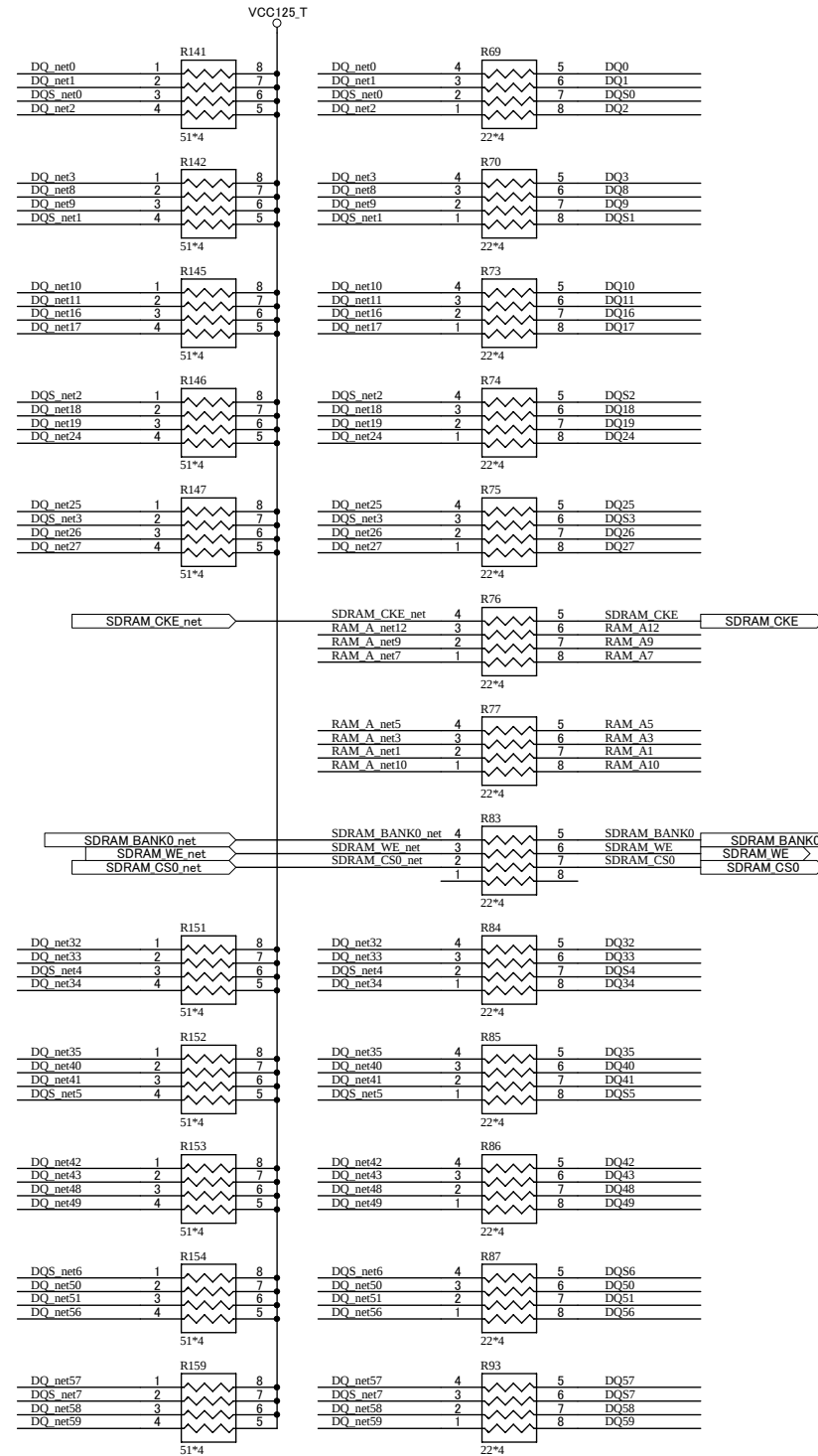
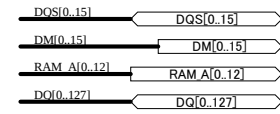
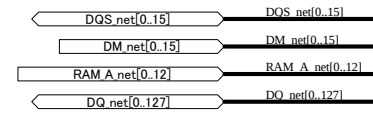


RMTプロセッサ

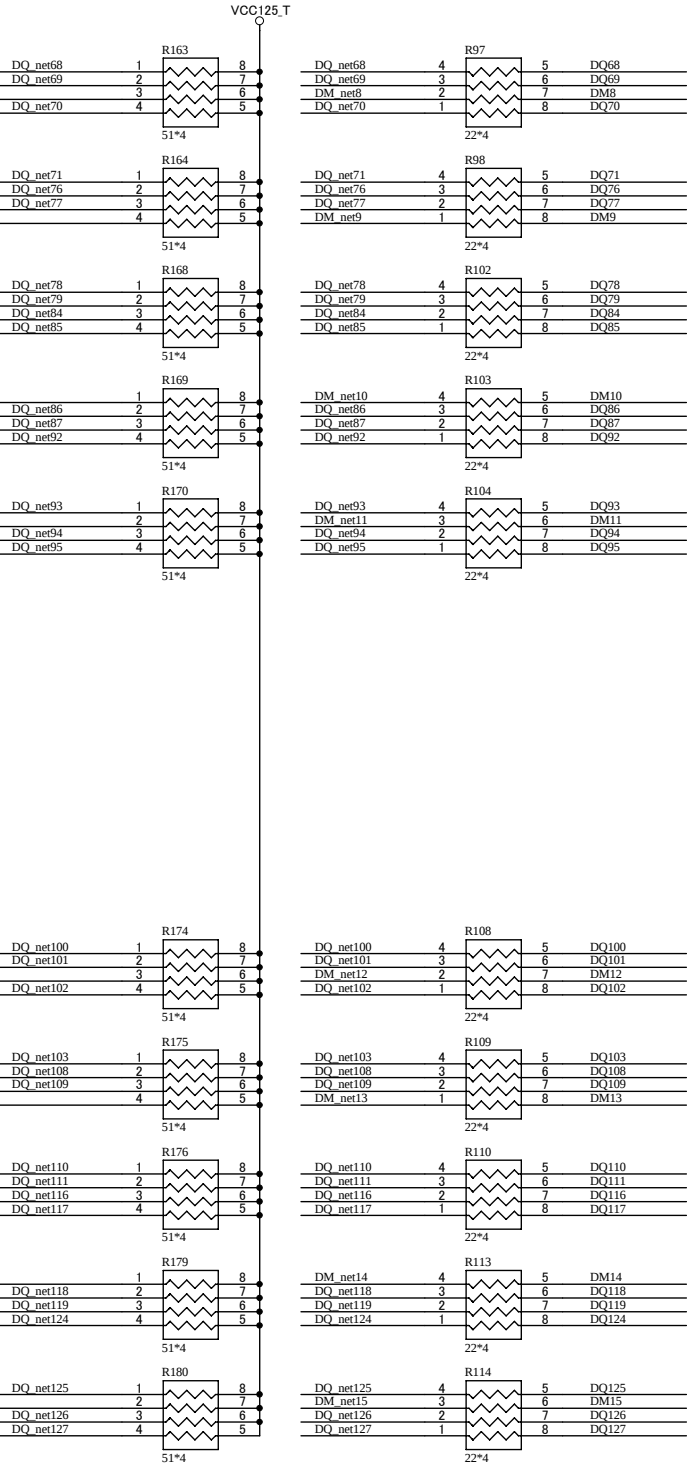
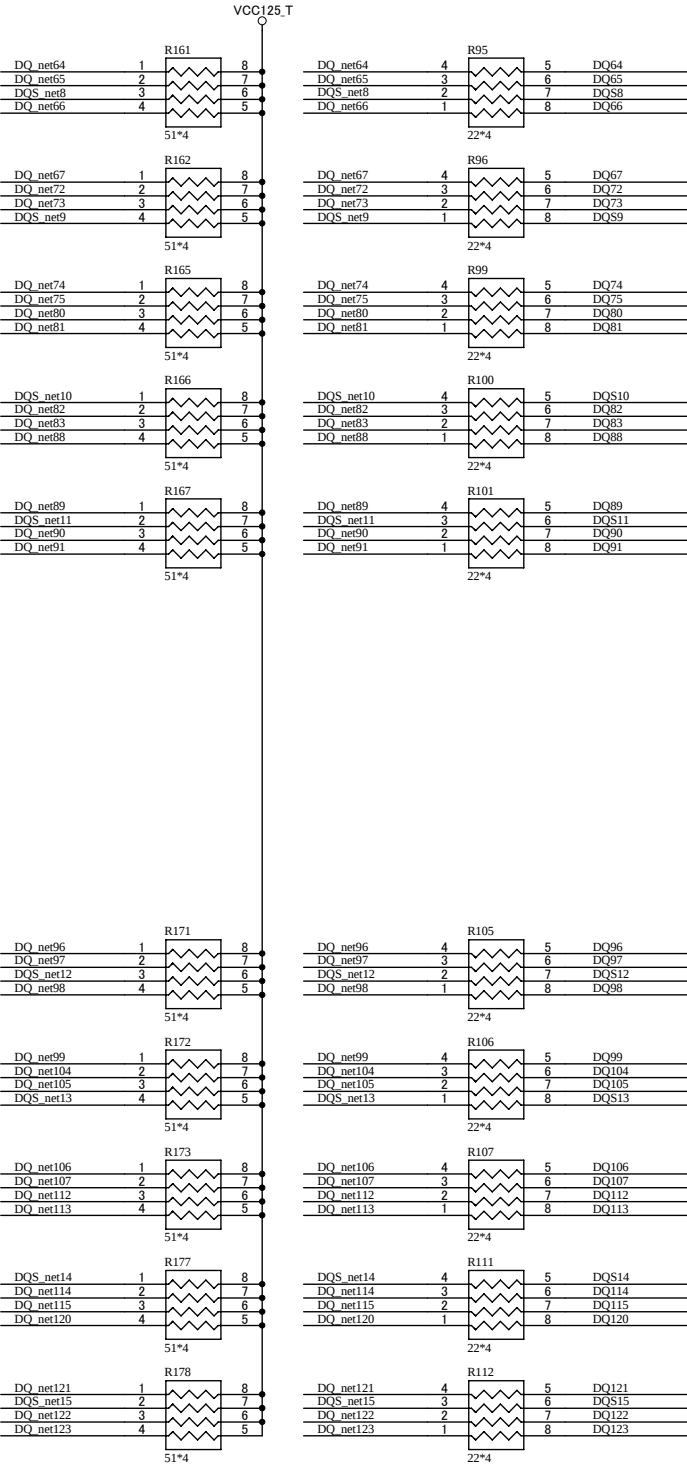
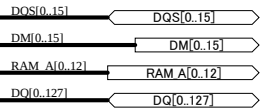
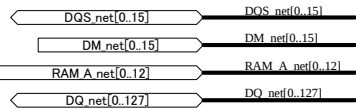


DDR SO-DIMMと接続





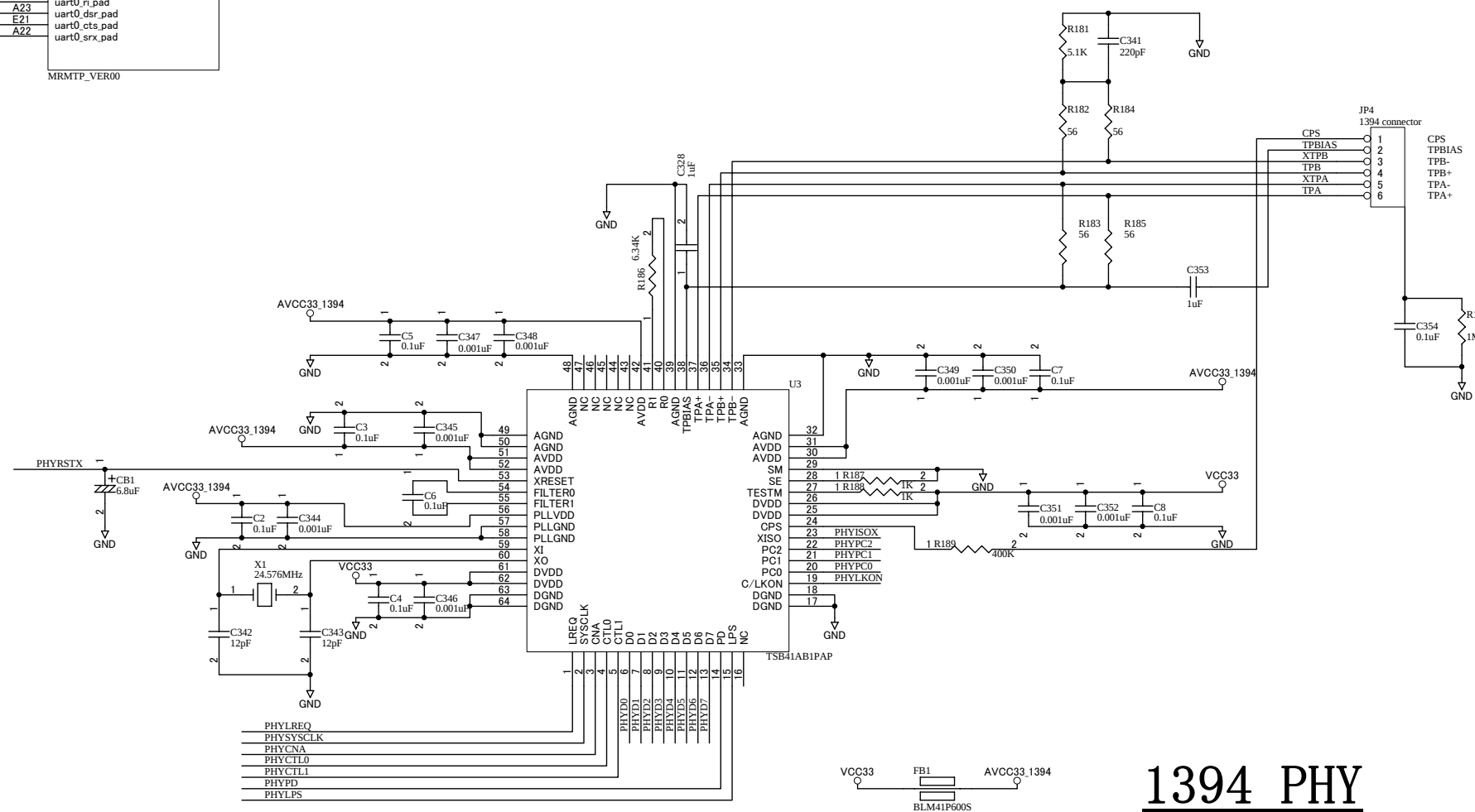
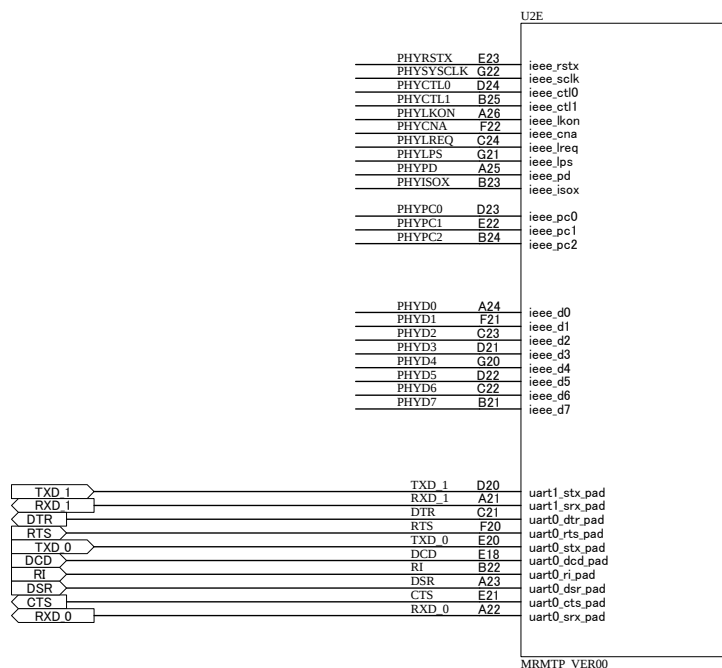
DDR SO-DIMMと接続



DDR SO-DIMMと接続

RMTプロセッサ

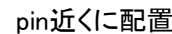
UART



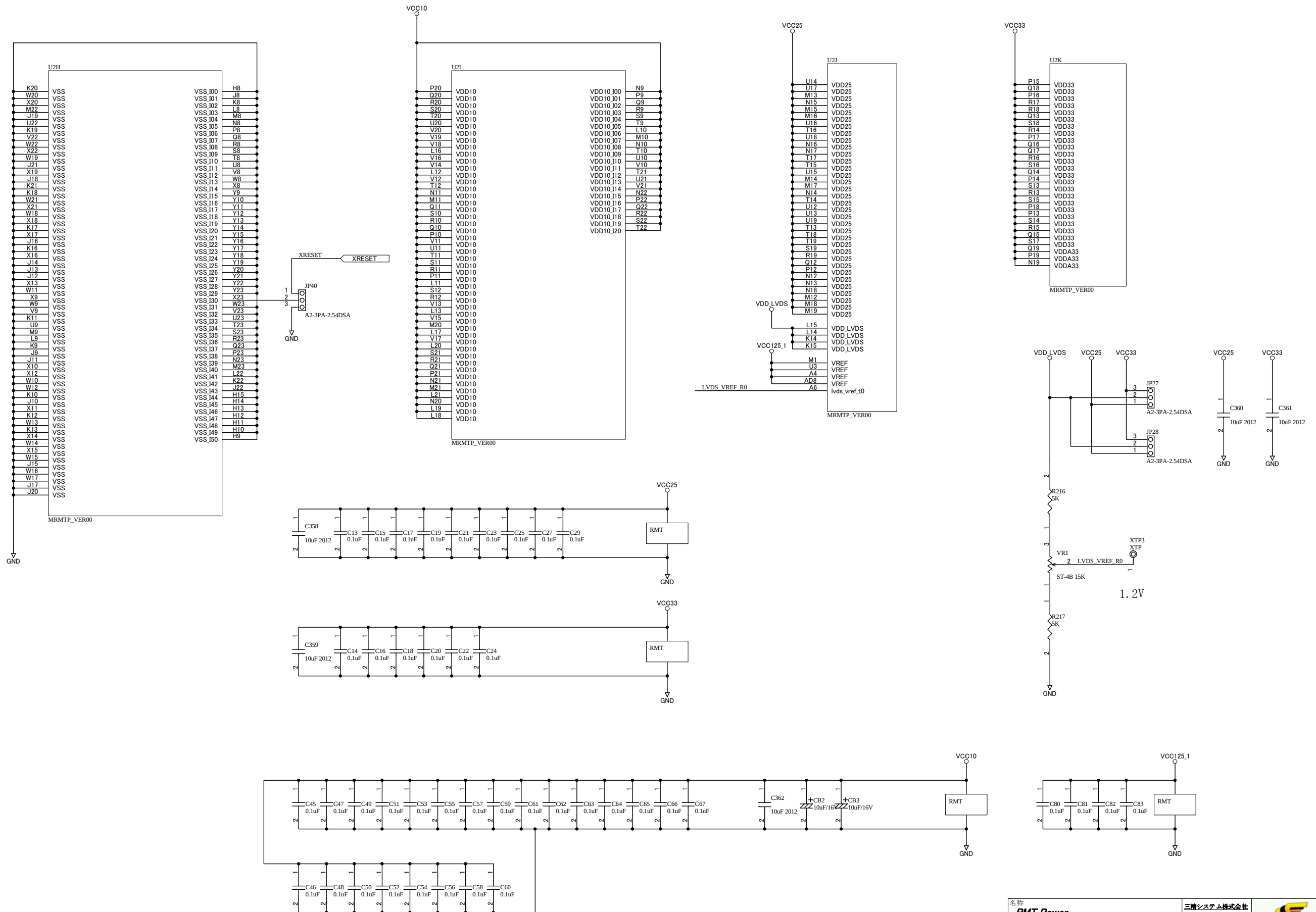
1394 PHY



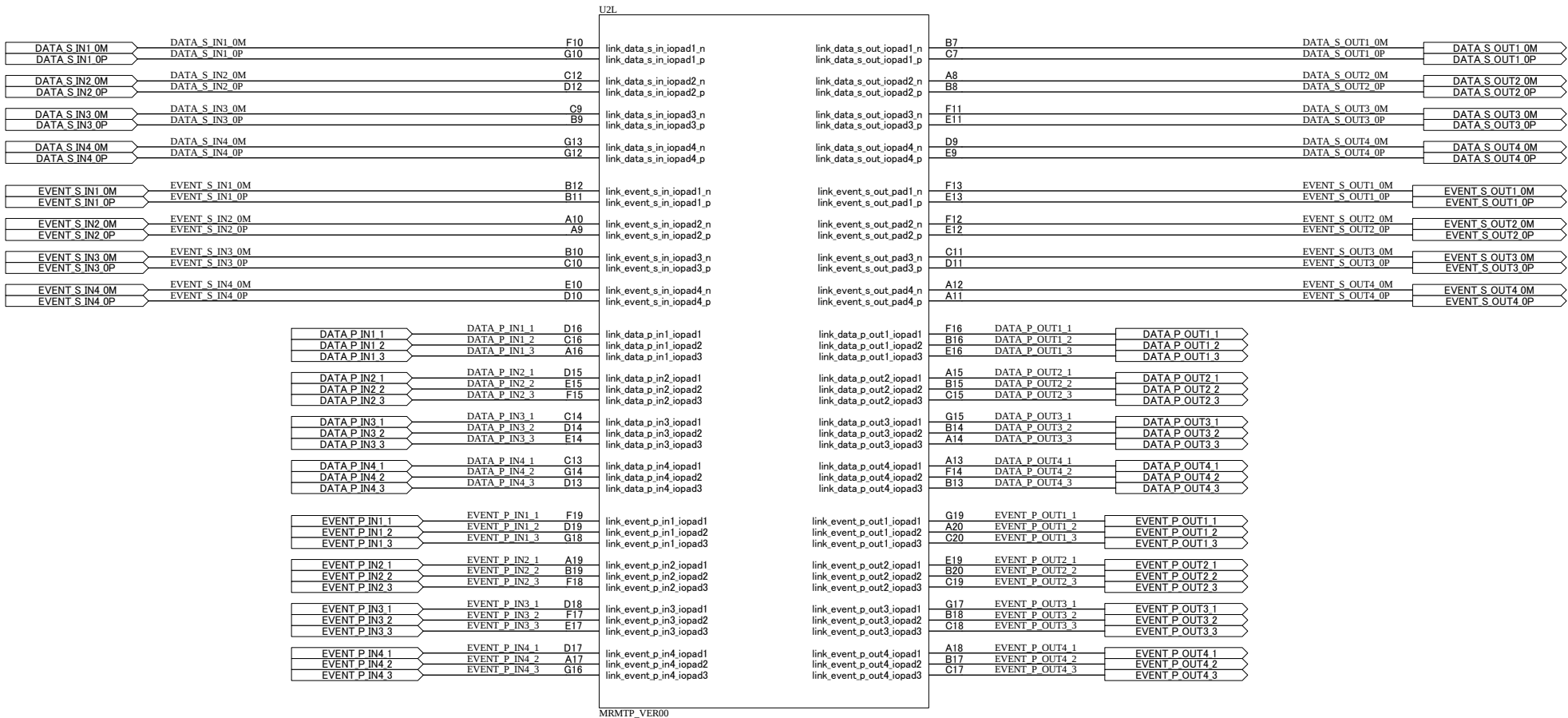
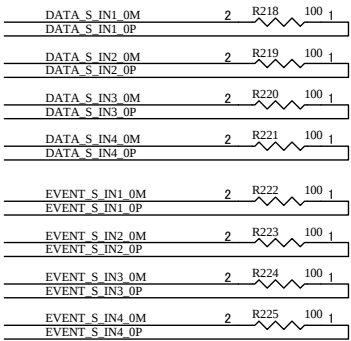
D

A

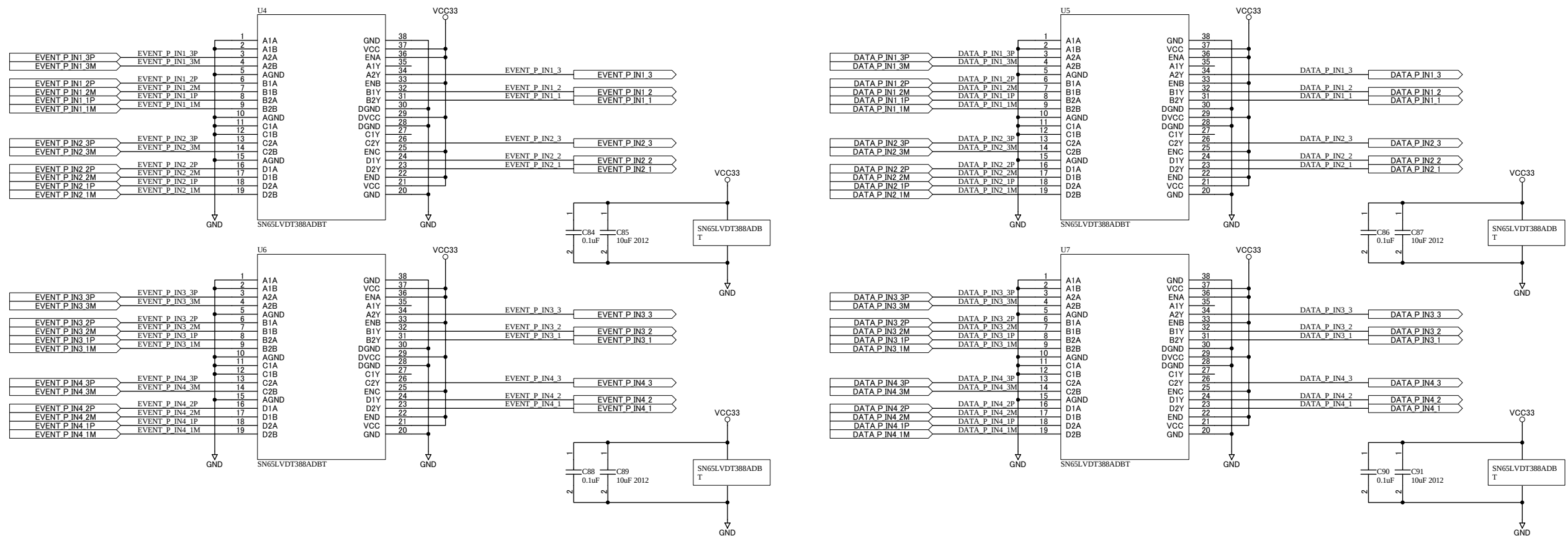
RMTプロセッサ



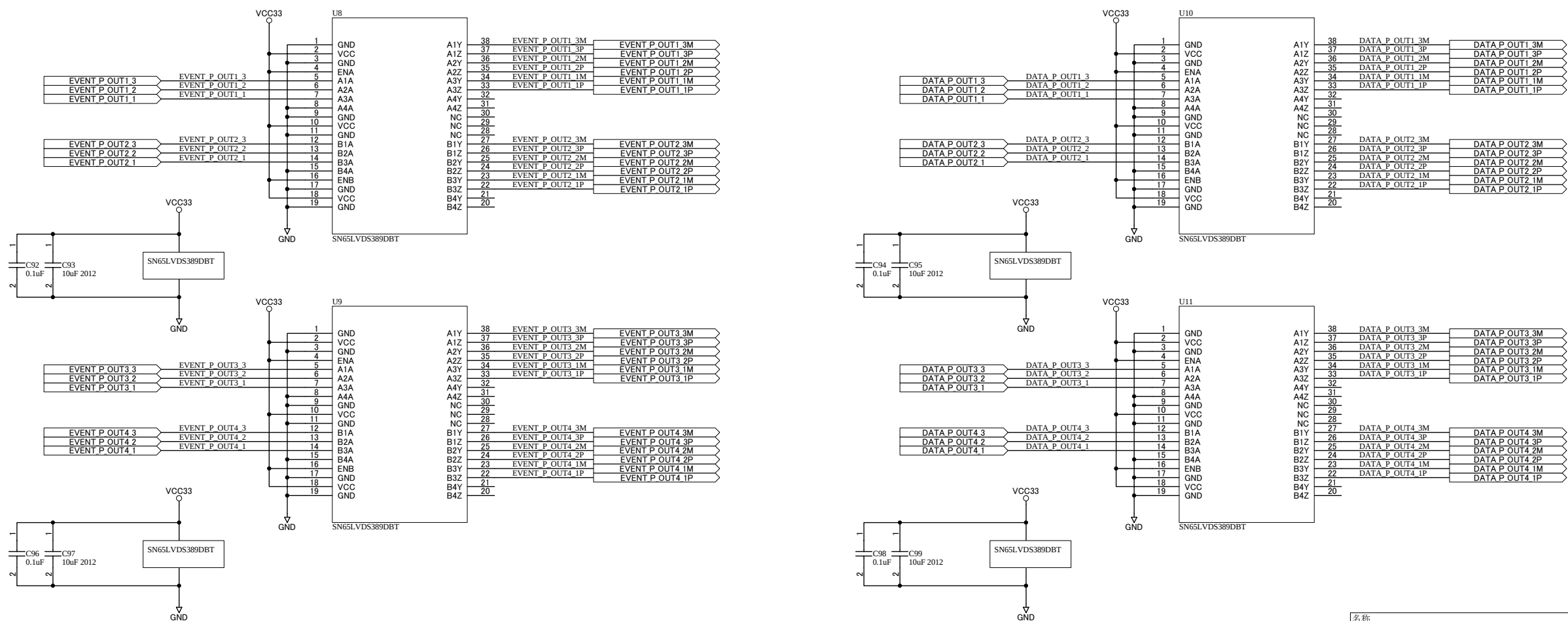
RMTプロセッサ

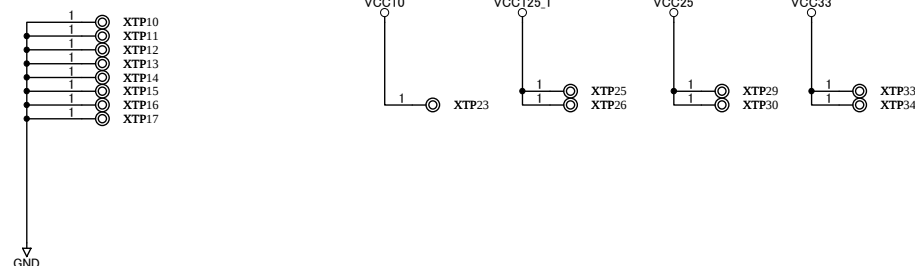
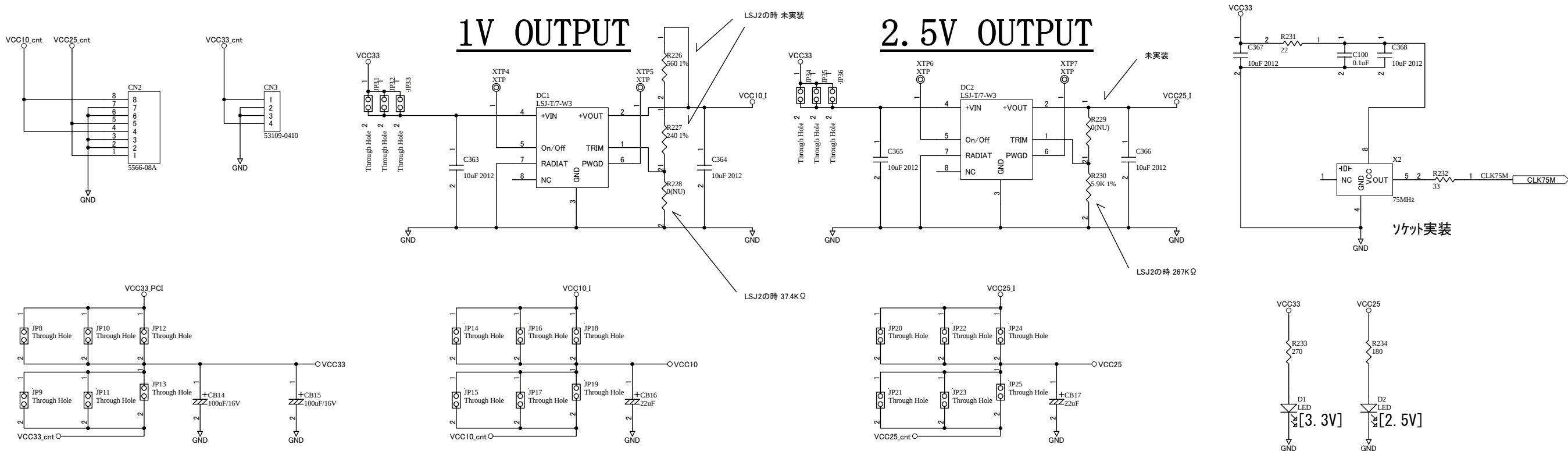


LVDS INPUT

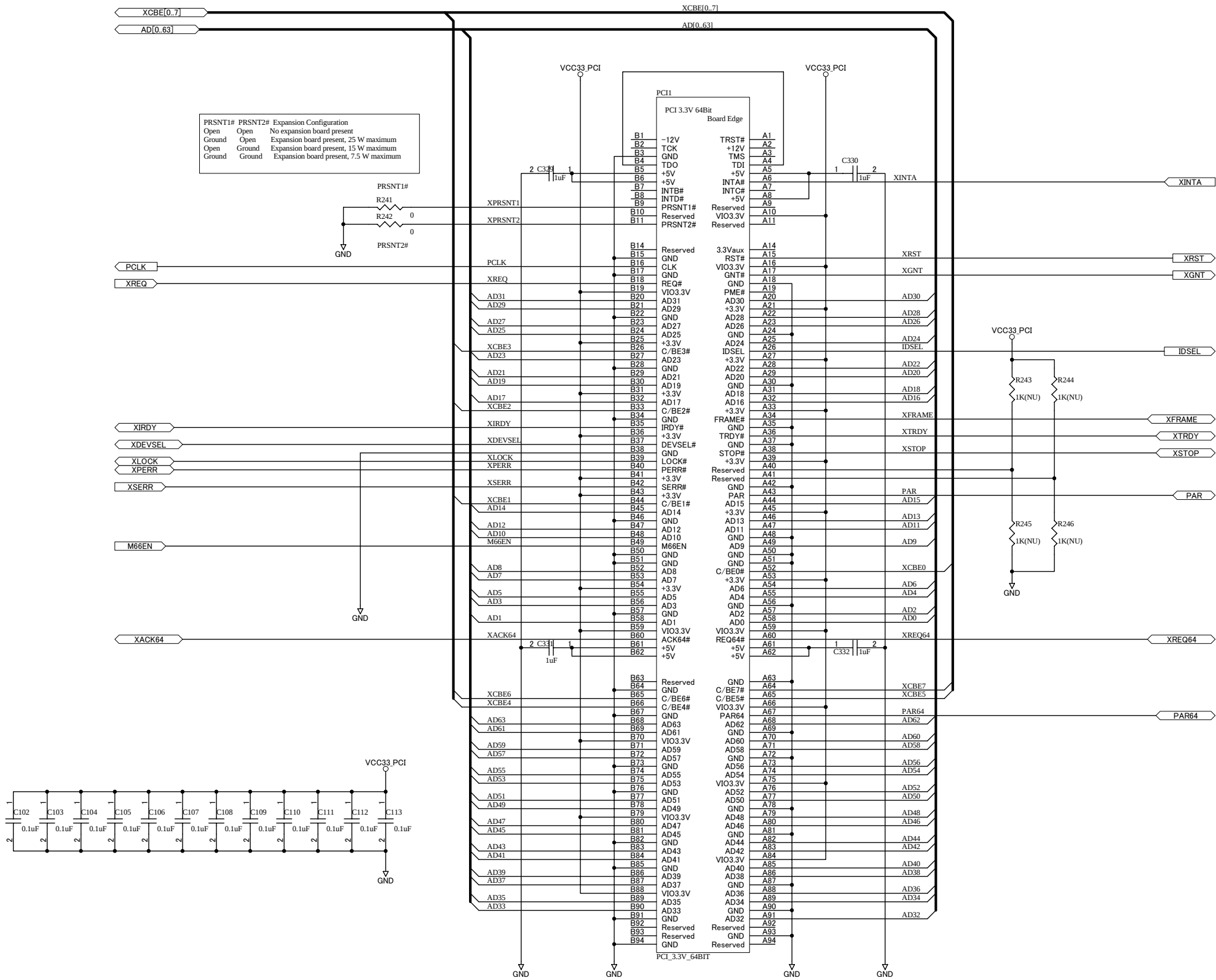


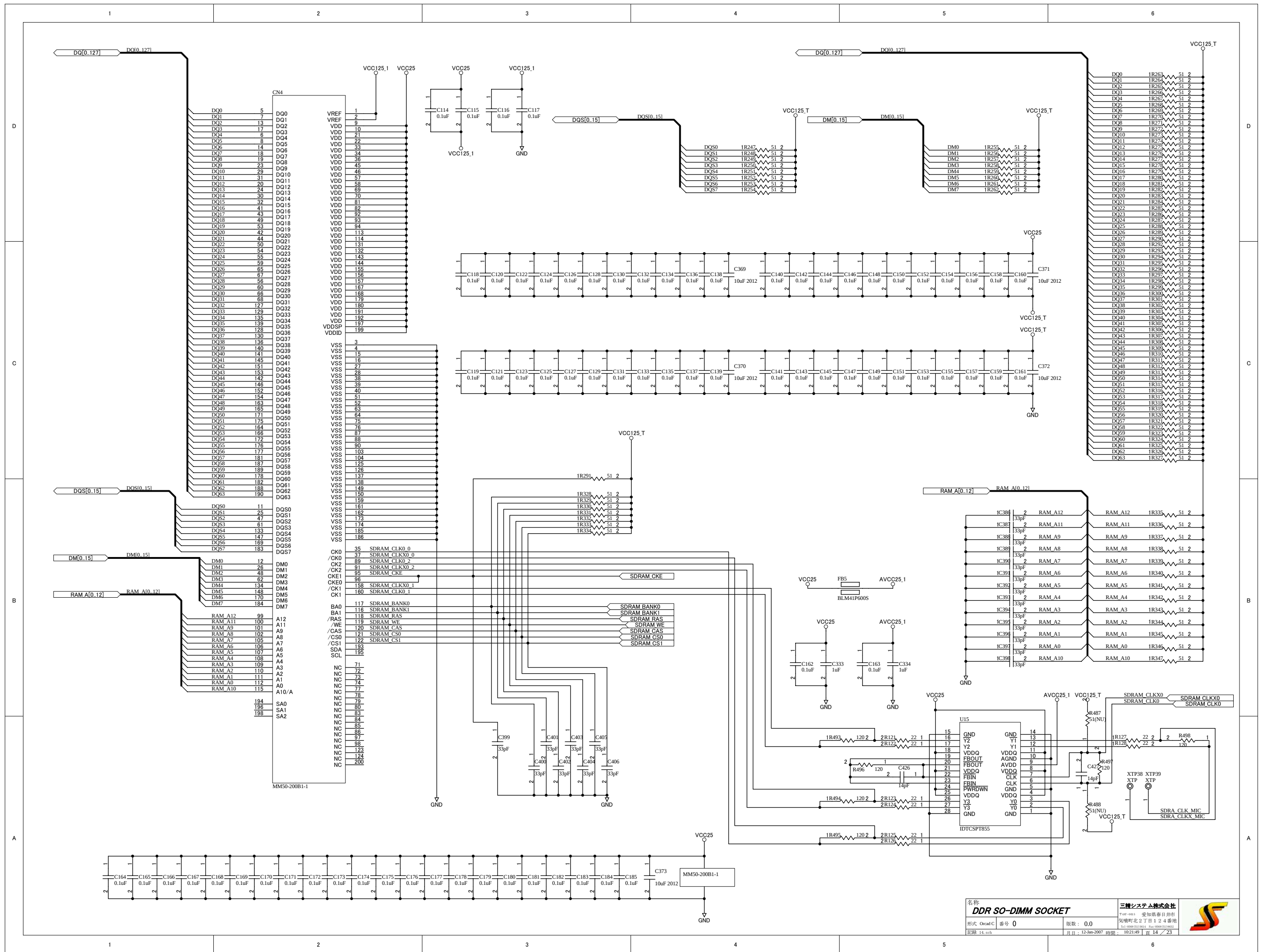
LVDS OUTPUT

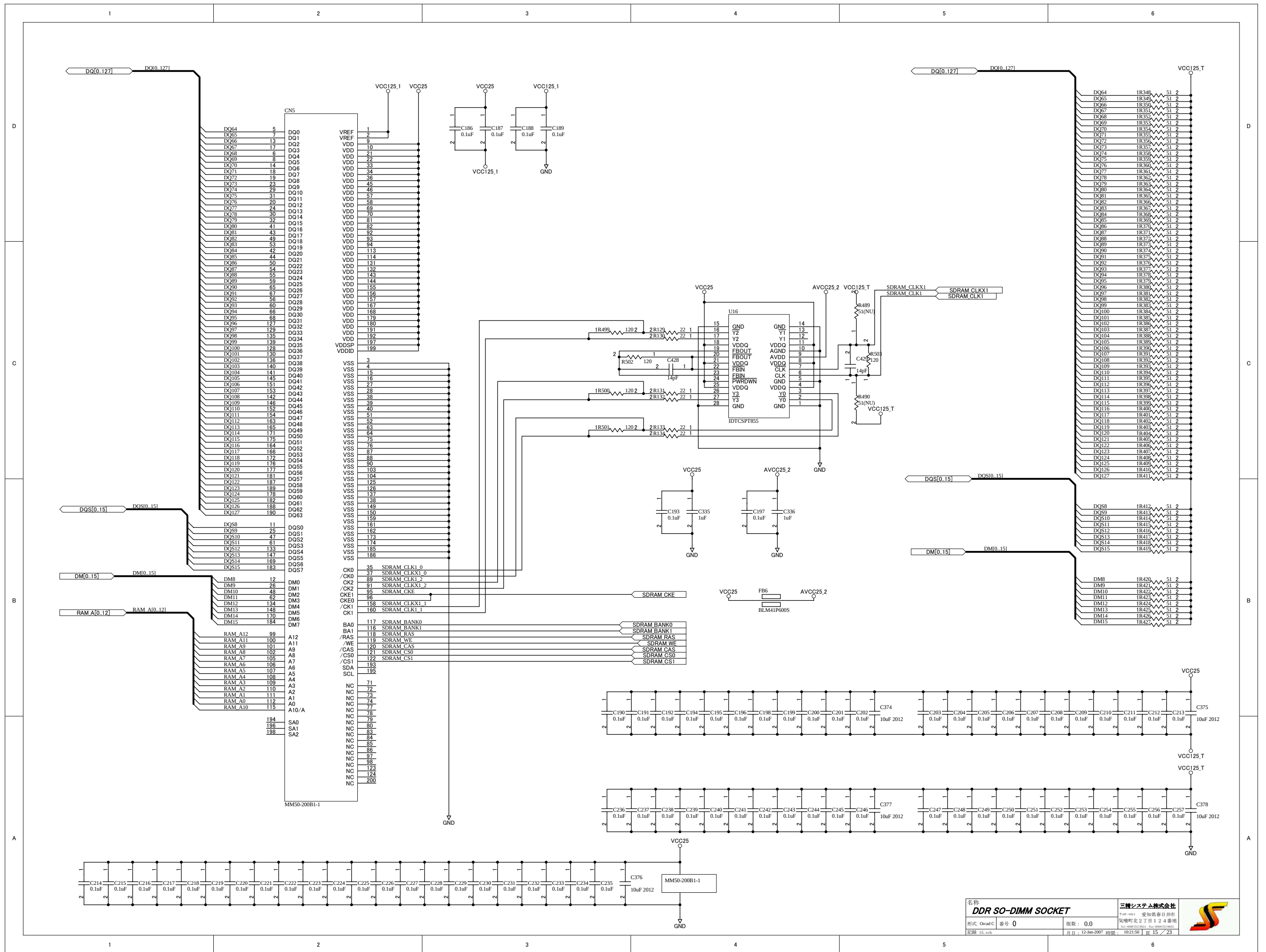




PCI bus Edge







Link用 DDR SDRAM

D

C

B

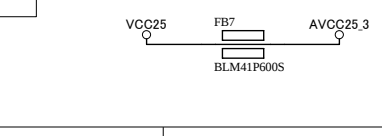
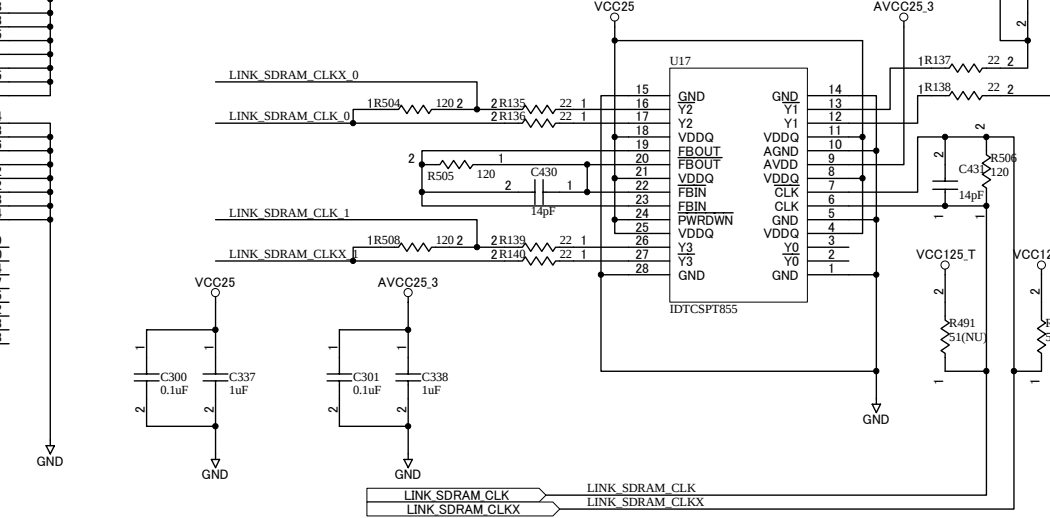
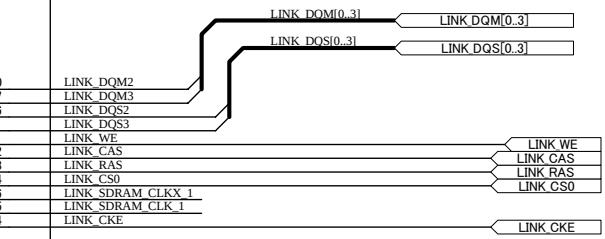
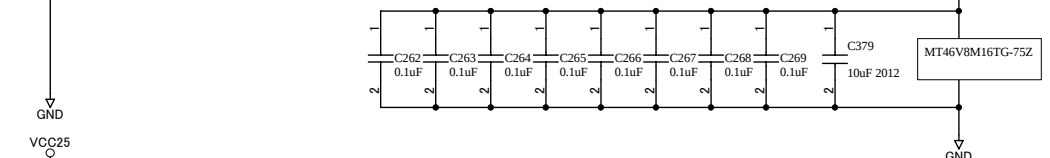
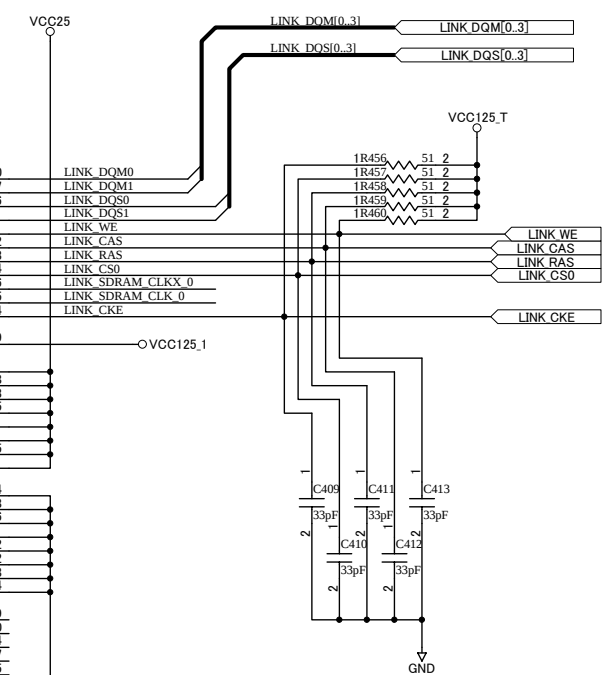
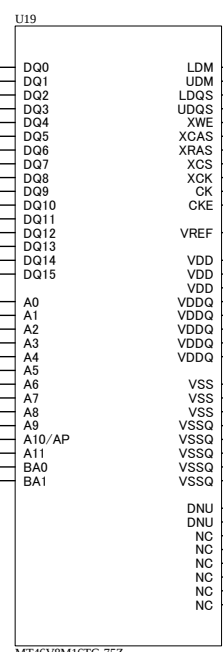
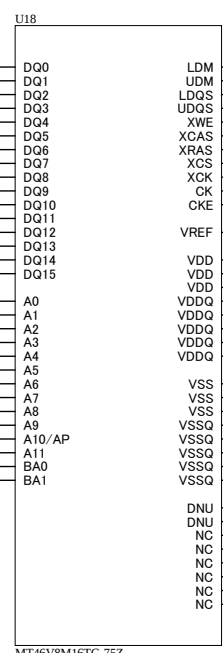
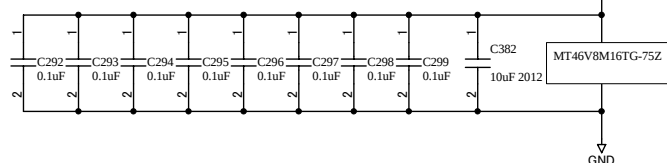
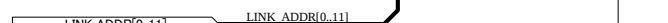
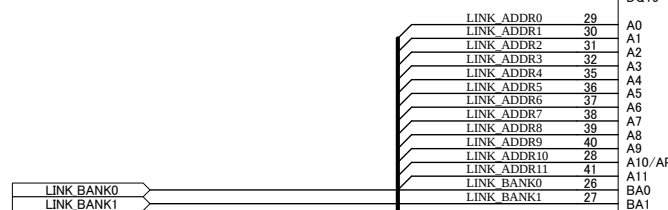
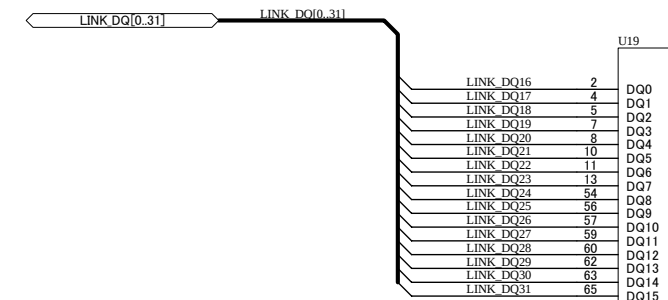
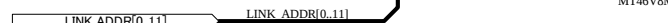
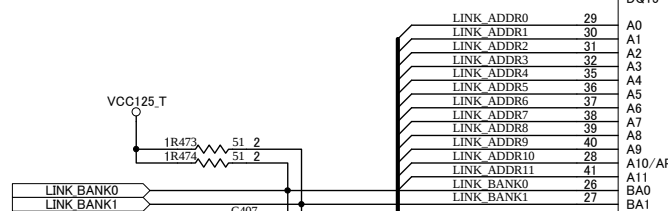
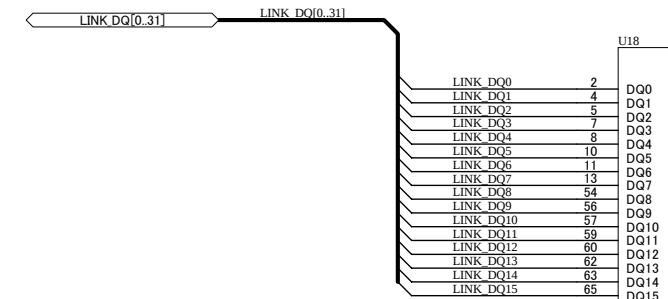
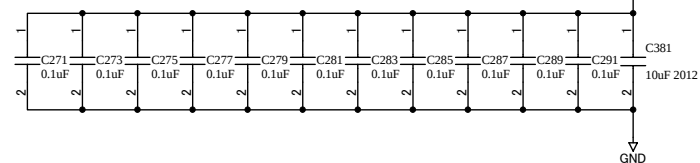
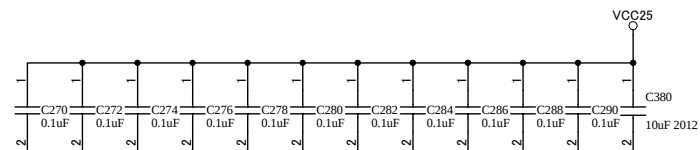
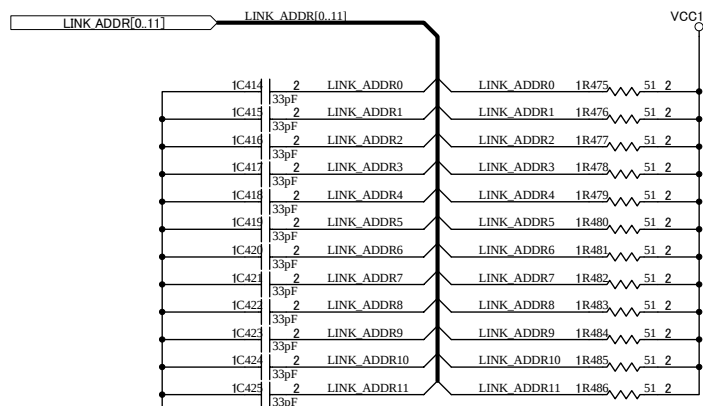
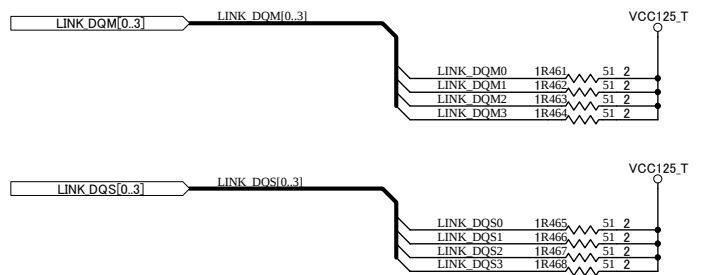
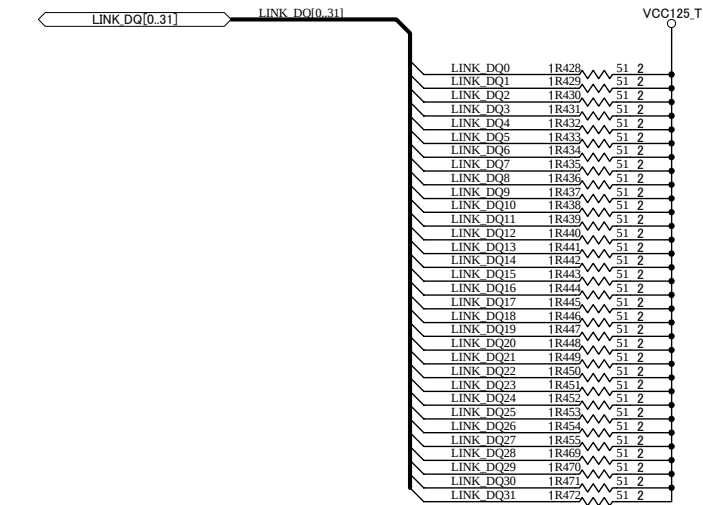
A

D

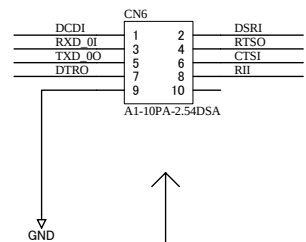
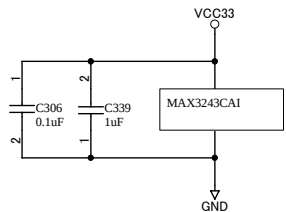
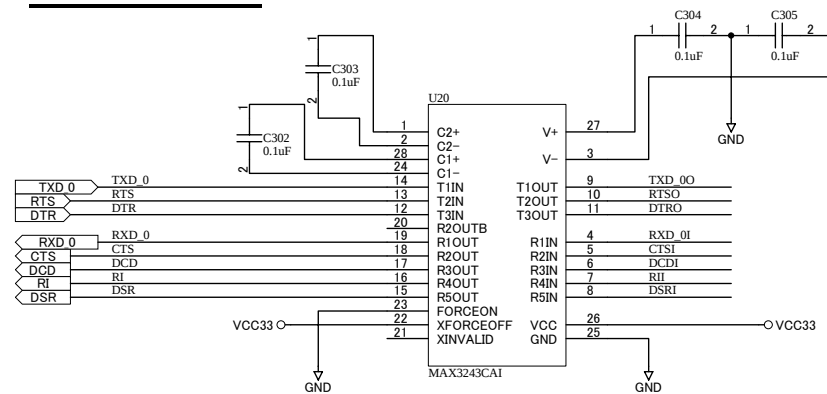
C

B

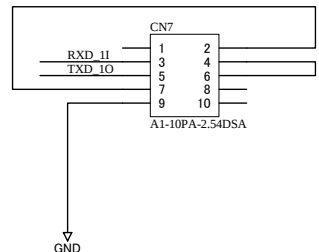
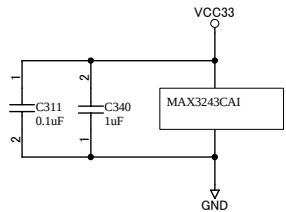
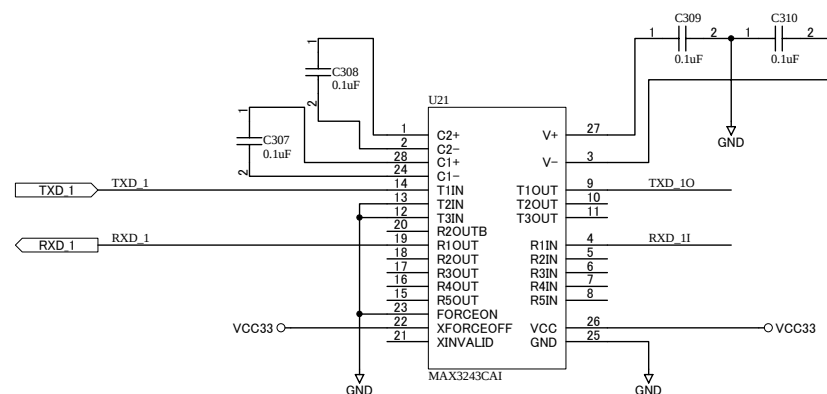
A



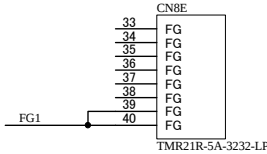
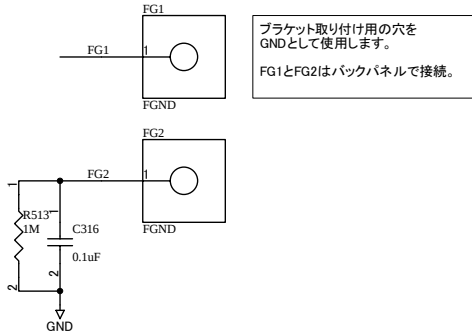
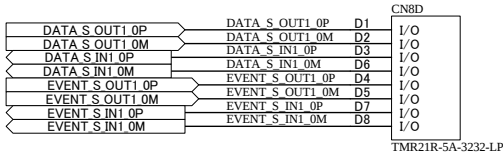
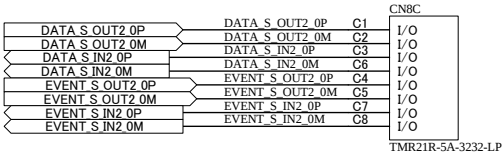
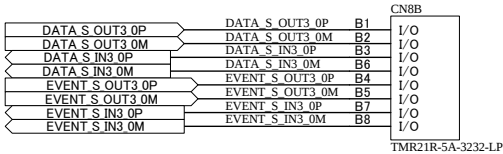
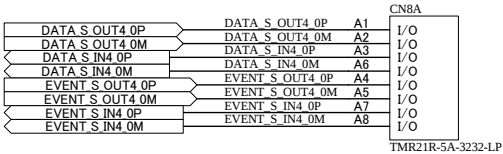
RS-232C



コネクタ間を離す

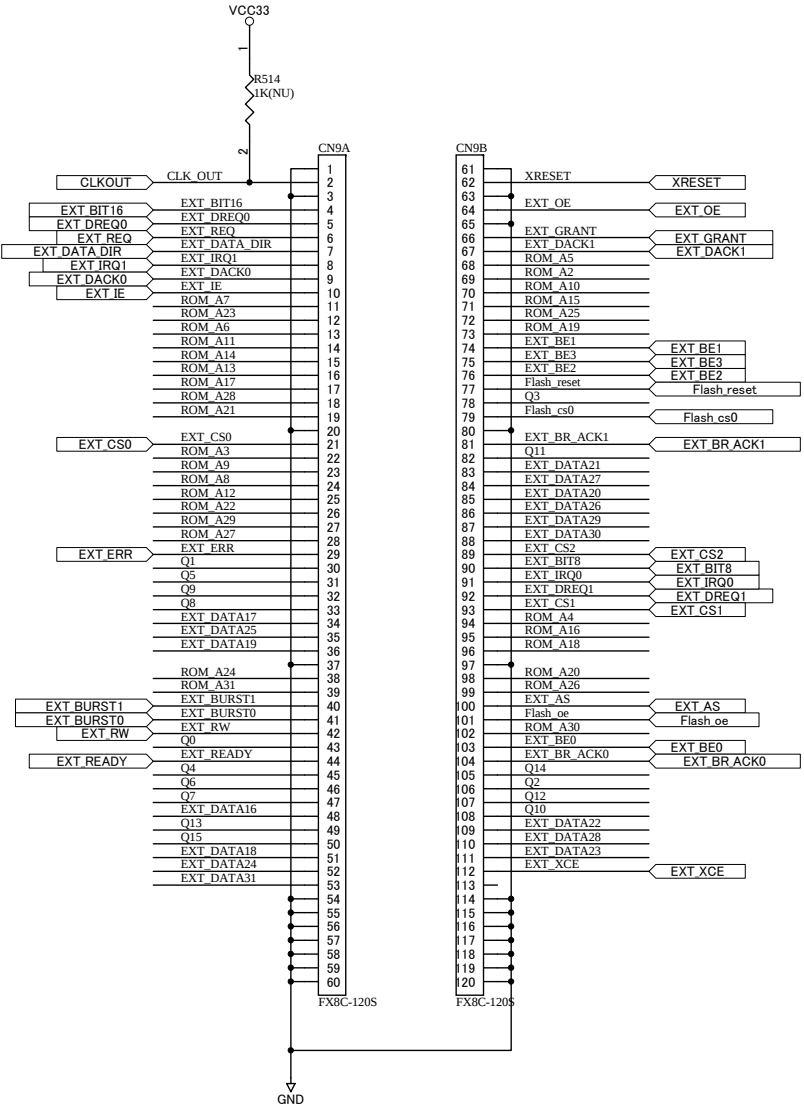


LINK Serial



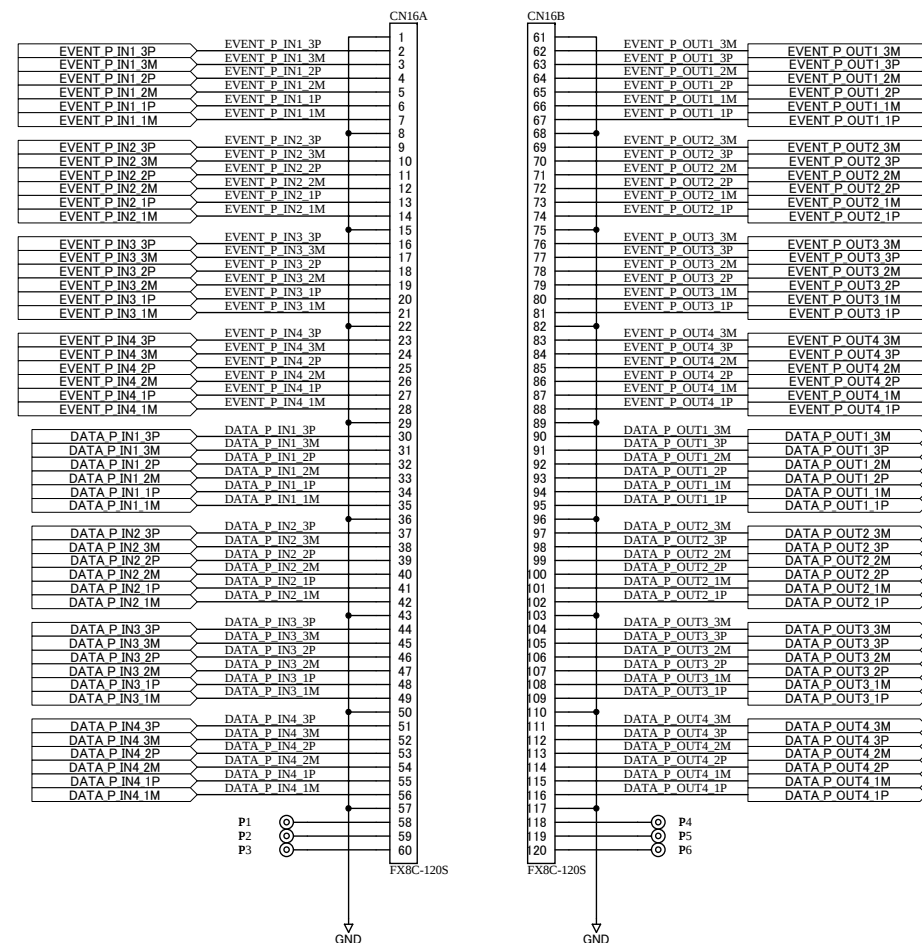
2ボード間接続コネクタ

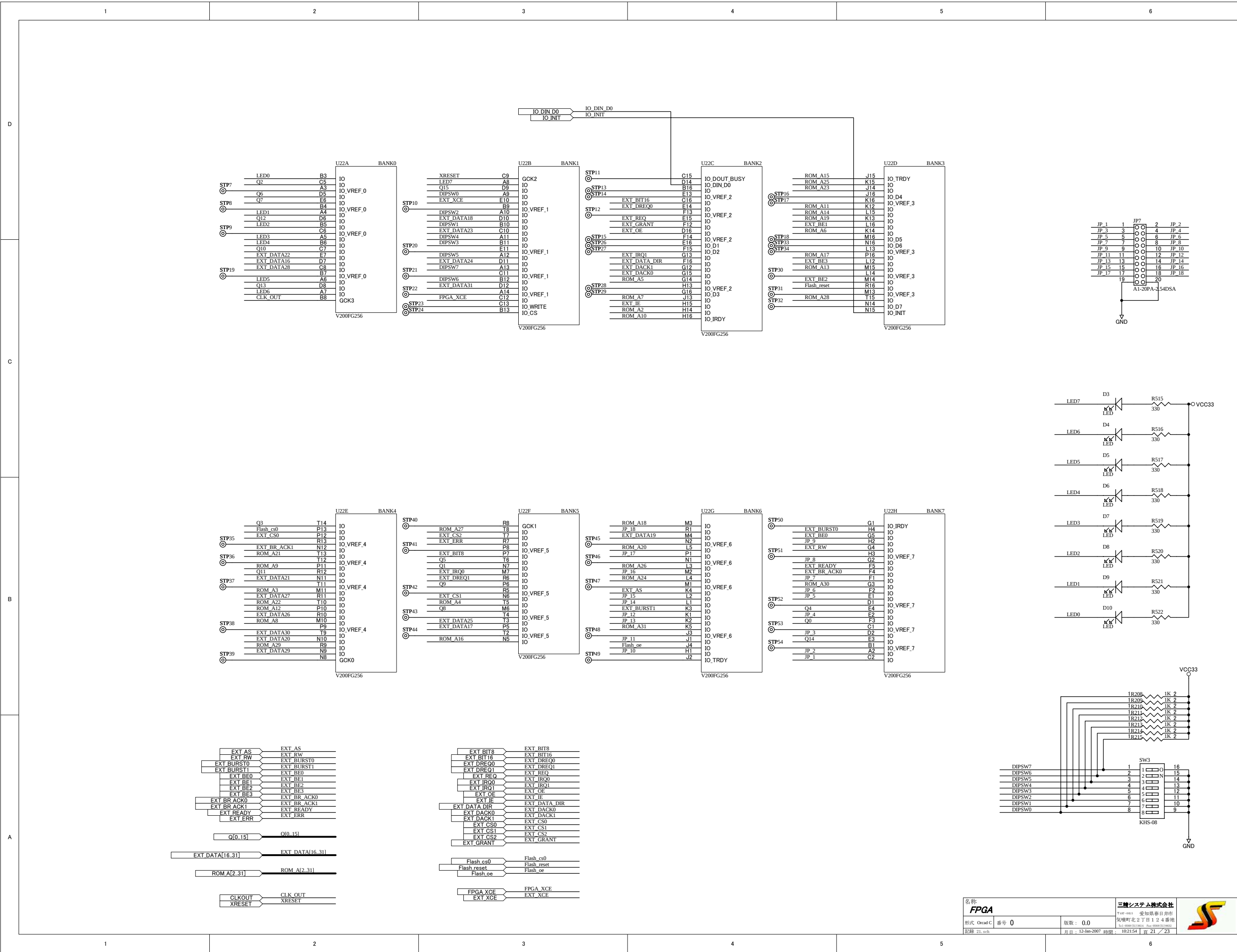
FOR MICTOR



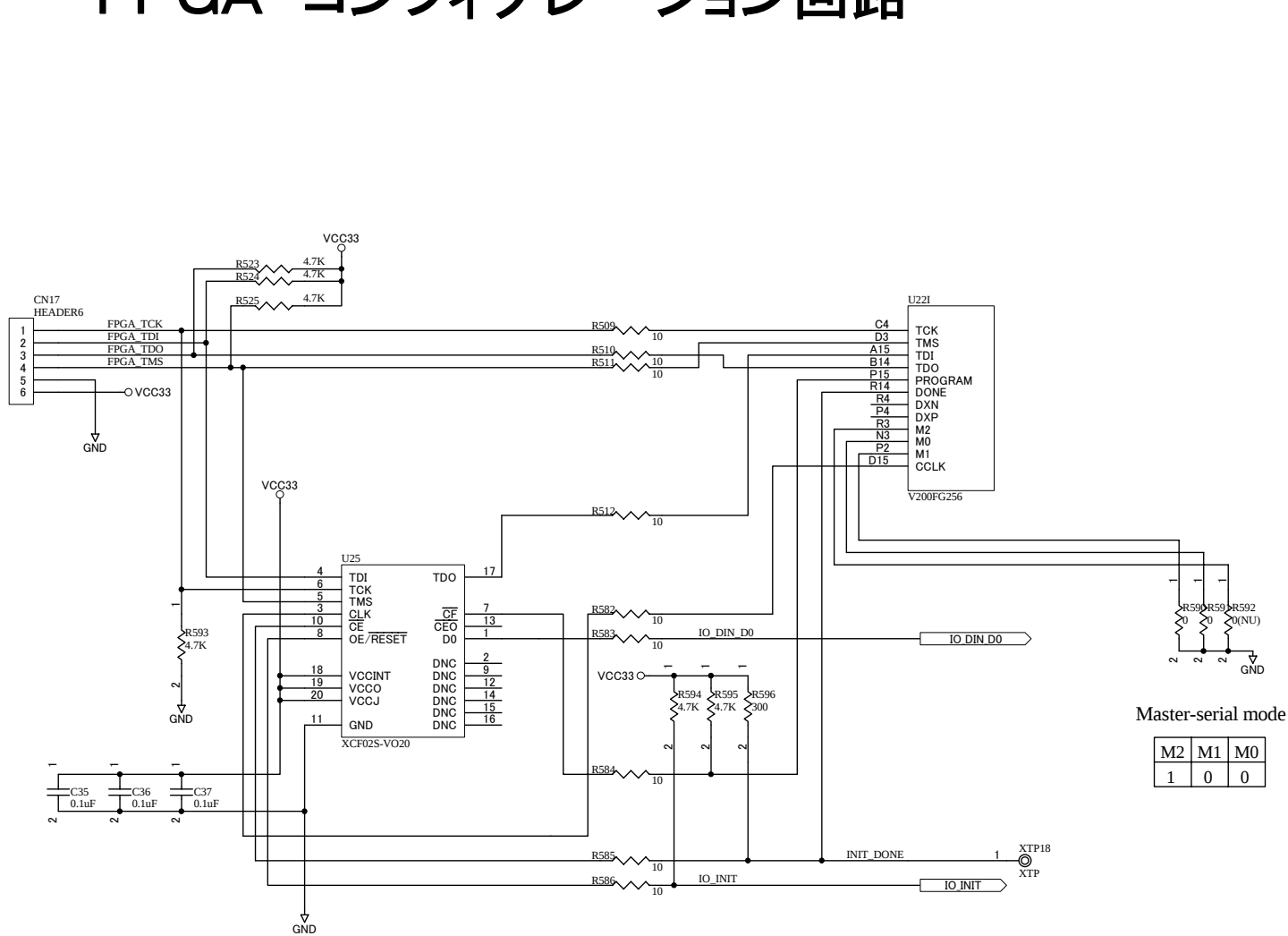
2ボード間接続コネクタ

FOR LINK PARALLEL



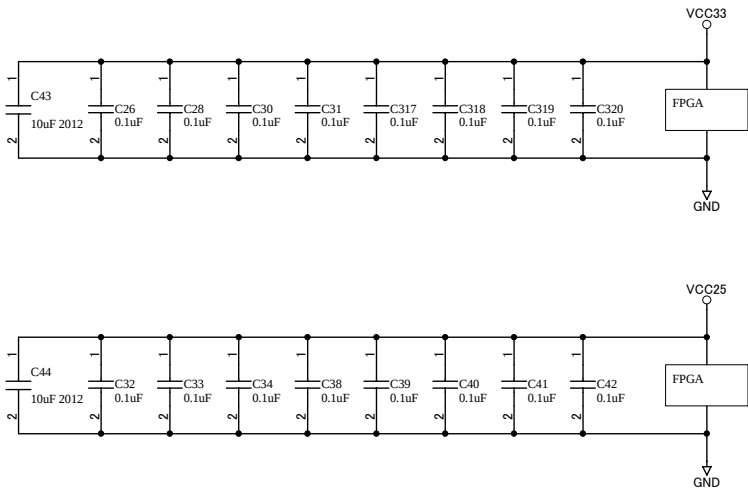
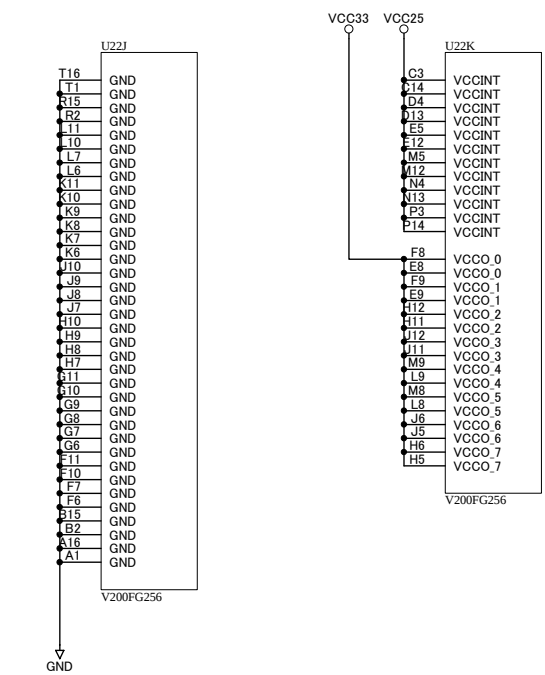


FPGA コンフィグレーション回路



Master-serial mode

M2	M1	M0
1	0	0



U2M