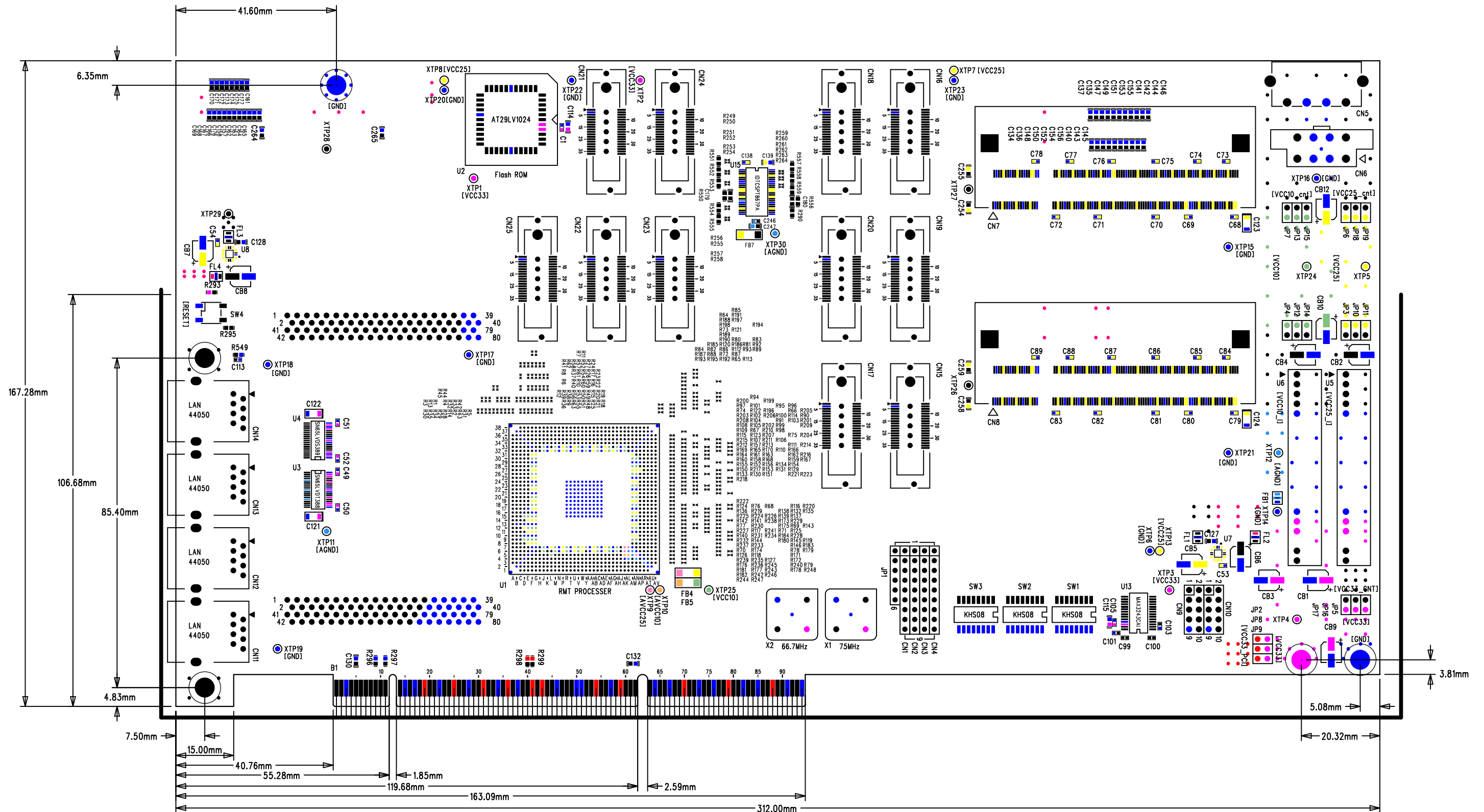


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L1 TOP SILK
L1 ARTWORK

SST001020.PHO
ART001.PHO



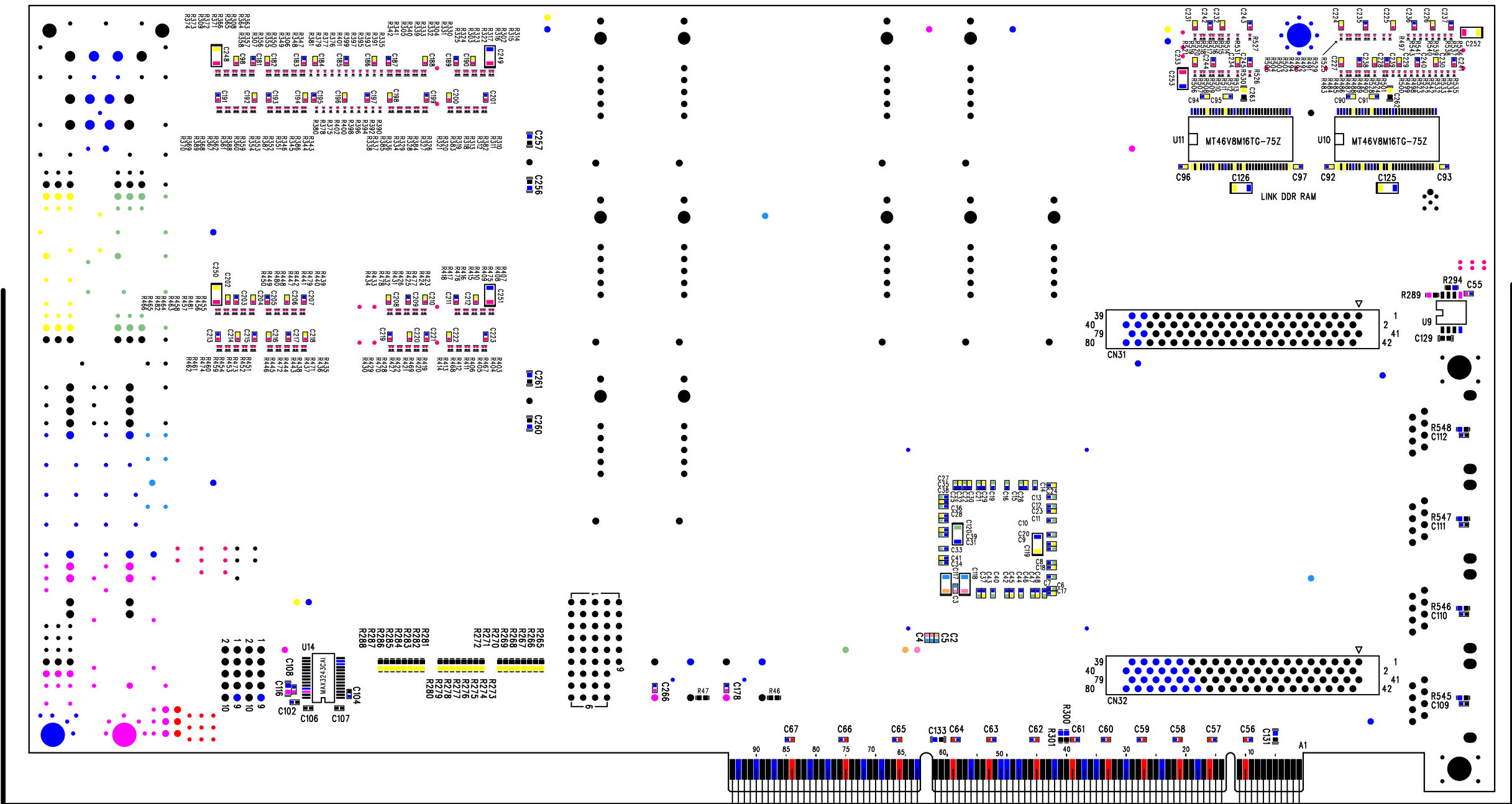
KB04011(RMT PCI EVA BOARD)2ANSEI SYSTEM 5004.05.02

22B015051.PHO

ART015.PHO

L15 BOTTOM SILK

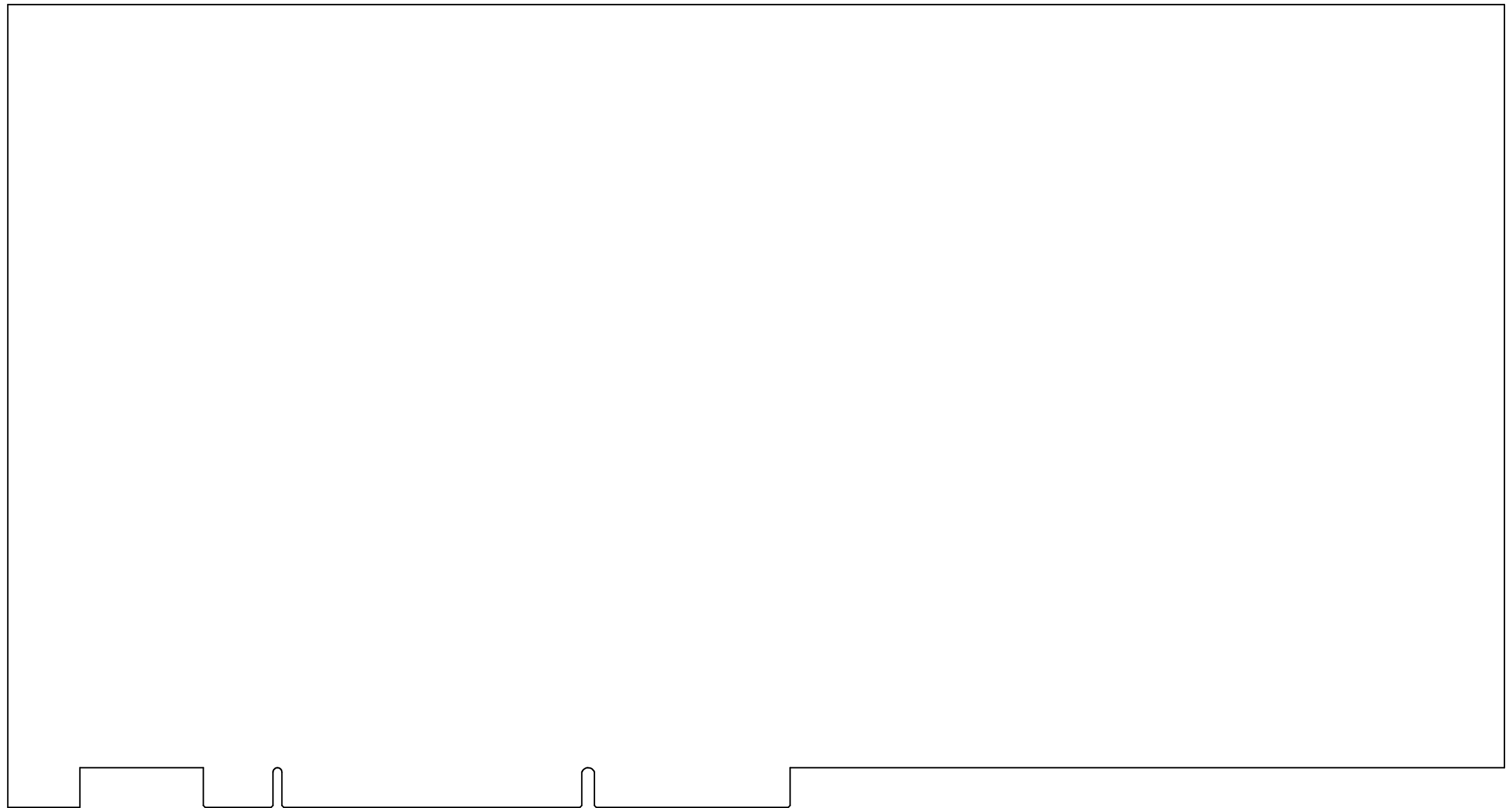
L15 ARTWORK



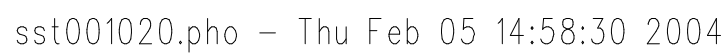
KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

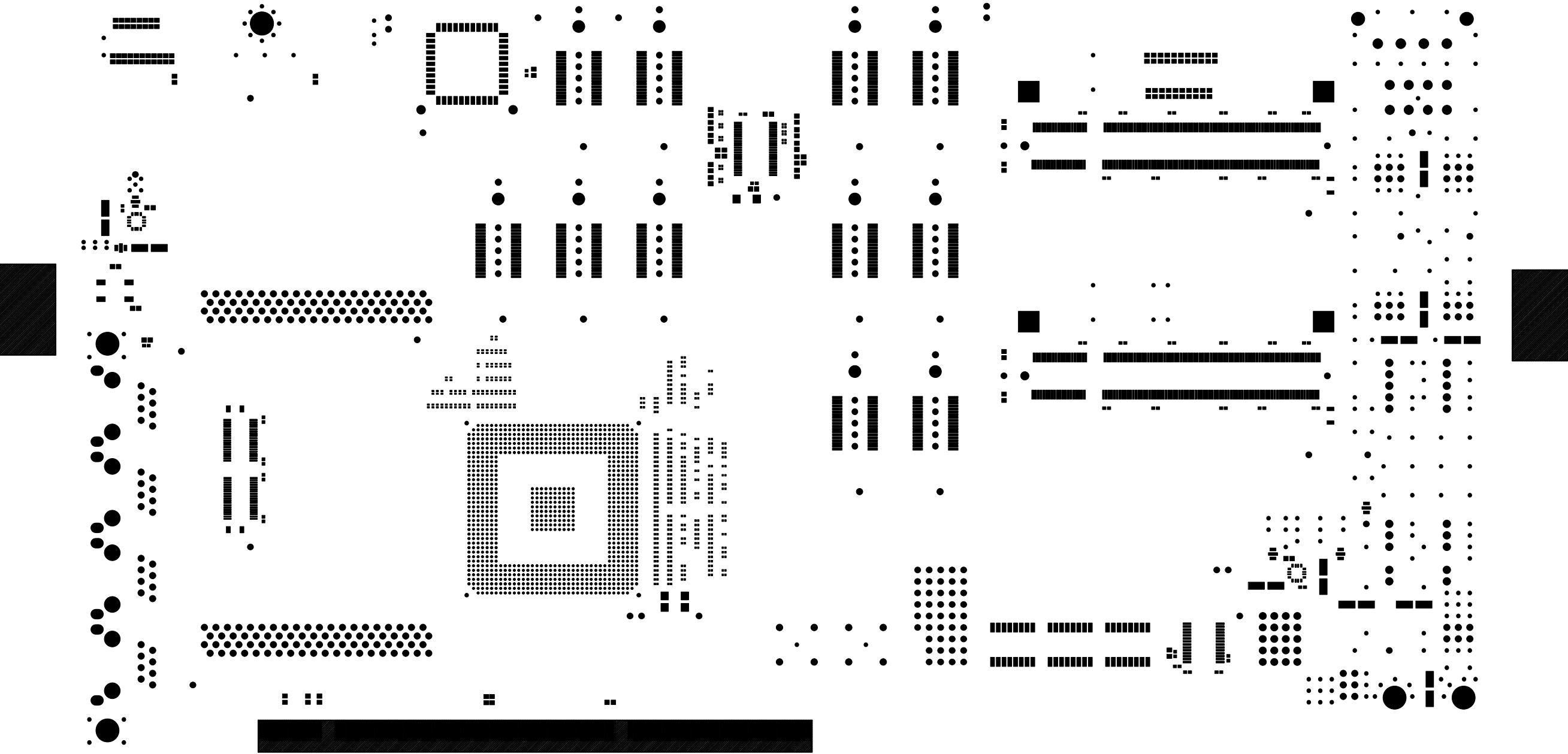
BOARDOUTLINE

GEN030.PHO



SST001020.PH0

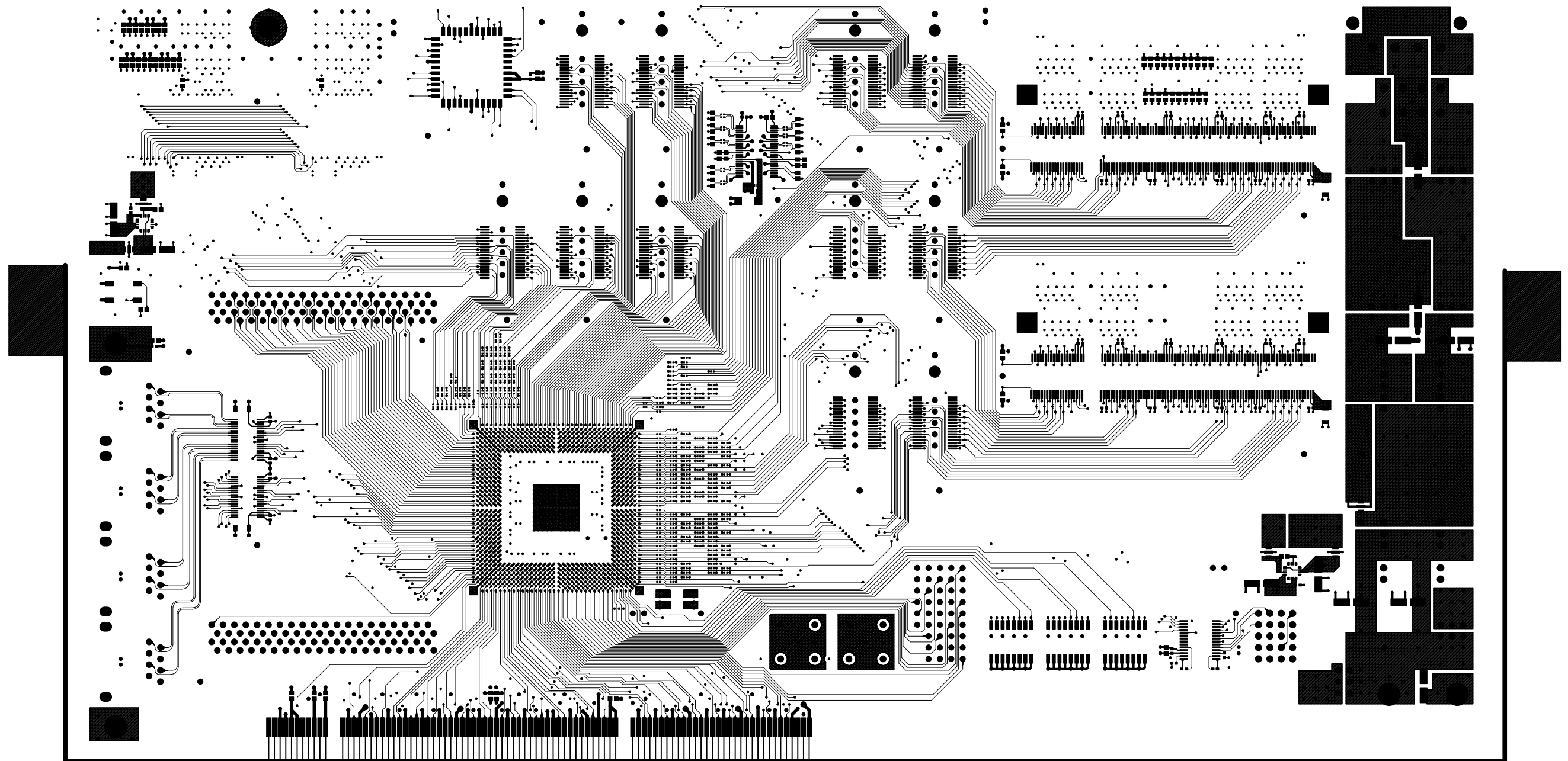




KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L1 ARTWORK

ART001.PHO

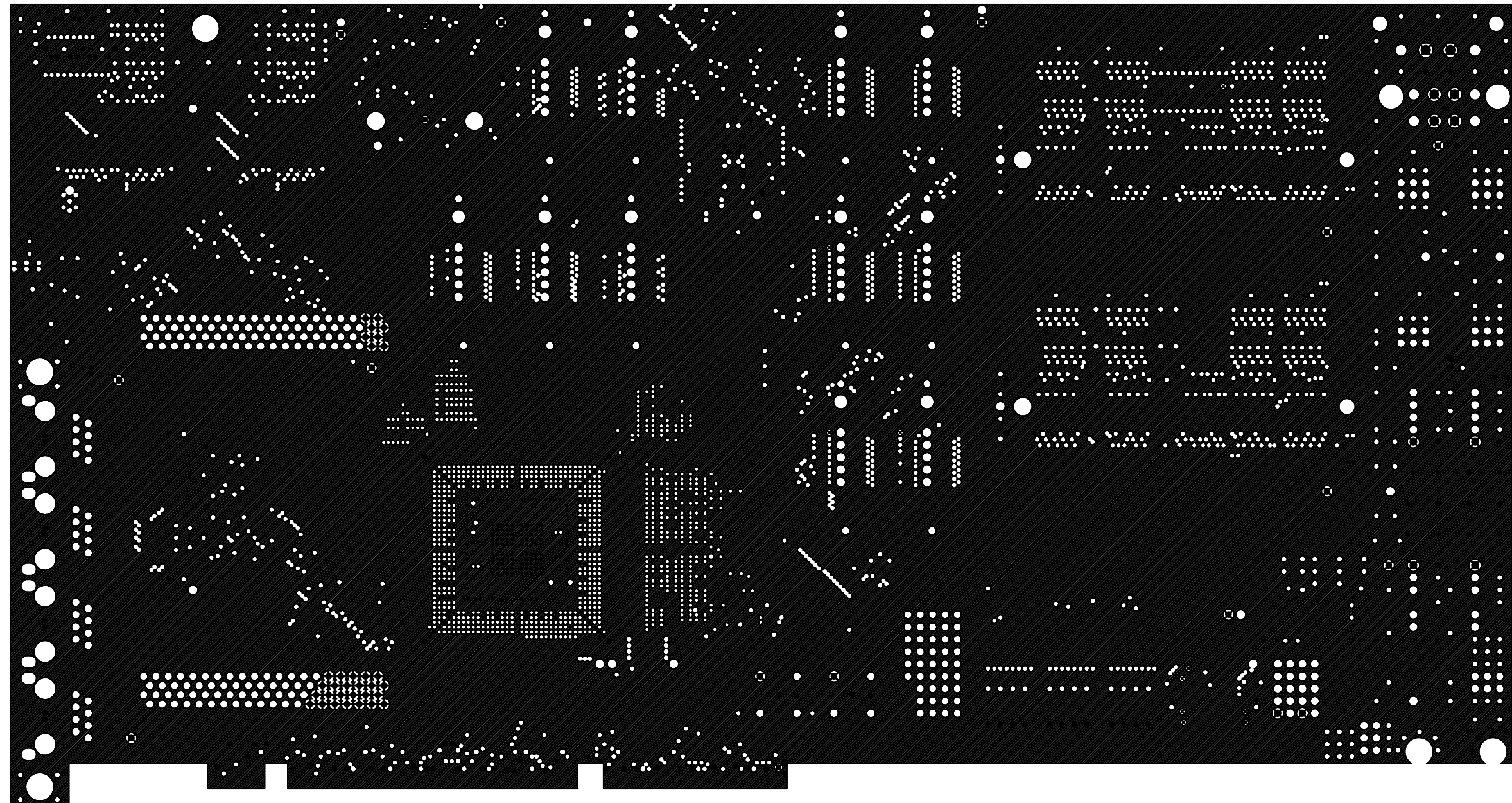


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L2 ARTWORK

ART02.PHO

(GND)

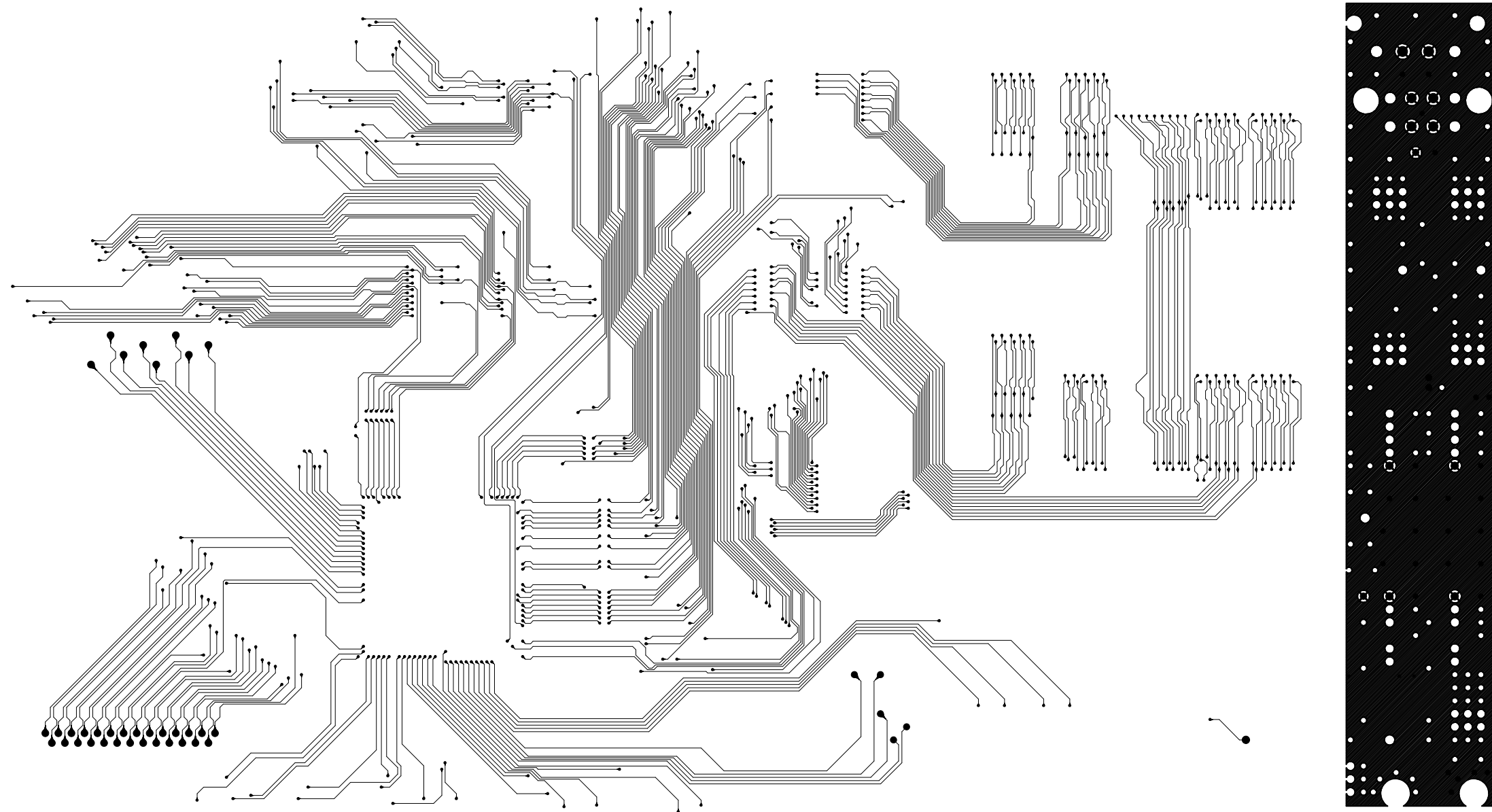


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L3 ARTWORK

ART003.PHO

(電源部GND)

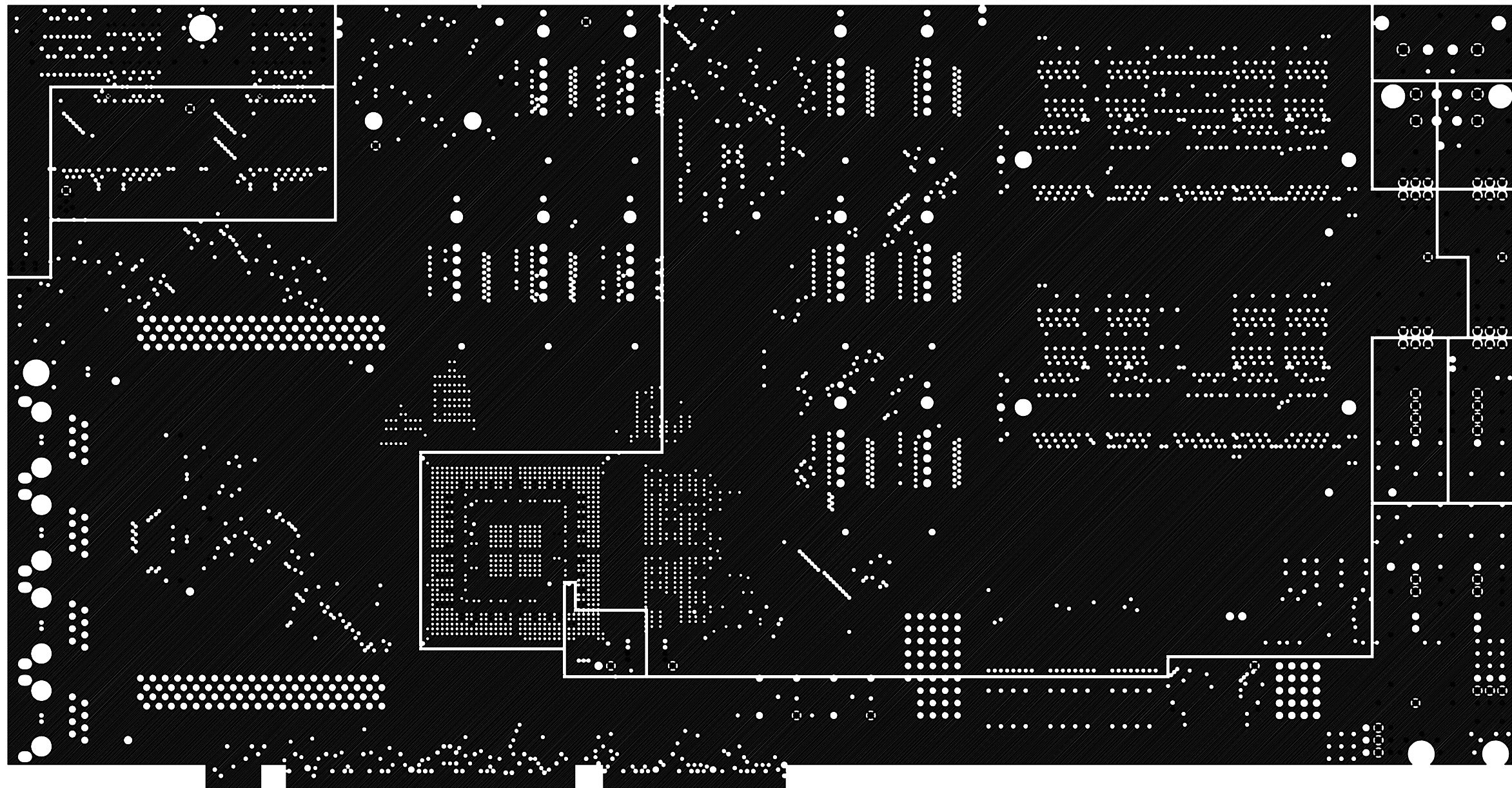


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L4 ARTWORK

ART004.PHO

(VCC33,VCC10,AVCC10,VCC125_T2,VCC125_2)

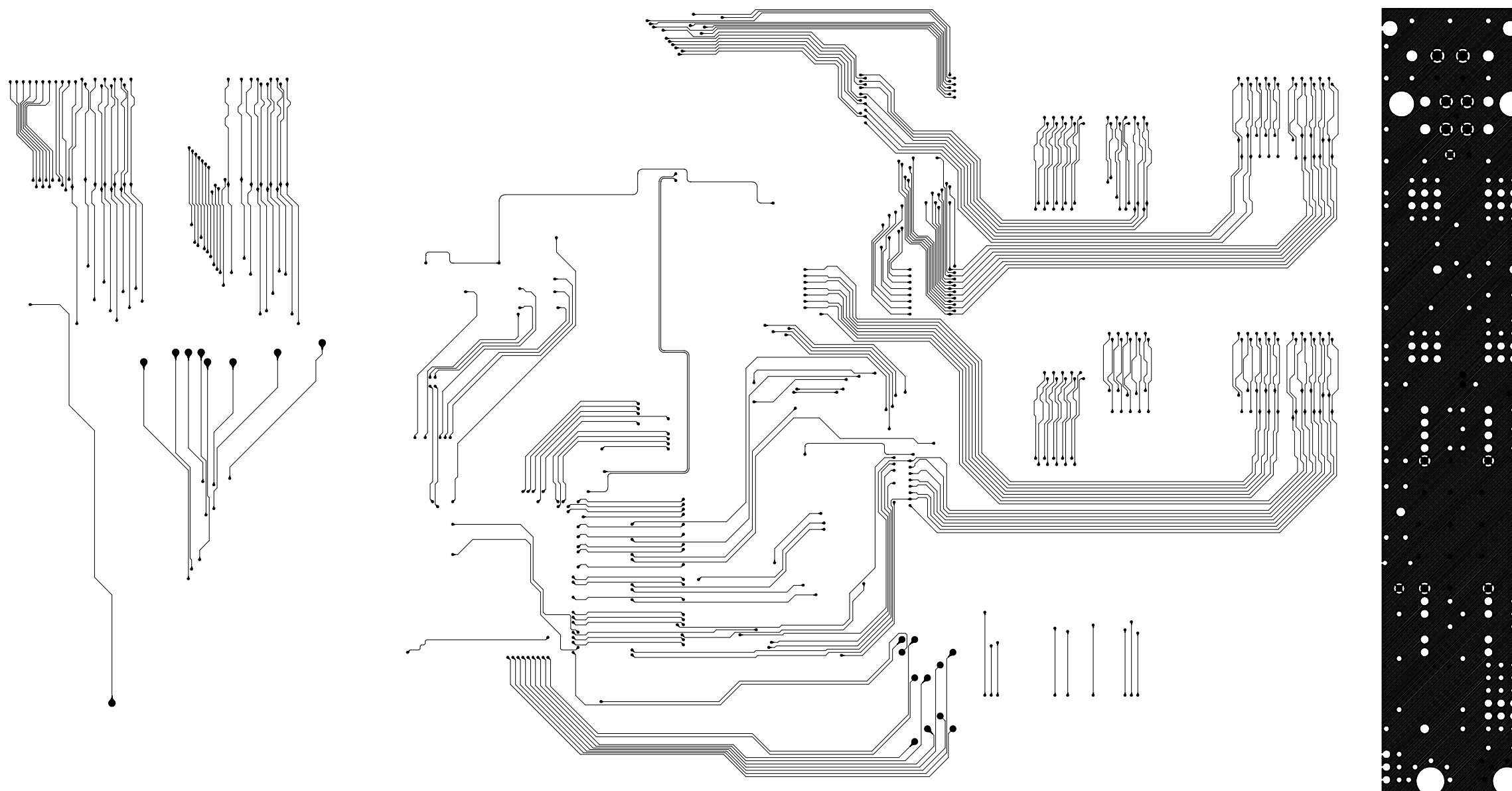


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L5 ARTWORK

ART005.PHO

(電源部GND)

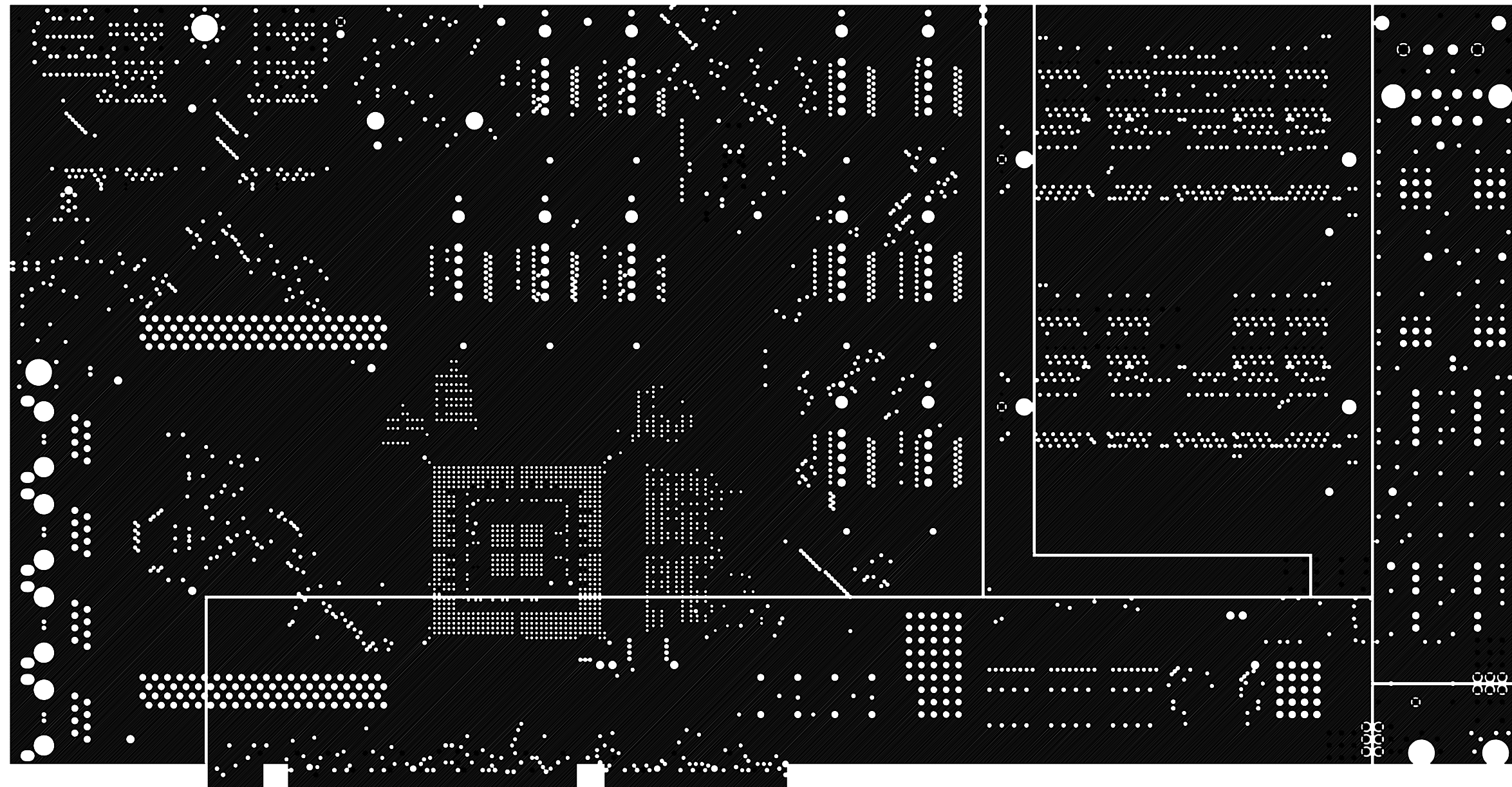


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L6 ARTWORK

ART006.PHO

(VCC33_PCI,VCC25,VCC125_T,VCC125_1)

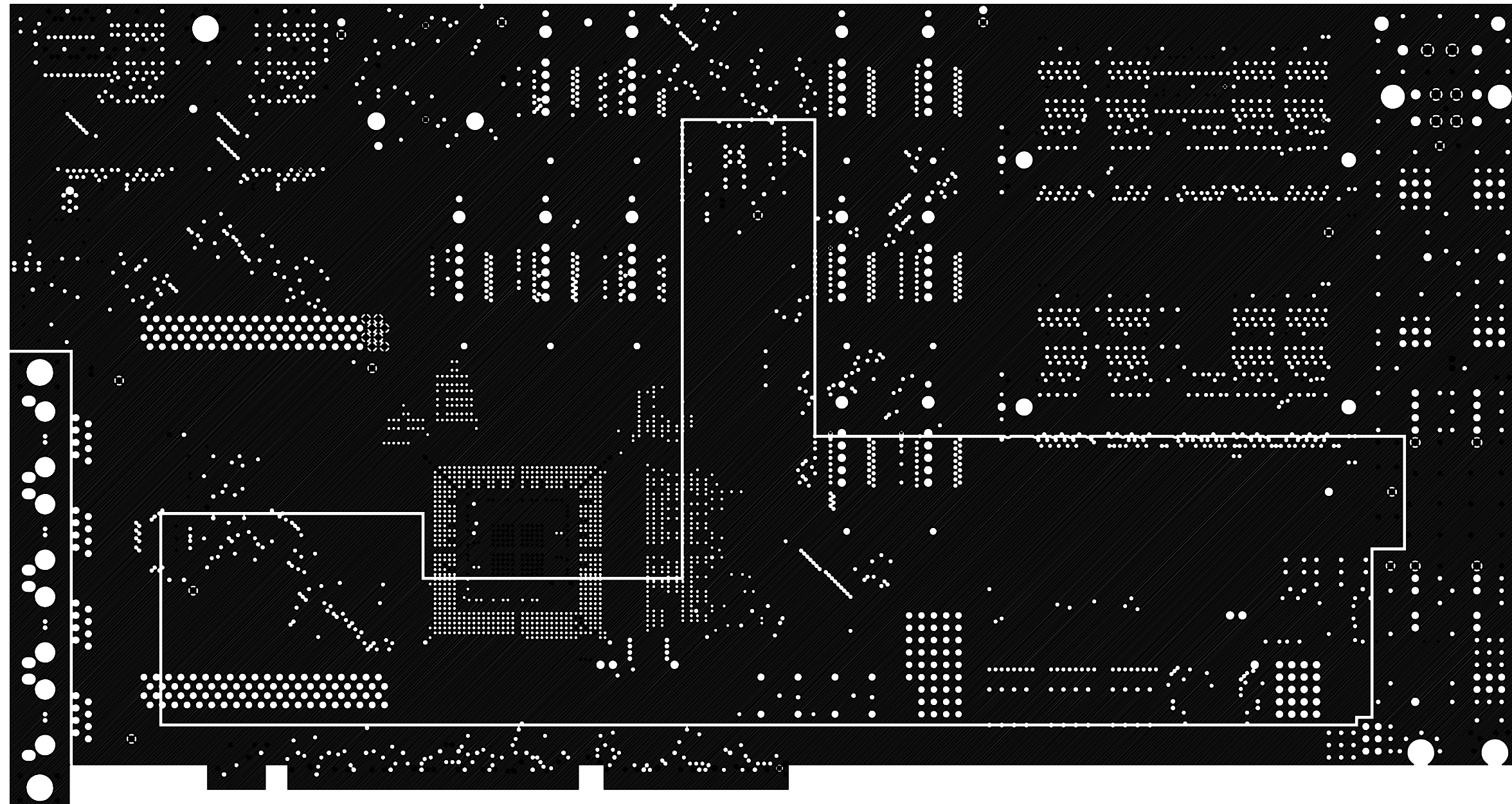


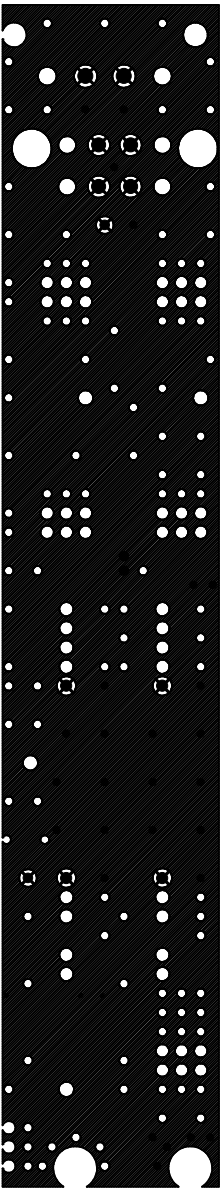
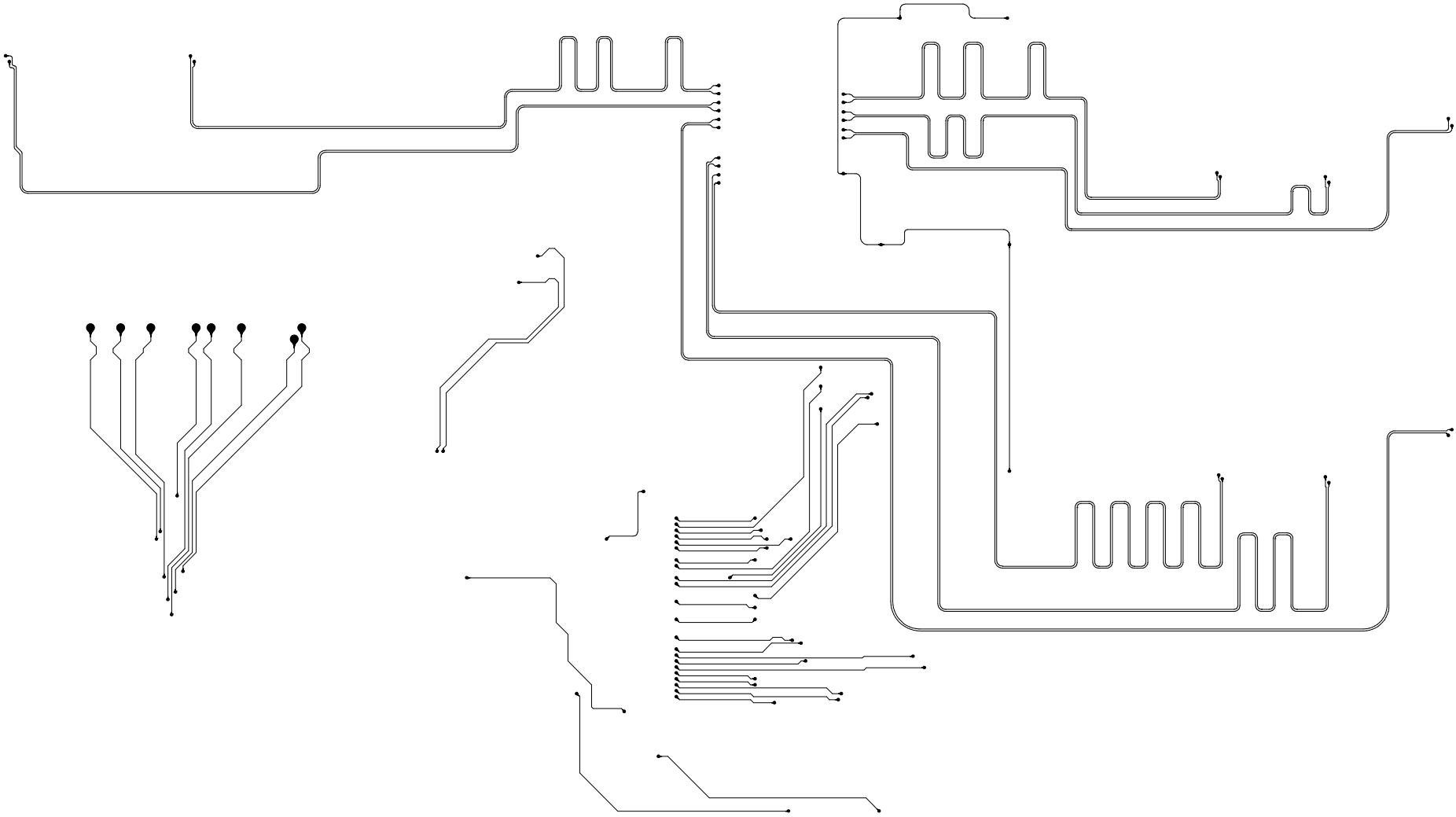
KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L7 ARTWORK

ART07.PHO

(GND,AGND)



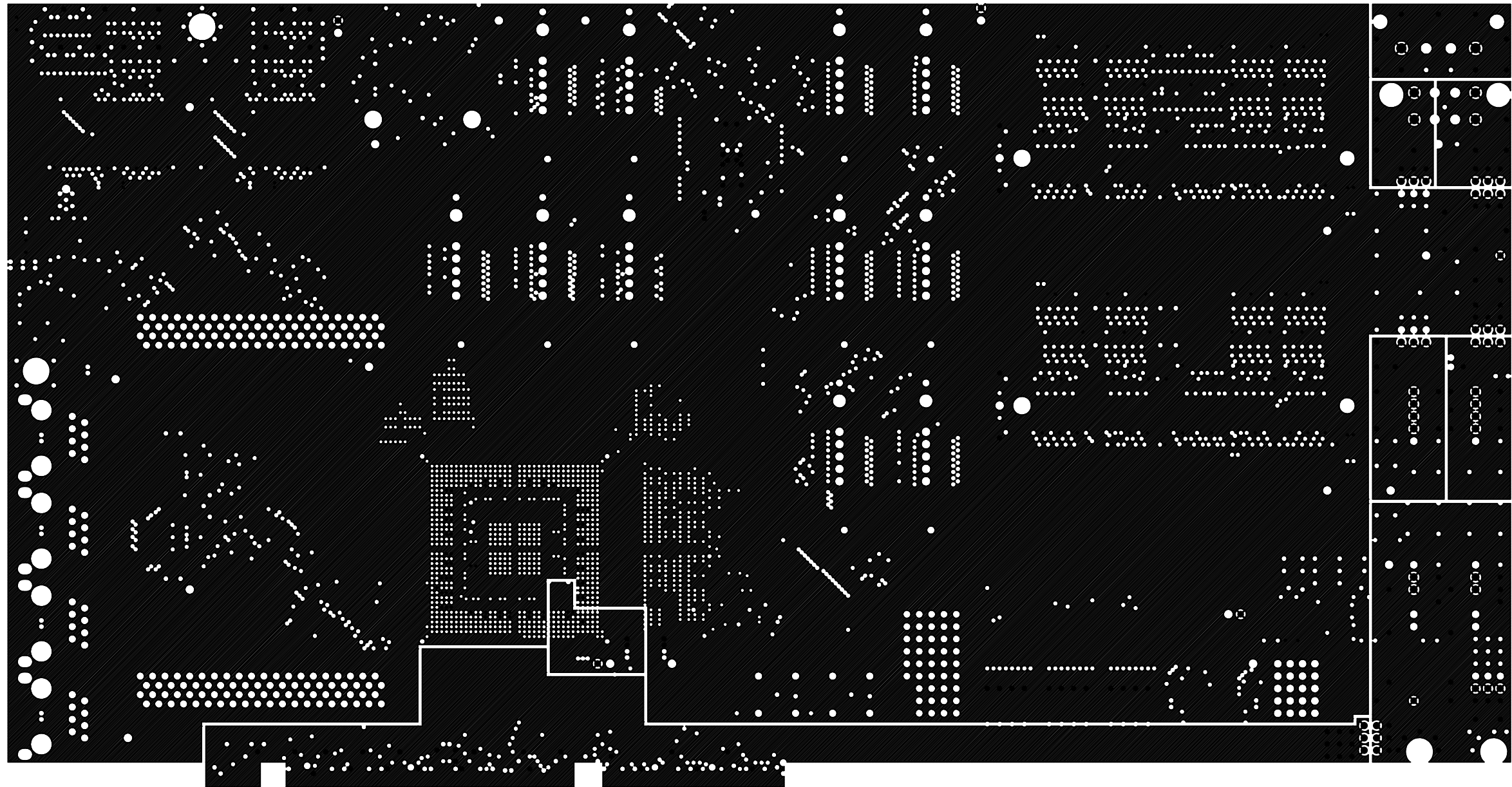


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L9 ARTWORK

ART009.PHO

(VCC33_PCI,VCC25,AVCC25)

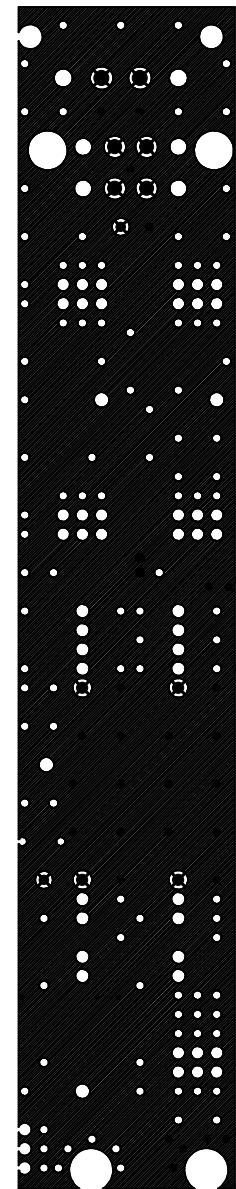
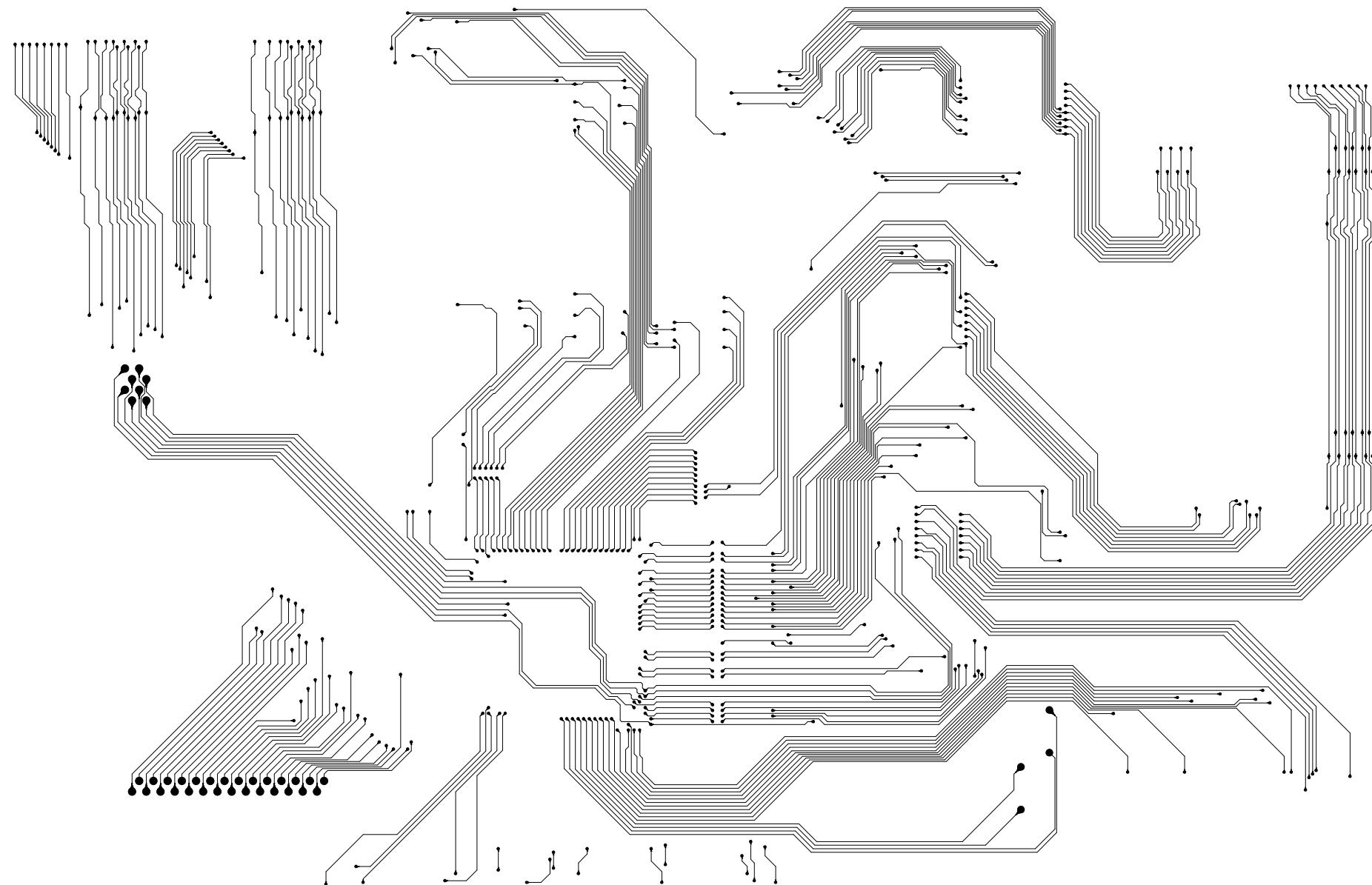


KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

L10 ARTWORK

ART010.PHO

(電源部GND)

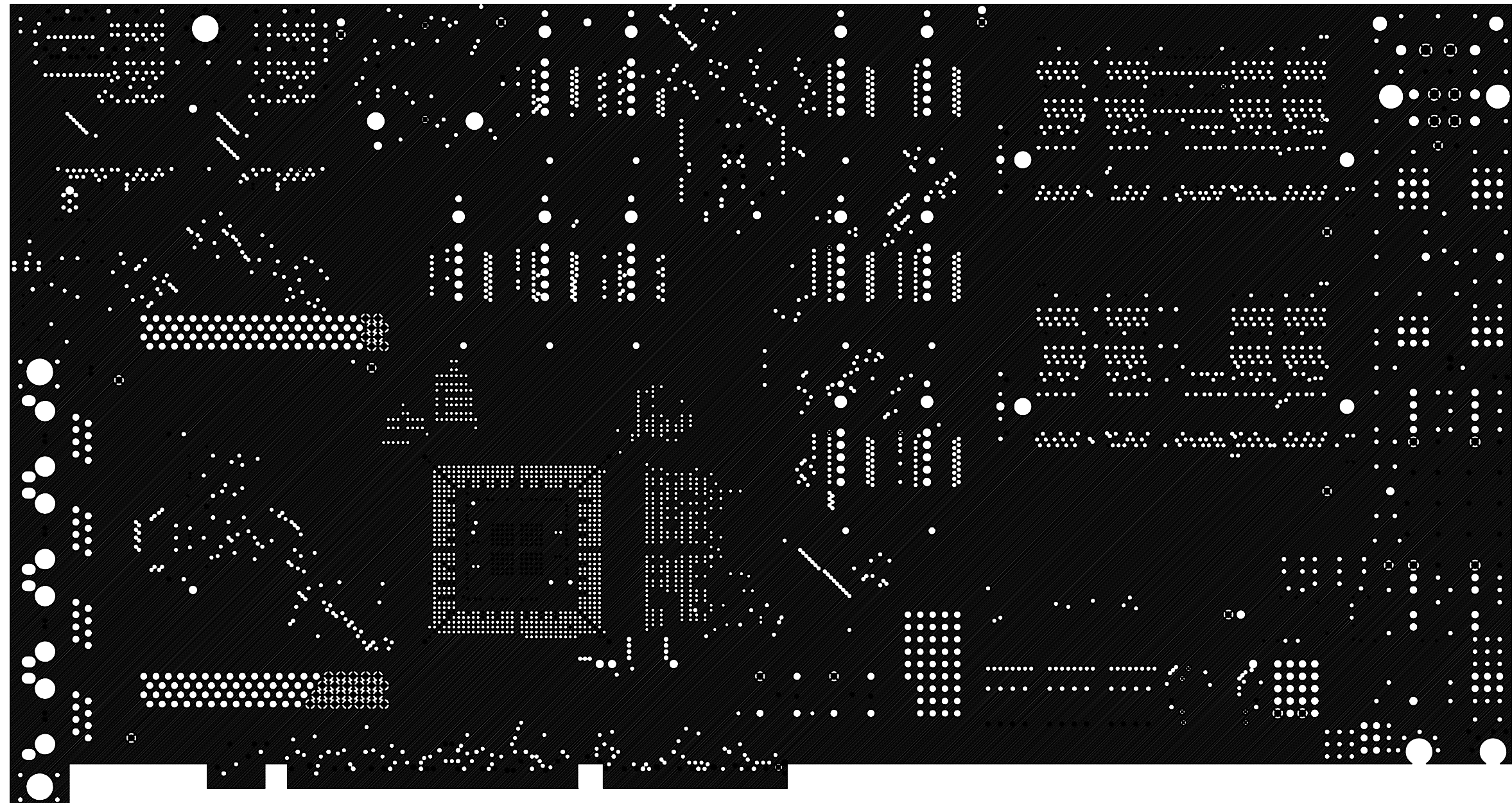


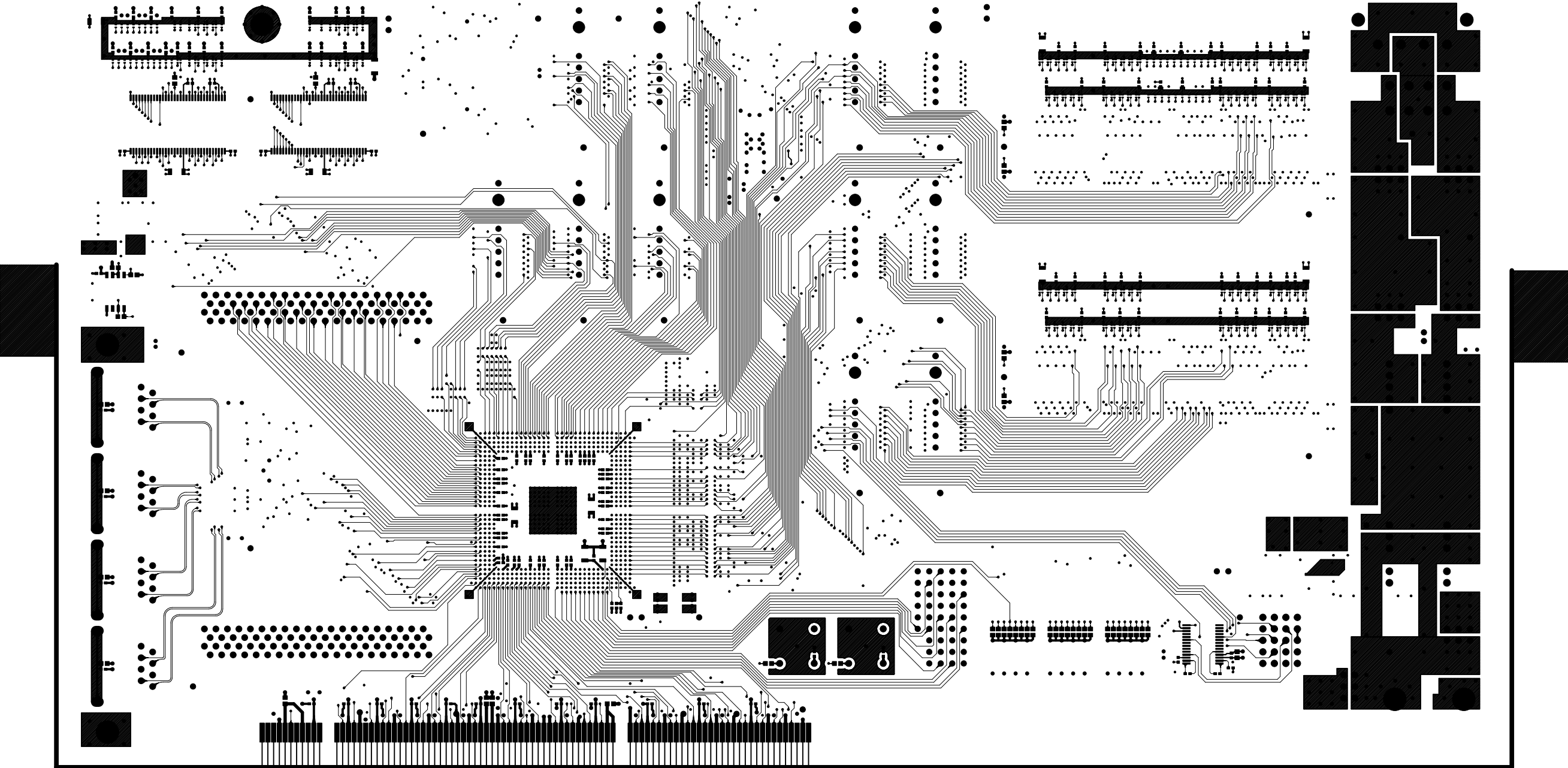
KB04011(RMT PCI EVA BOARD)SANSEI SYSTEM 2004.02.05

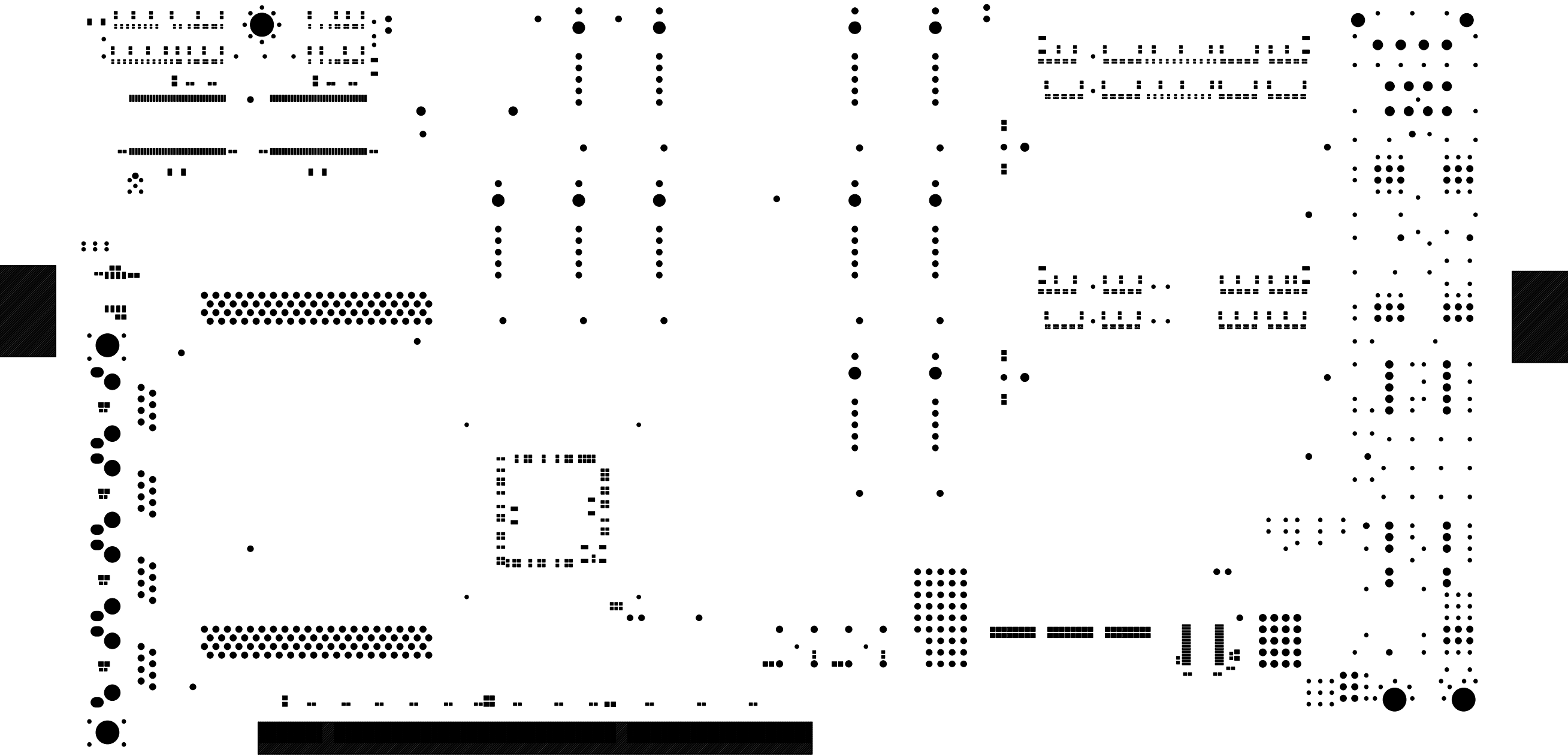
L11 ARTWORK

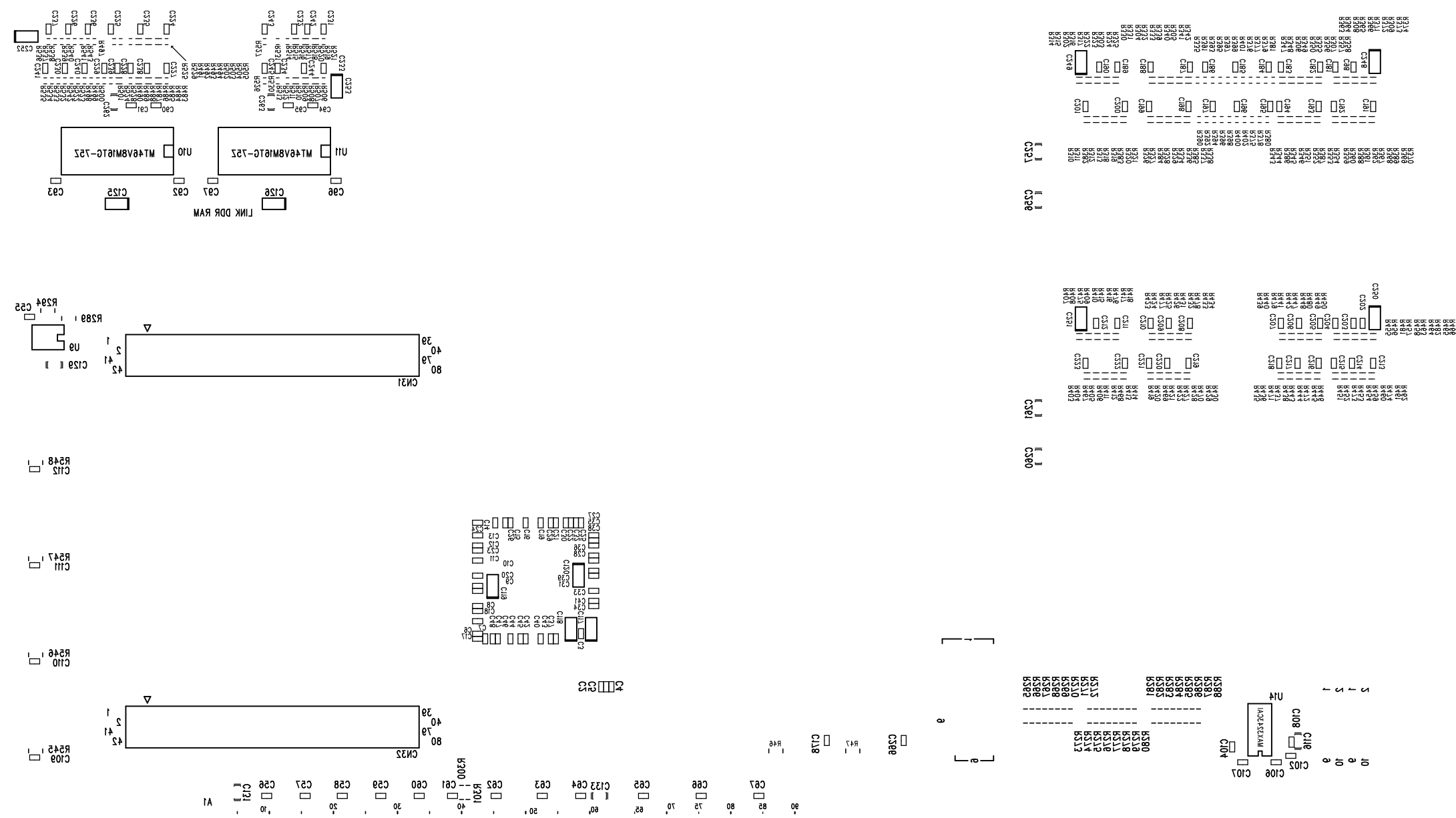
ART11.PHO

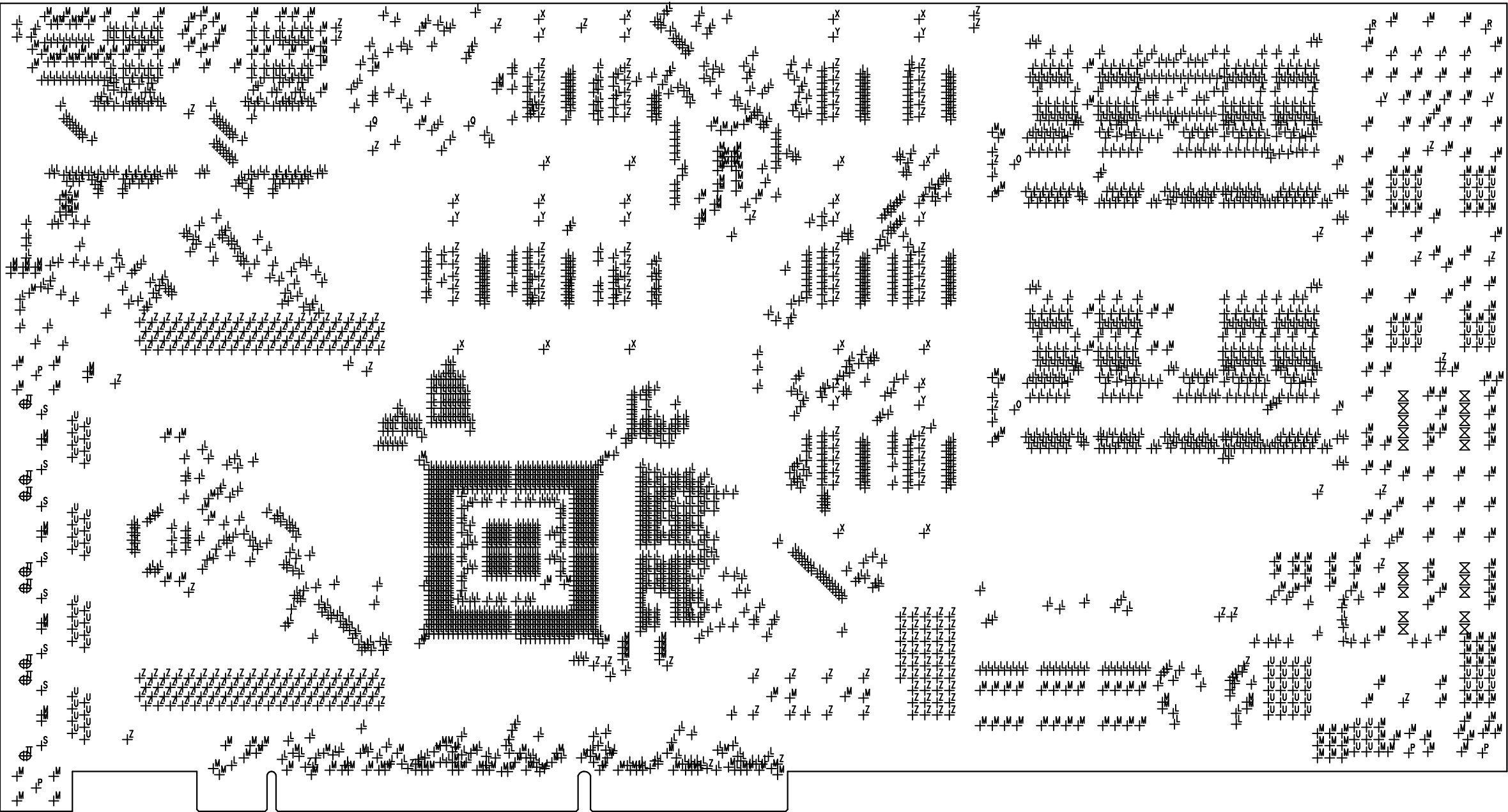
(GND)











寸法	個数	記号	PLTD
40	20	⊗	PLTD
60	4	A	PLTD
12	2914	L	PLTD
16	396	M	PLTD
43	2	N	NPLTD
63	2	O	NPLTD
138	5	P	PLTD
67	2	Q	PLTD
91	2	R	PLTD
128	8	S	PLTD
62.99	16	T	PLTD
36	88	U	PLTD
118	2	V	PLTD
55	8	W	PLTD
30	22	X	NPLTD
78	11	Y	PLTD
32	302	Z	PLTD
62.99 x 87.8	8	T	PLTD