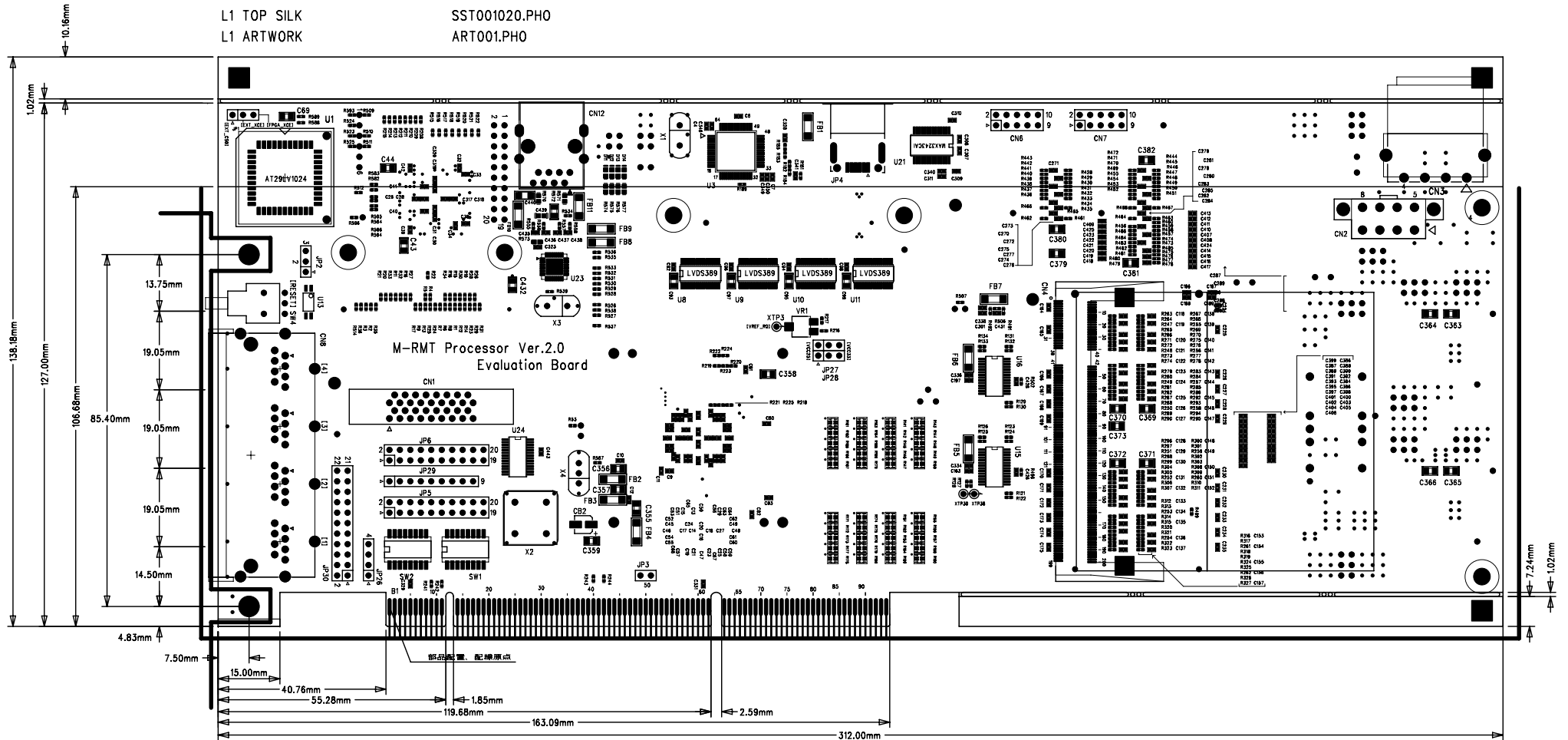


073-070301-31(MRMT Ver2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L1 TOP SILK
L1 ARTWORK

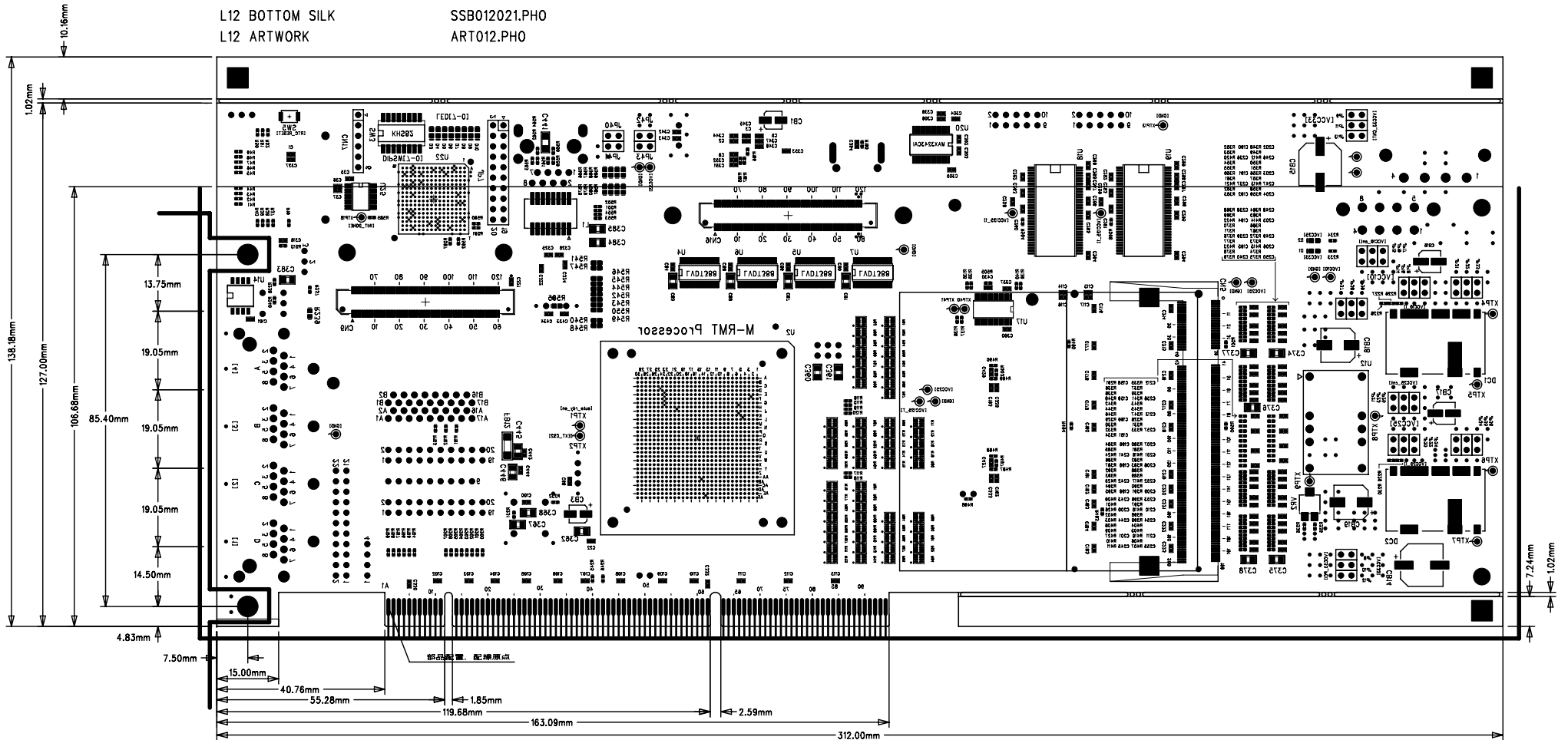
SST001020.PHO
ART001.PHO



073-070301-31(MRMT PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

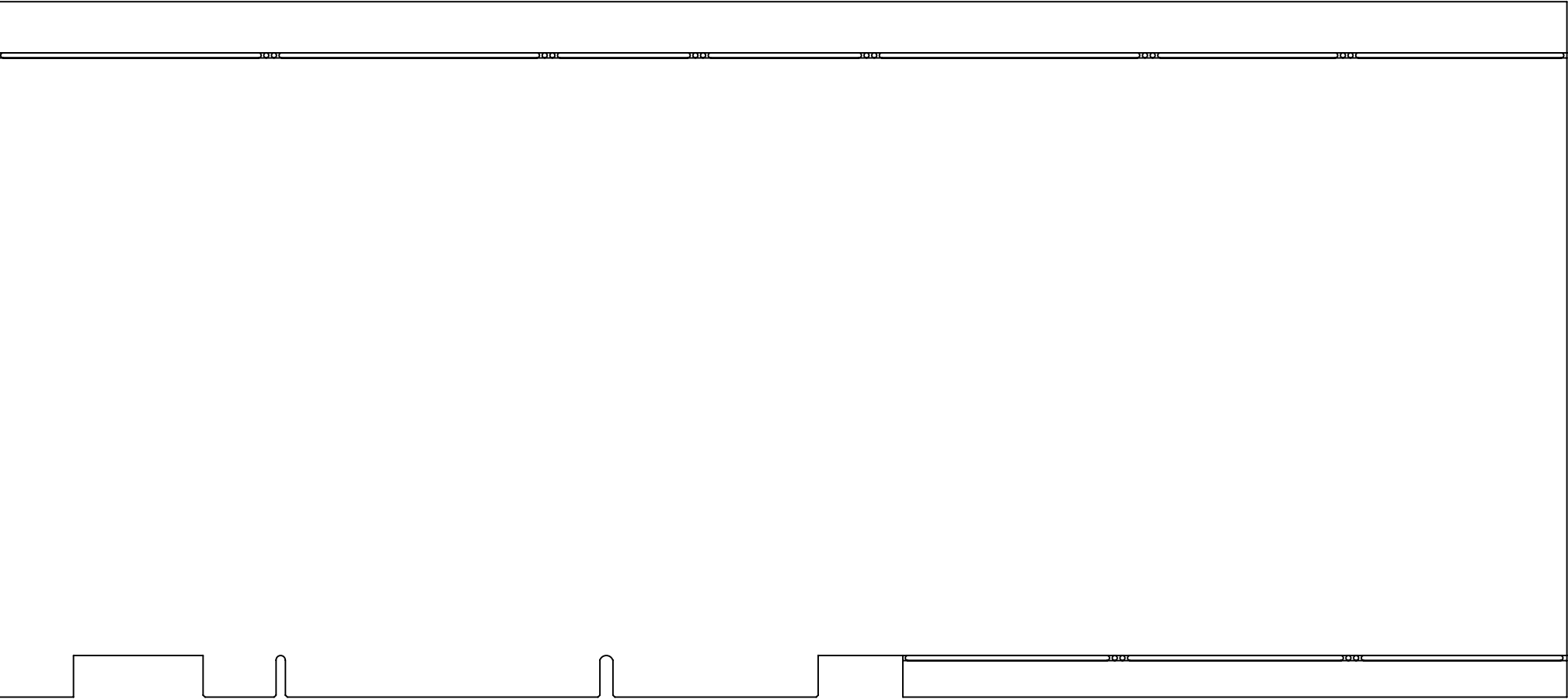
L12 BOTTOM SILK
L12 ARTWORK

SSB012021.PHO
ART012.PHO



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

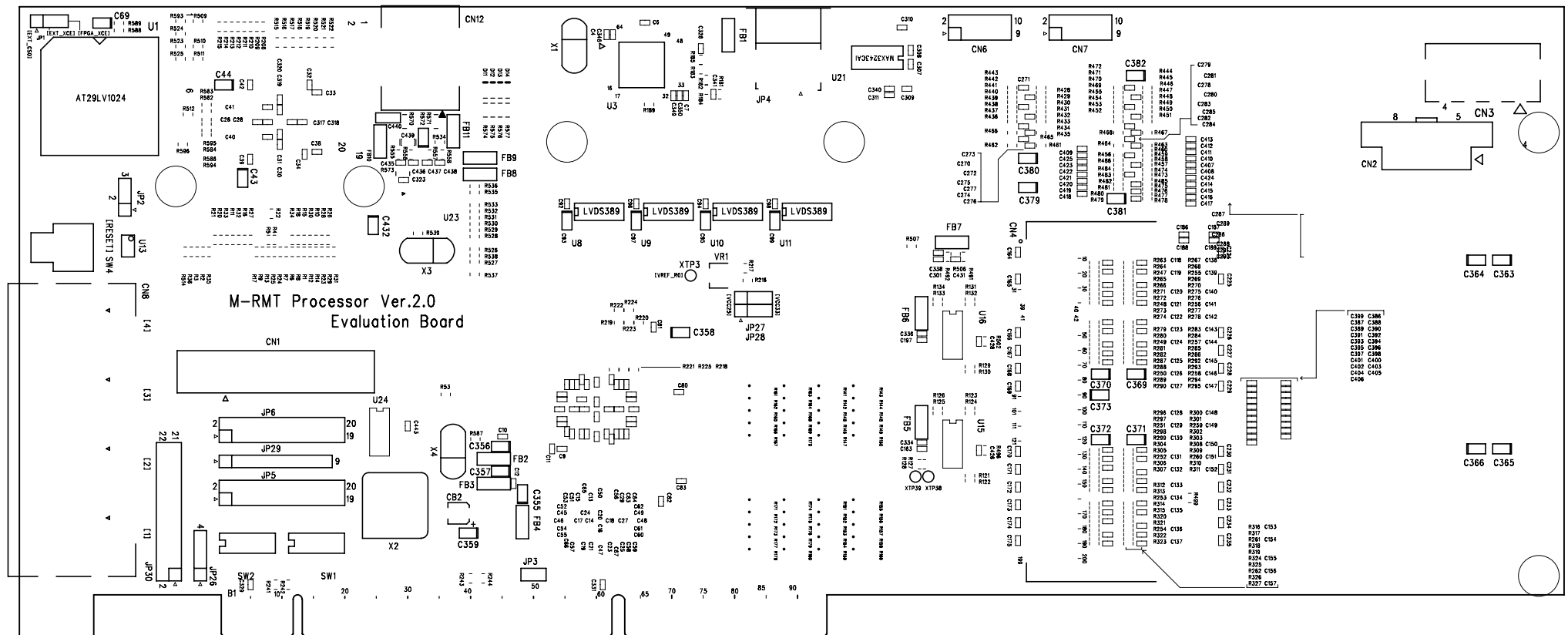
BOARDOUTLINE GEN030.PHO



073-070301-31(MRMP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L1 TOP SILK

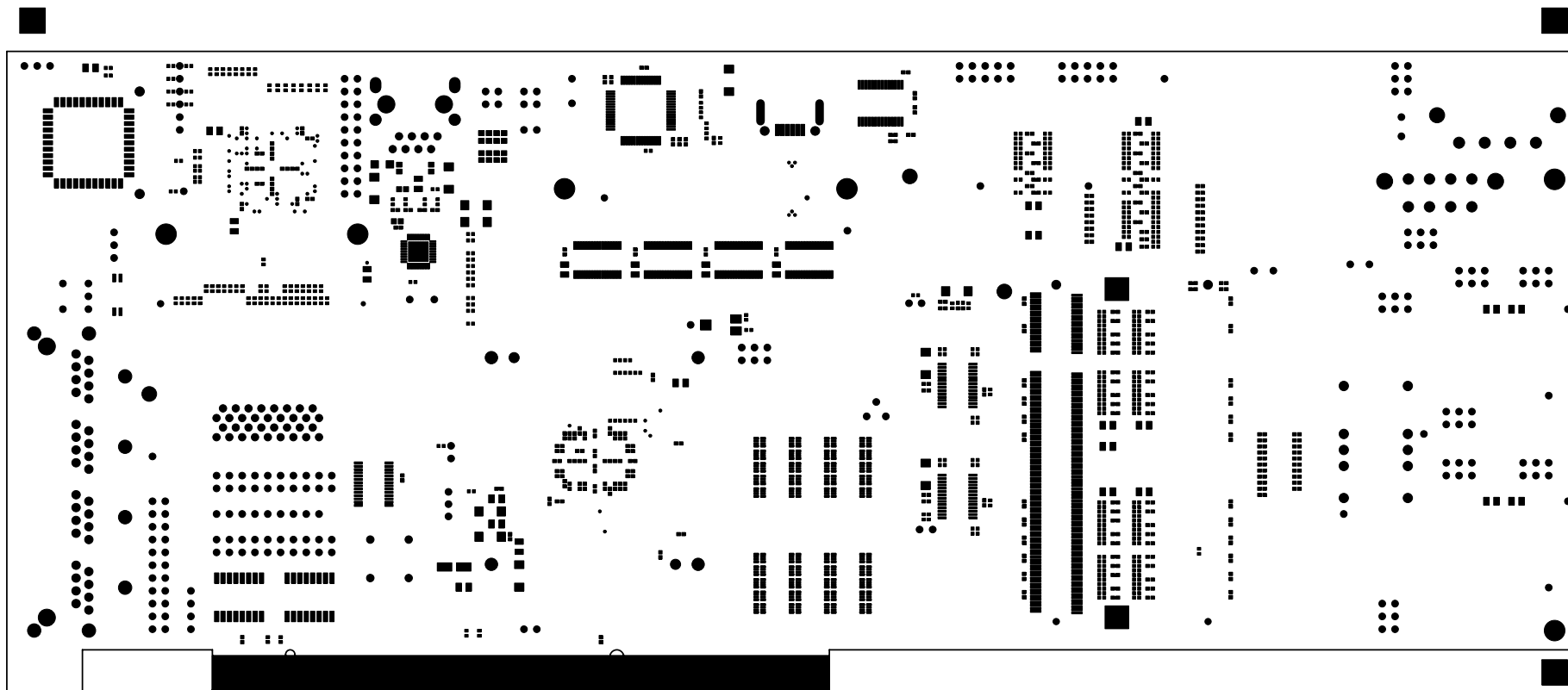
SST001020.PHO



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L1 SOLDERMASK(TOP)

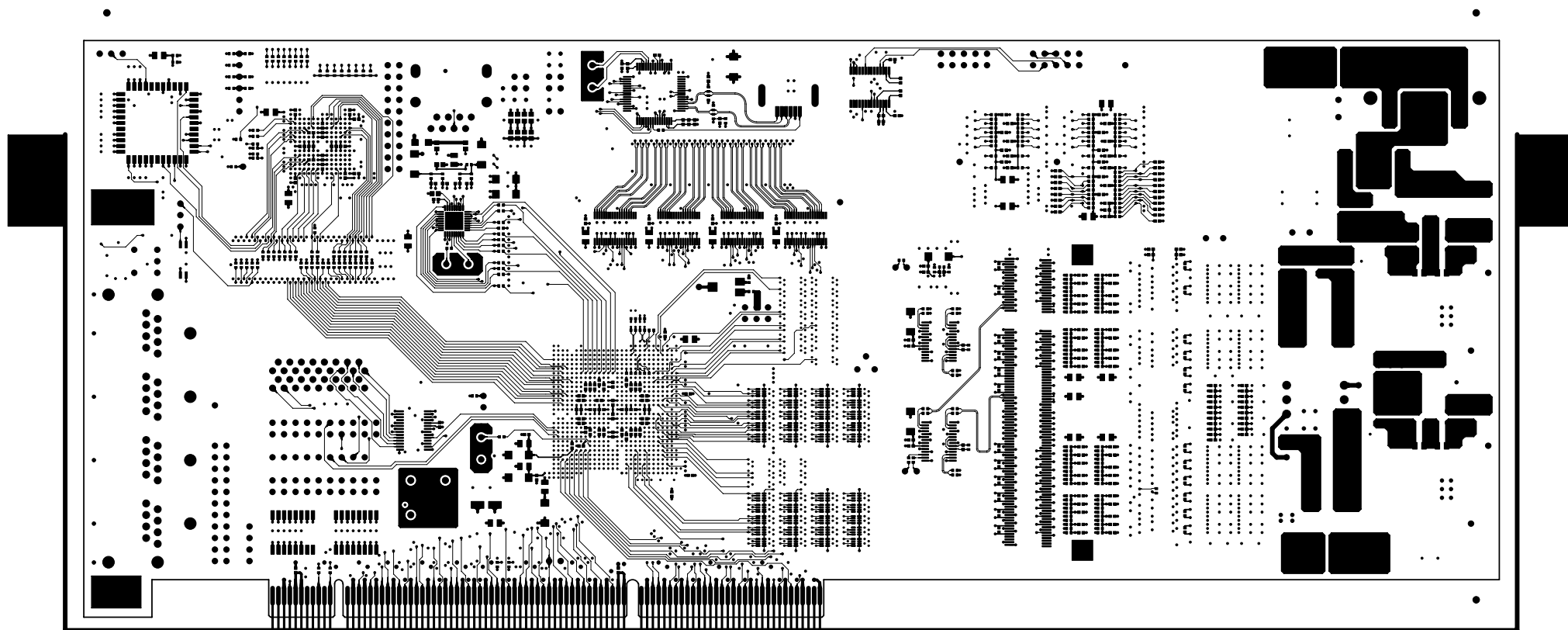
SM001024.PHO



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L1 ARTWORK

ART001.PHO

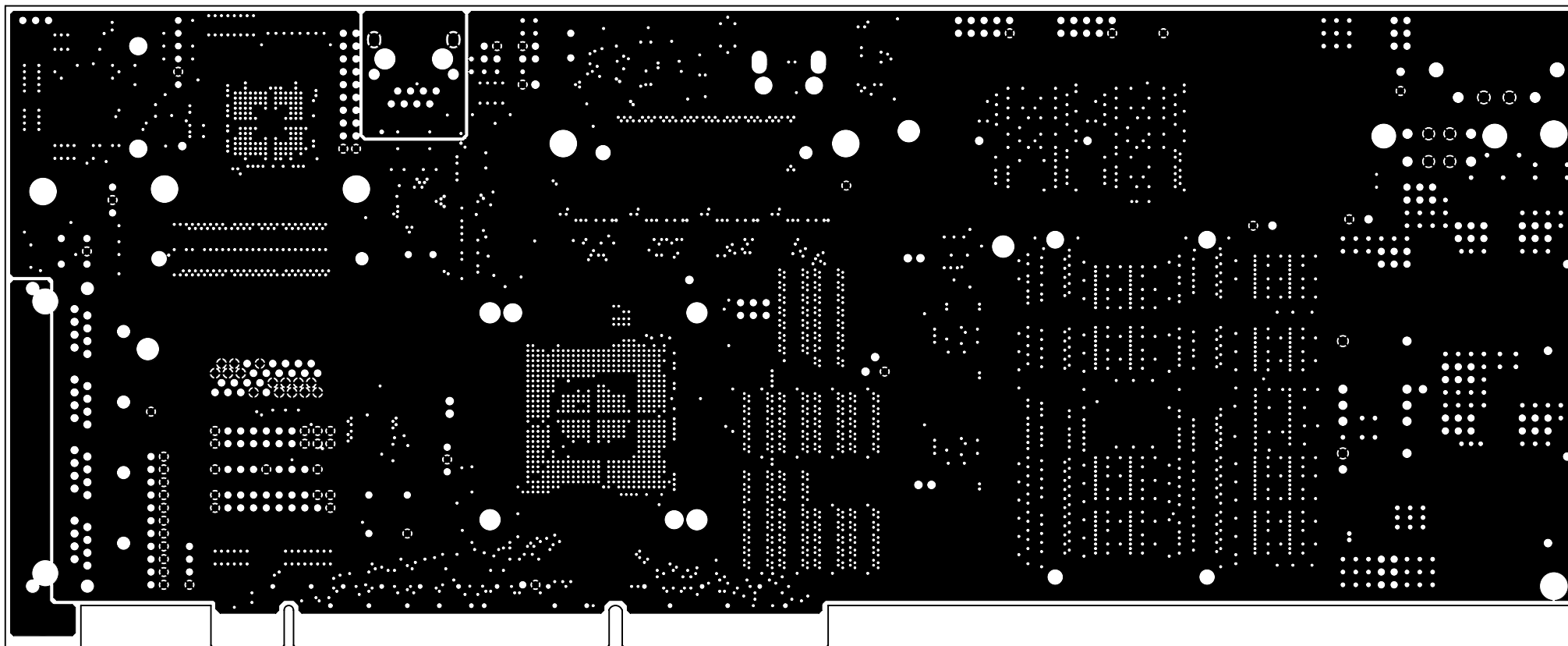


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L2 ARTWORK

ART02.PHO

(GND)

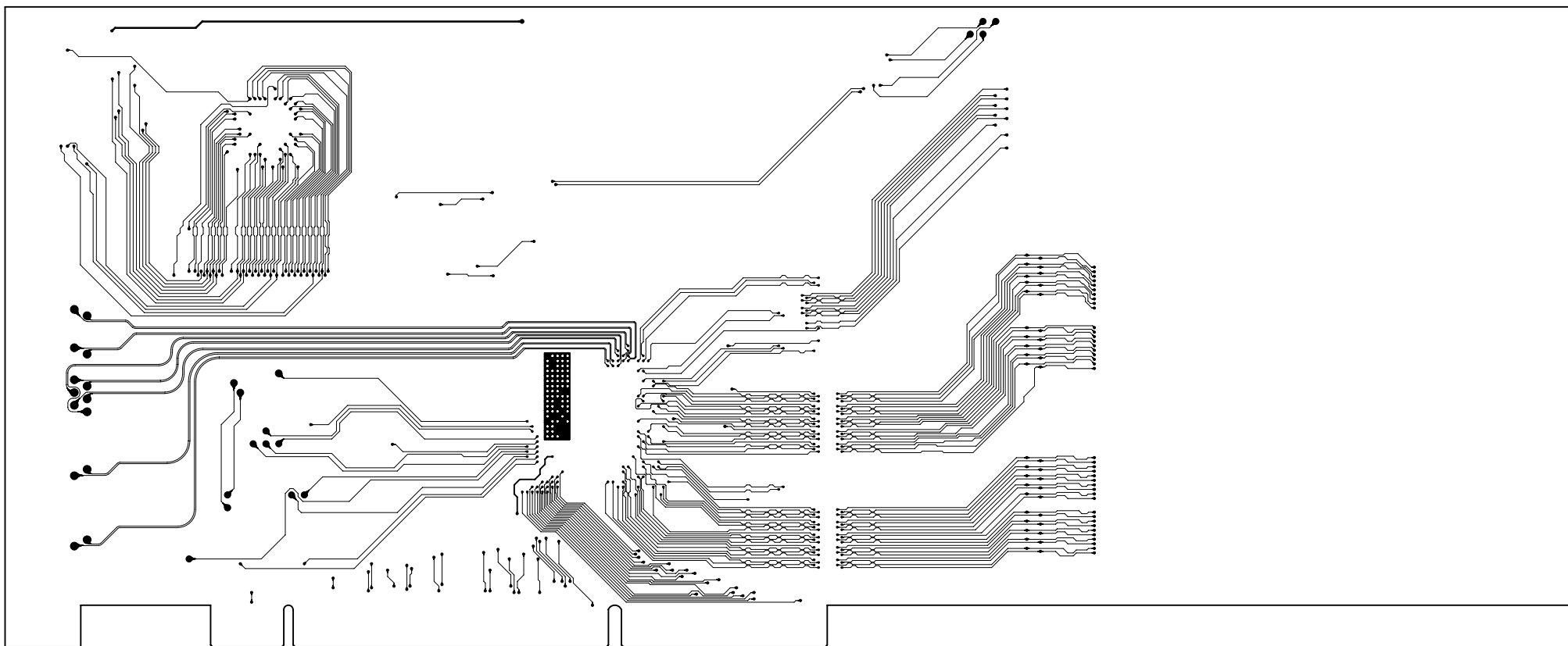


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L3 ARTWORK

ART003.PHO

(BAT_VCC10)

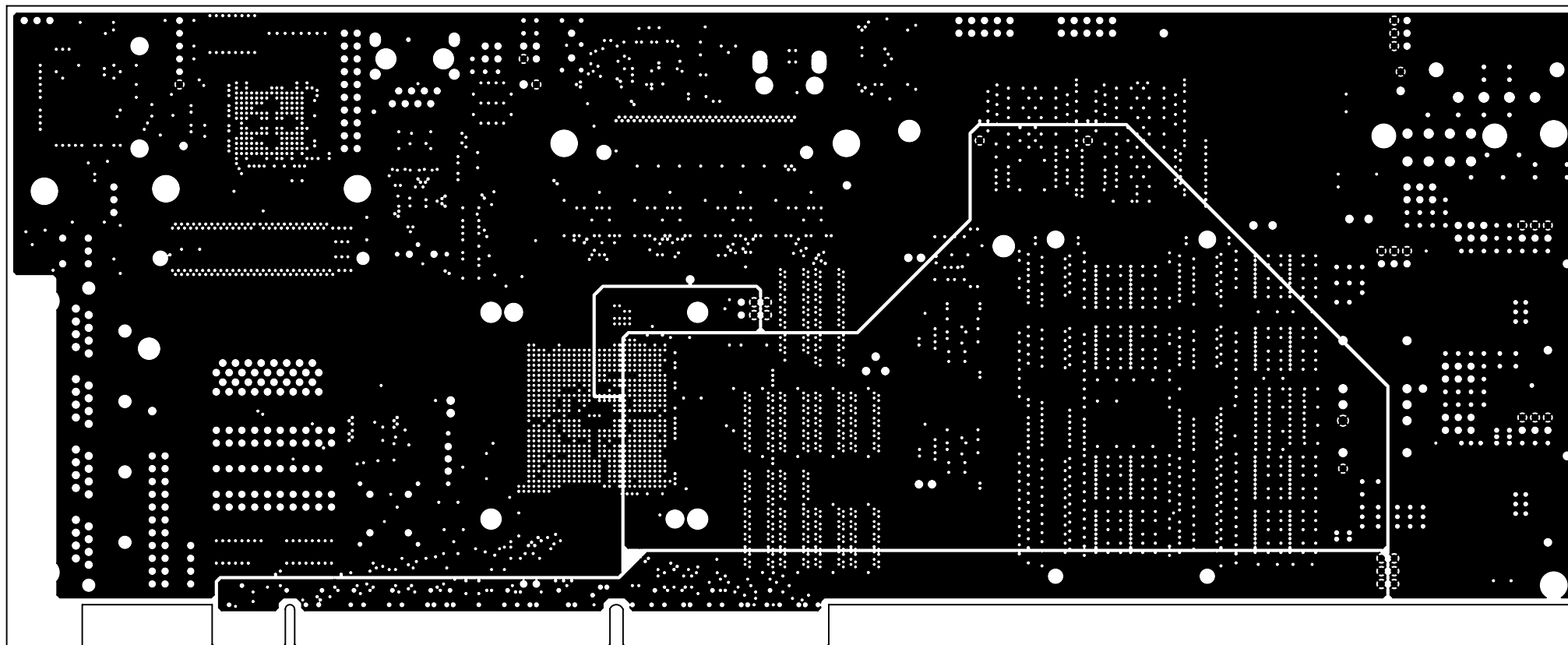


073-070301-31(MRMTF VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L4 ARTWORK

ART004.PHO

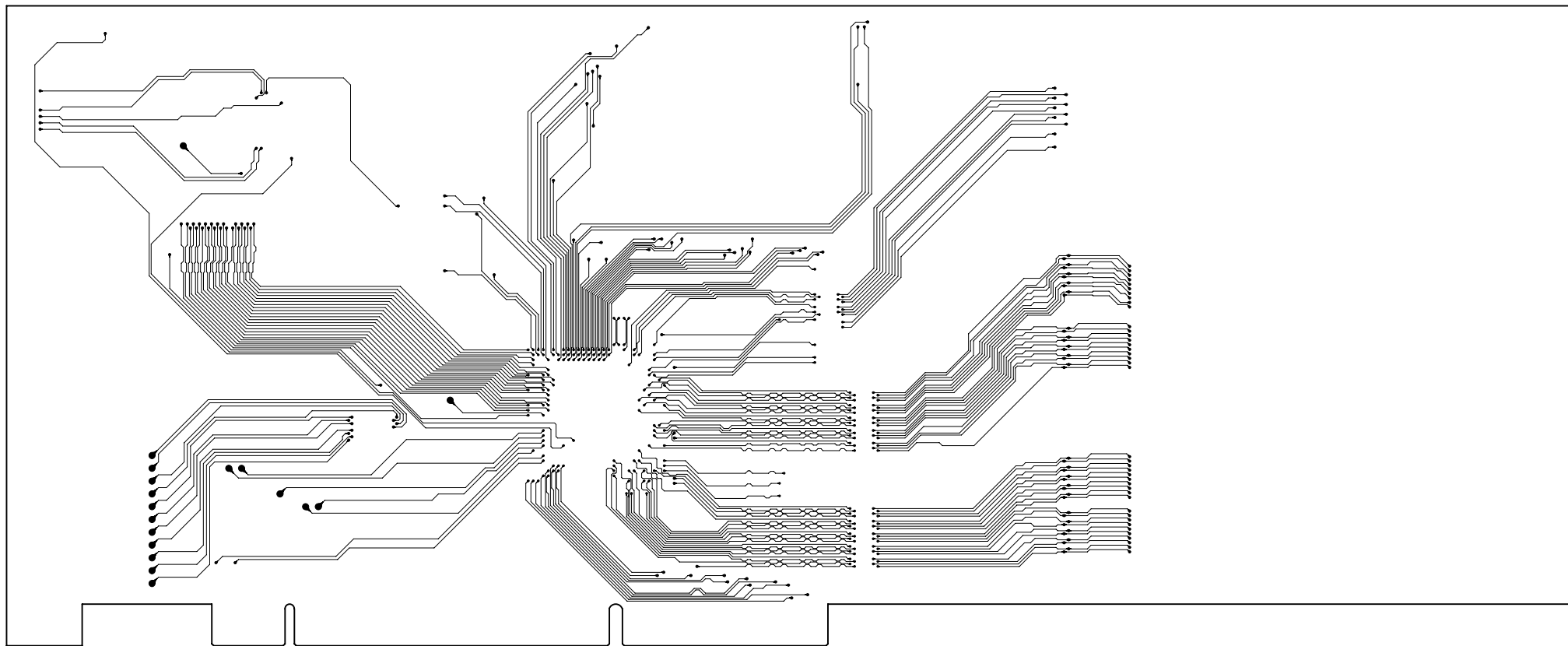
(,VCC33, VCC33_PCI, VCC125_1, VDD_LVDS)



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L5 ARTWORK

ART005.PHO

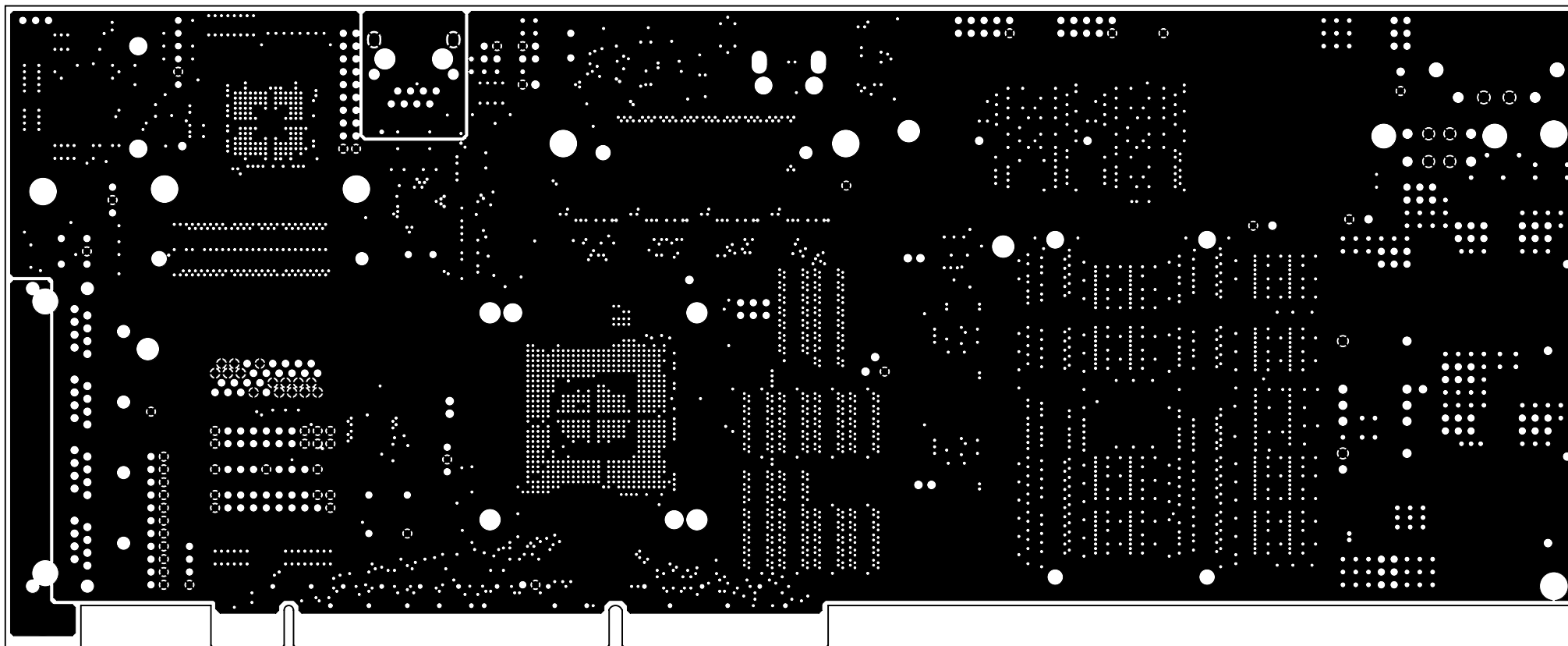


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L6 ARTWORK

ART006.PHO

(GND)

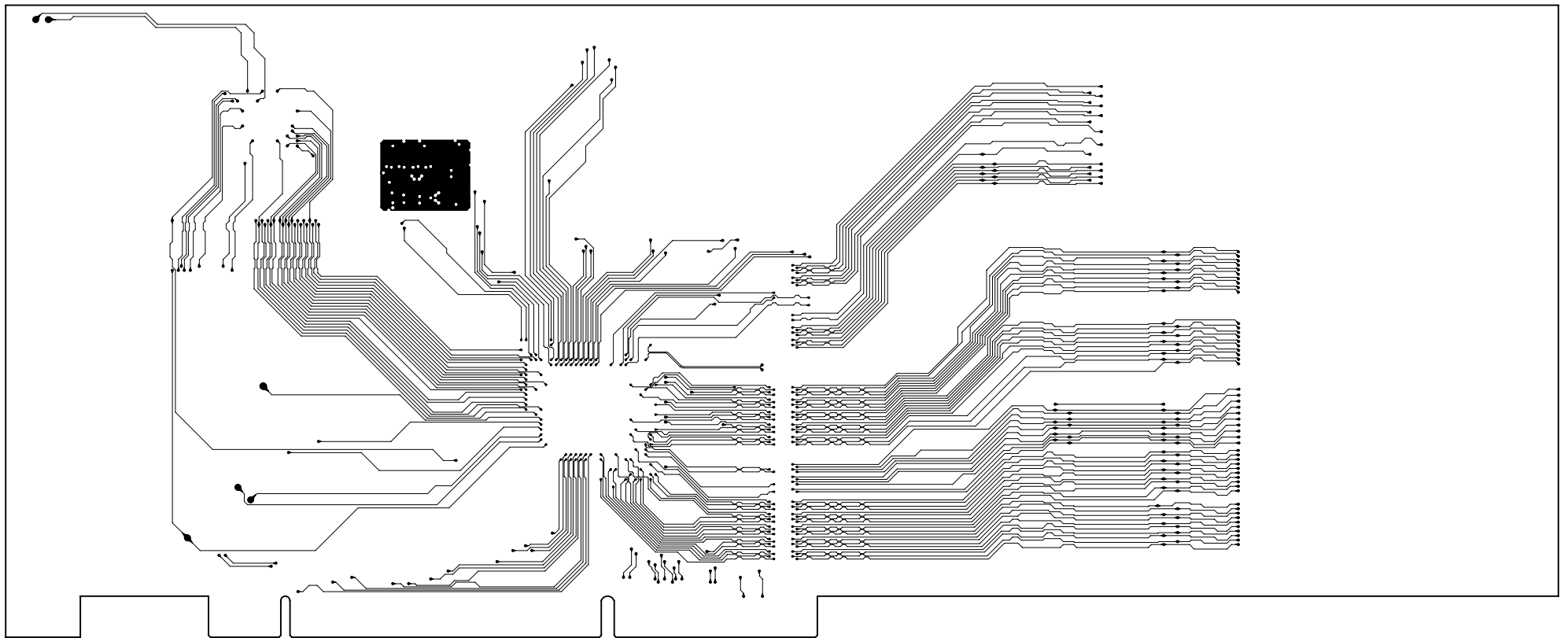


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L7 ARTWORK

ART07.PHO

(VCC_ETHER_A)

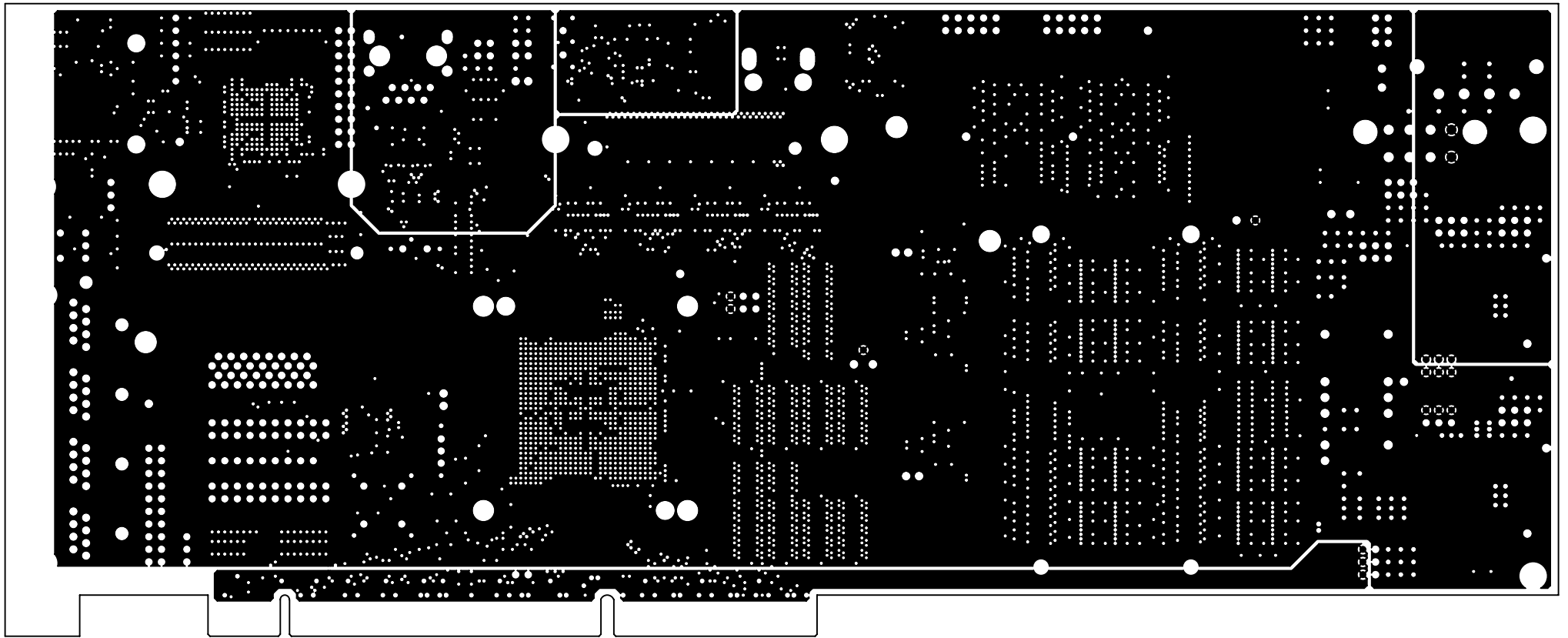


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L8 ARTWORK

ART008.PHO

(VCC25, VCC25_CNT, VCC33_PCI, AVCC33_1394, VCC_ETHER)

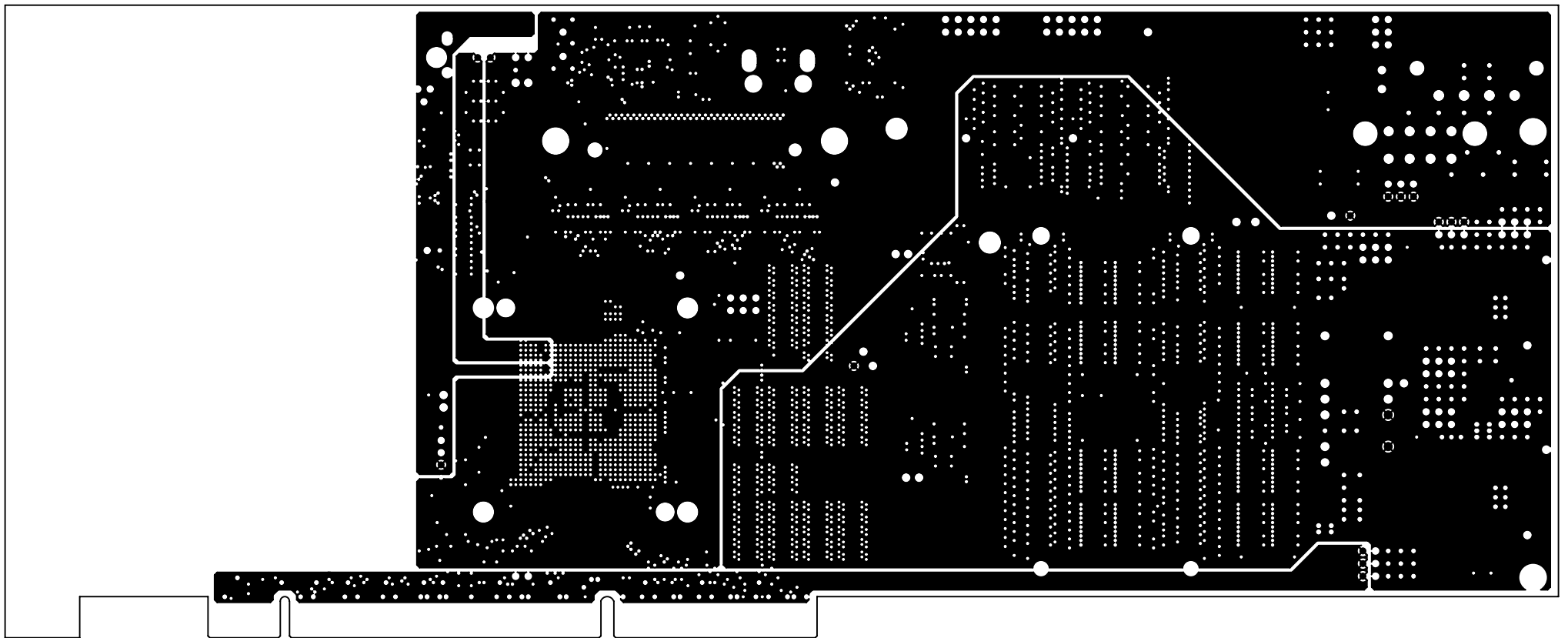


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L9 ARTWORK

ART009.PHO

(VCC10, VCC33_PCI, VCC125_T, BAT_VCC10, BAT_VCC33)

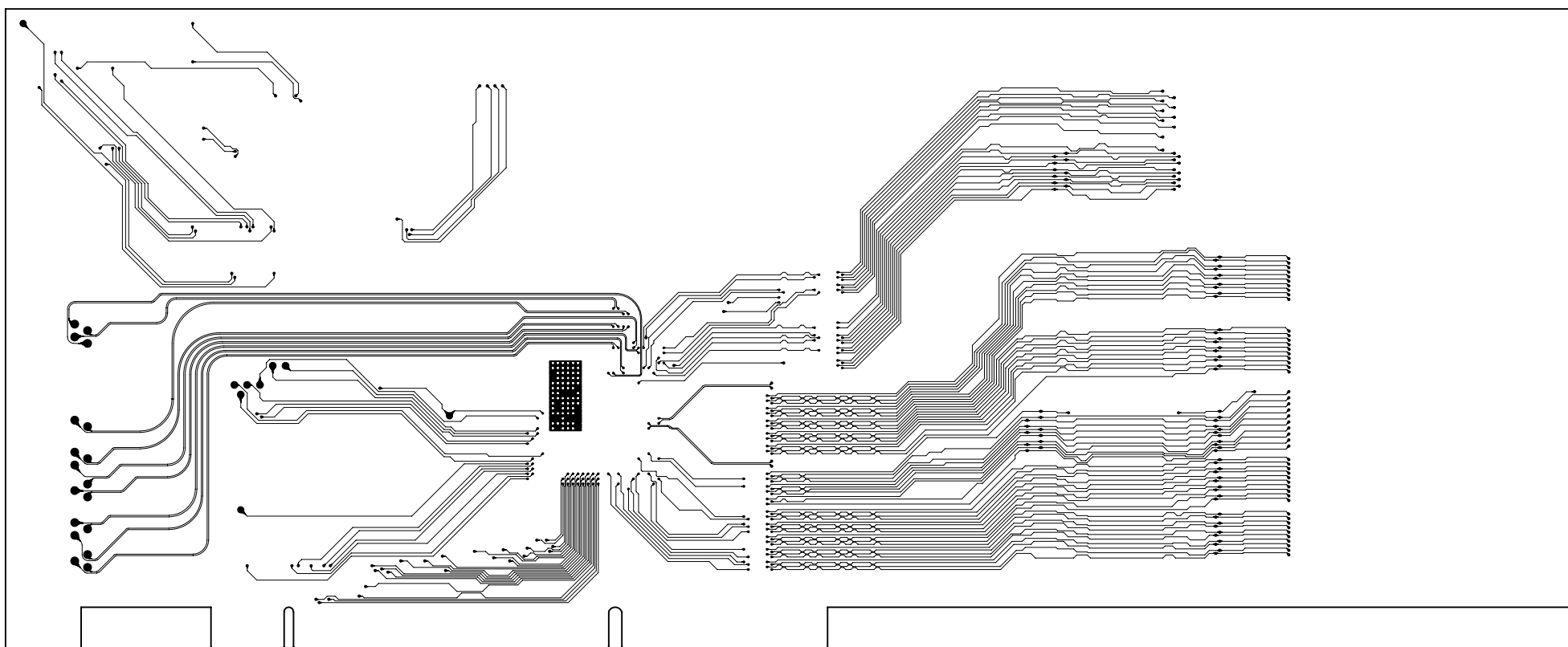


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L10 ARTWORK

ART010.PHO

(BAT_VCC33)

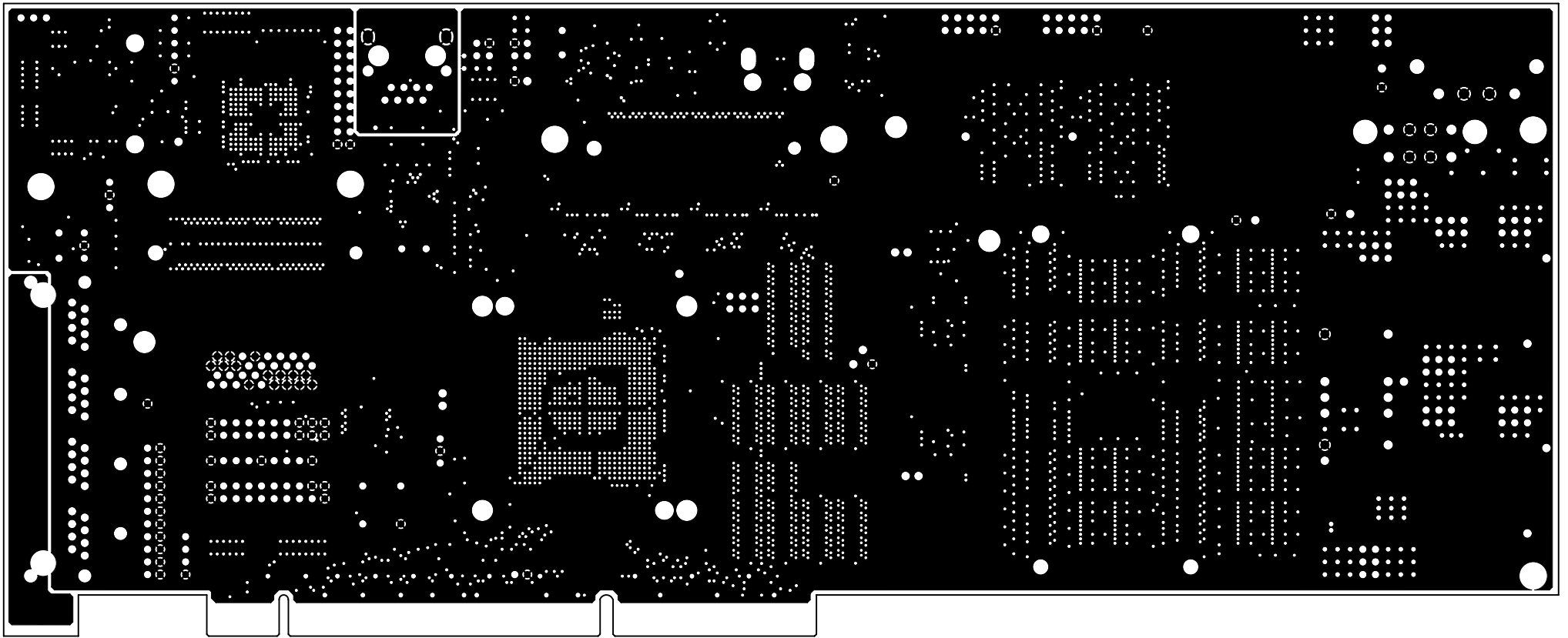


073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L11 ARTWORK

ART011.PHO

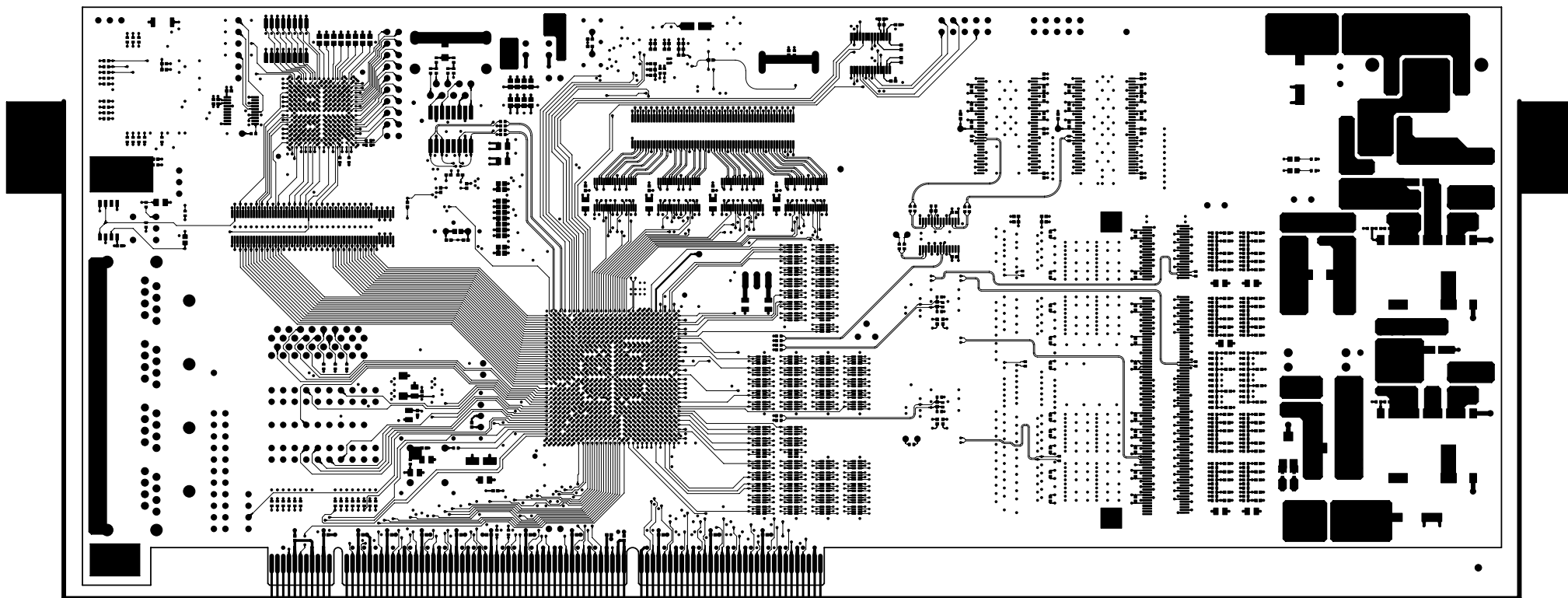
(GND)



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L12 ARTWORK

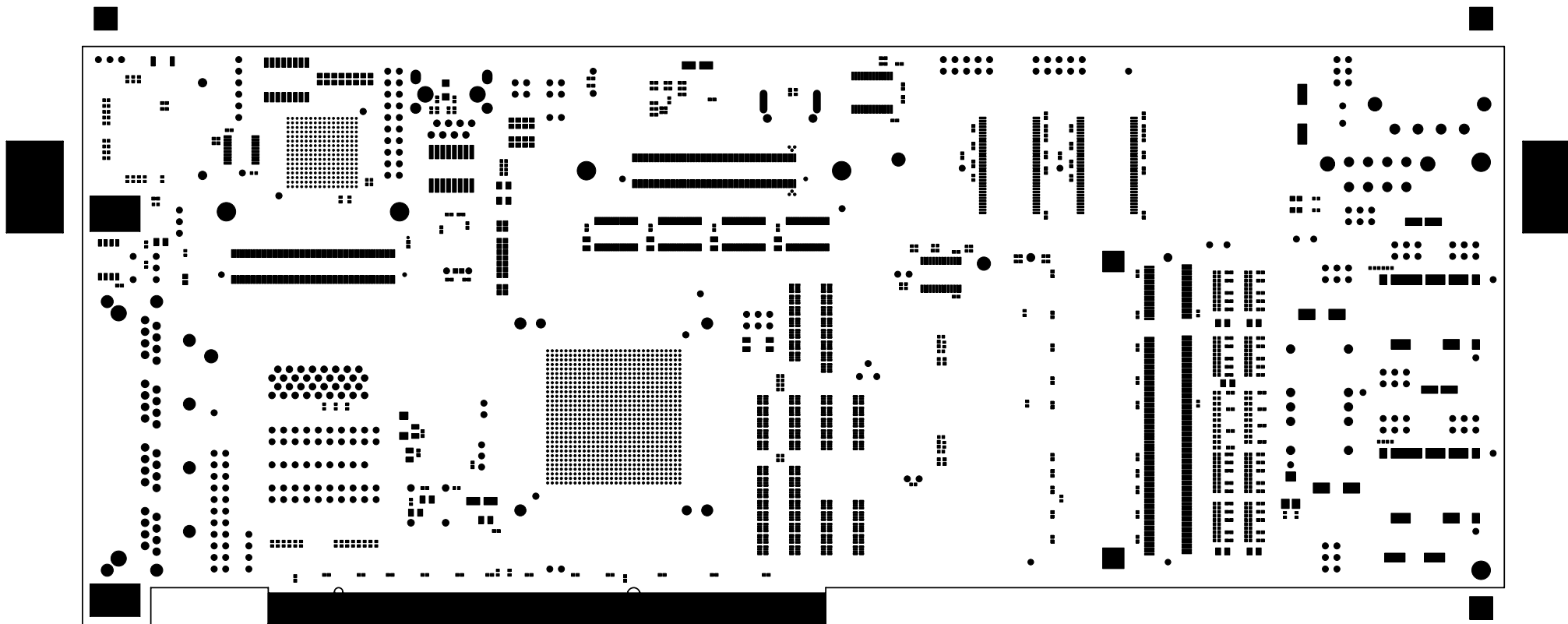
ART012.PHO



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L12 SOLDERMASK(BOTTOM)

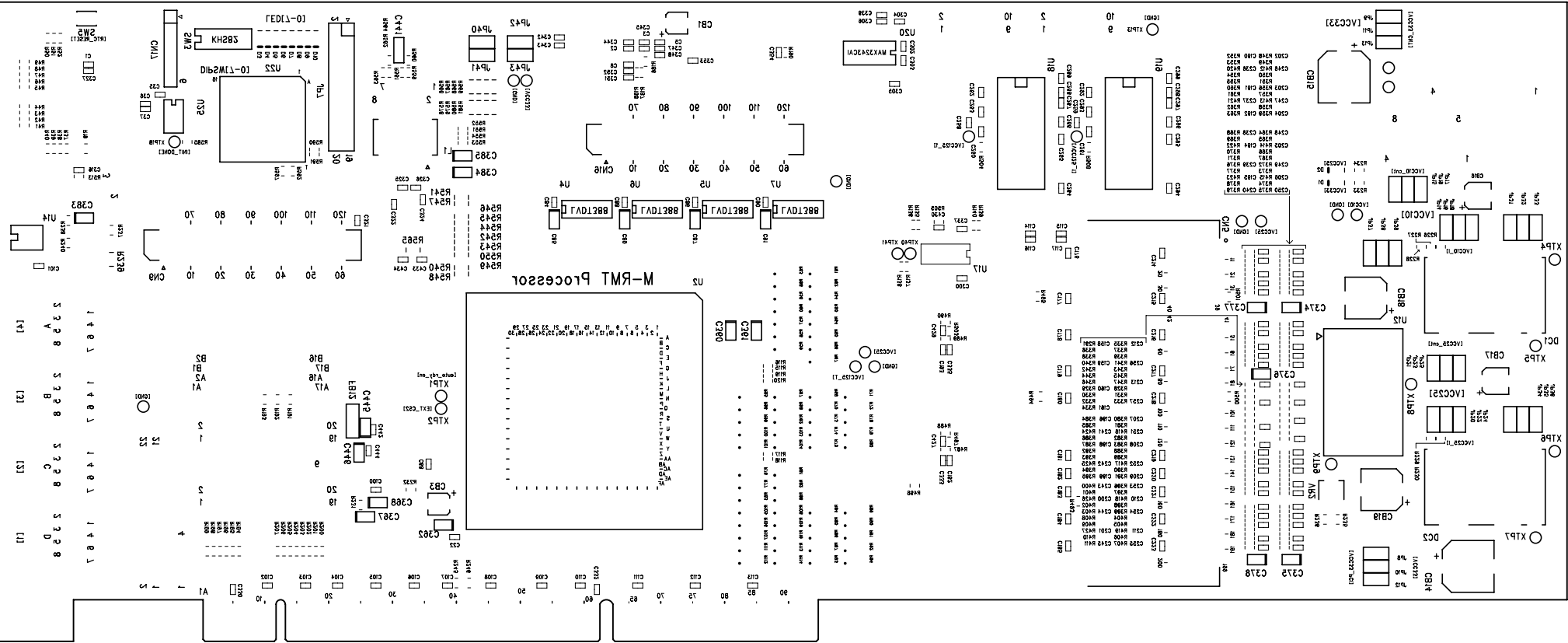
SM012025.PHO



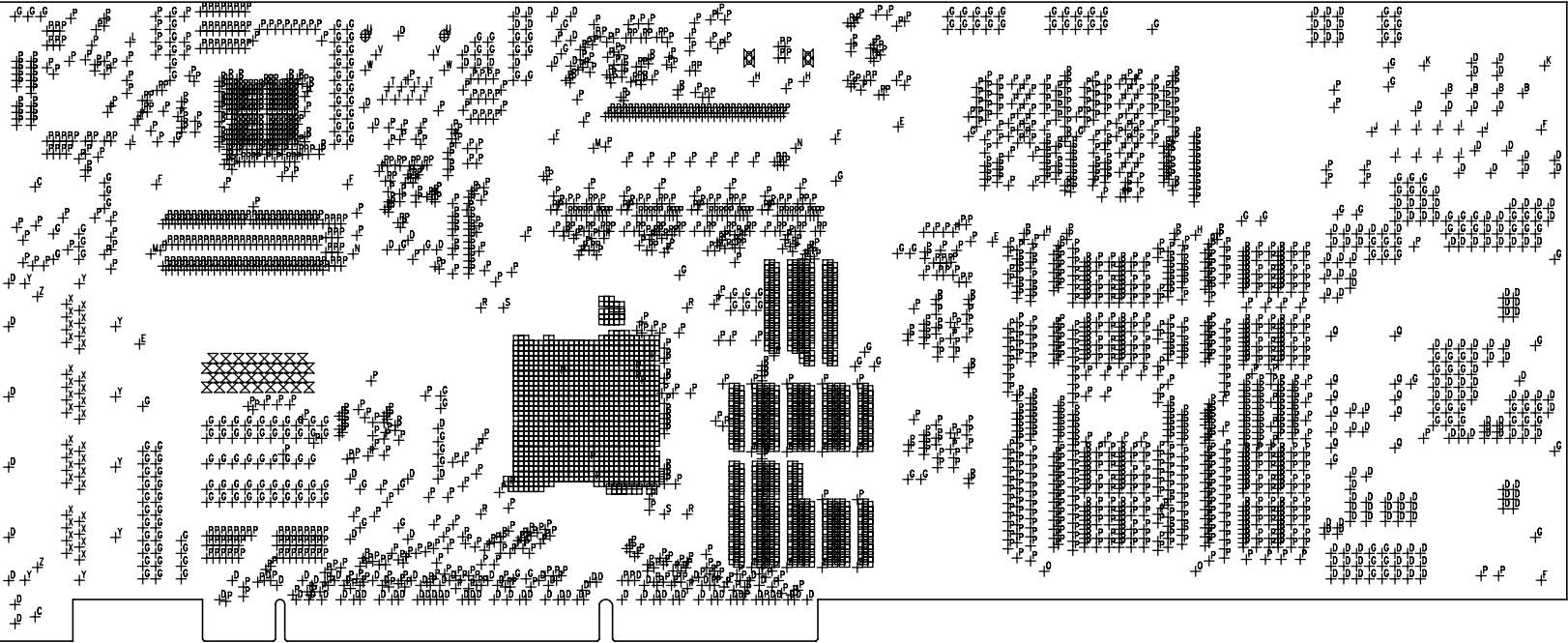
073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16

L12 BOTTOM SILK

SSB012021.PHO



073-070301-31(MRMTP VER2 EVA PCI EVA BOARD)SANSEI SYSTEM 2007.03.16
DRILL DRAWING DD001029



寸法	個数	記号	メッキ	TOL
35	34	⊠	YES	+/-0.0
40 x 100	2	⊠	YES	+/-0.0
60	4	⊠ ^B	YES	+/-0.0
138	2	⊠ ^C	YES	+/-0.0
16	265	⊠ ^D	YES	+/-0.0
98	3	⊠ ^E	NO	+/-0.0
138	6	⊠ ^F	NO	+/-0.0
32	243	⊠ ^G	YES	+/-0.0
63	4	⊠ ^H	NO	+/-0.0
55	8	⊠ ^I	YES	+/-0.0
118	2	⊠ ^J	NO	+/-0.0
91	2	⊠ ^K	YES	+/-0.0
67	2	⊠ ^L	NO	+/-0.0
43.31	2	⊠ ^M	NO	+/-0.0
27.56	2	⊠ ^N	NO	+/-0.0
43	2	⊠ ^O	NO	+/-0.0
12	2241	⊠ ^P	YES	+/-0.0
47	10	⊠ ^Q	YES	+/-0.0
90.55	4	⊠ ^R	NO	+/-0.0
72.83	2	⊠ ^S	NO	+/-0.0
36	8	⊠ ^T	YES	+/-0.0
62 x 87	2	⊠ ^U	YES	+/-0.0
128	2	⊠ ^V	NO	+/-0.0
62	2	⊠ ^W	YES	+/-0.0
39.37	32	⊠ ^X	YES	+/-0.0
78.74	8	⊠ ^Y	YES	+/-0.0
125.98	2	⊠ ^Z	NO	+/-0.0
7.87	1203	⊠	YES	+/-0.0