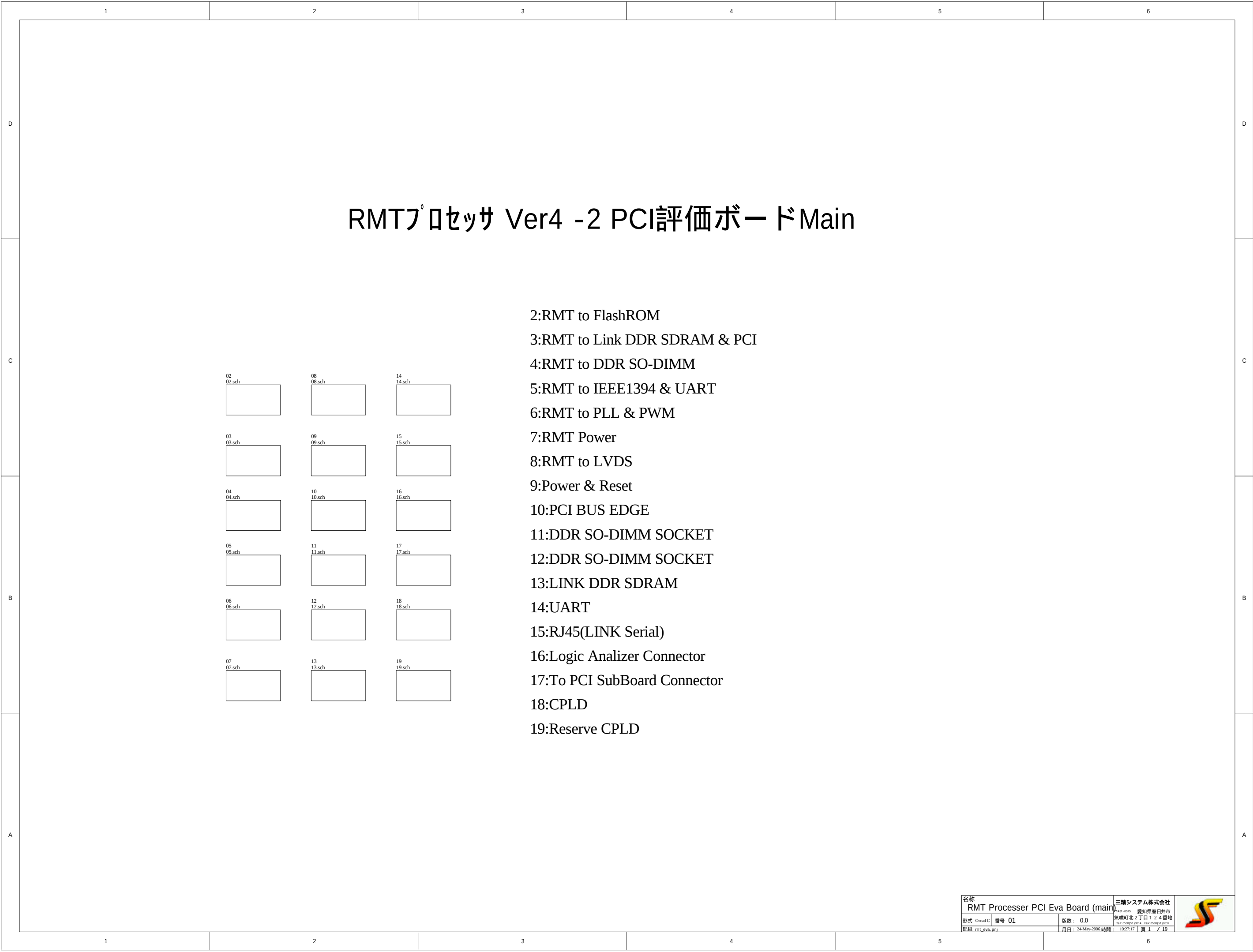
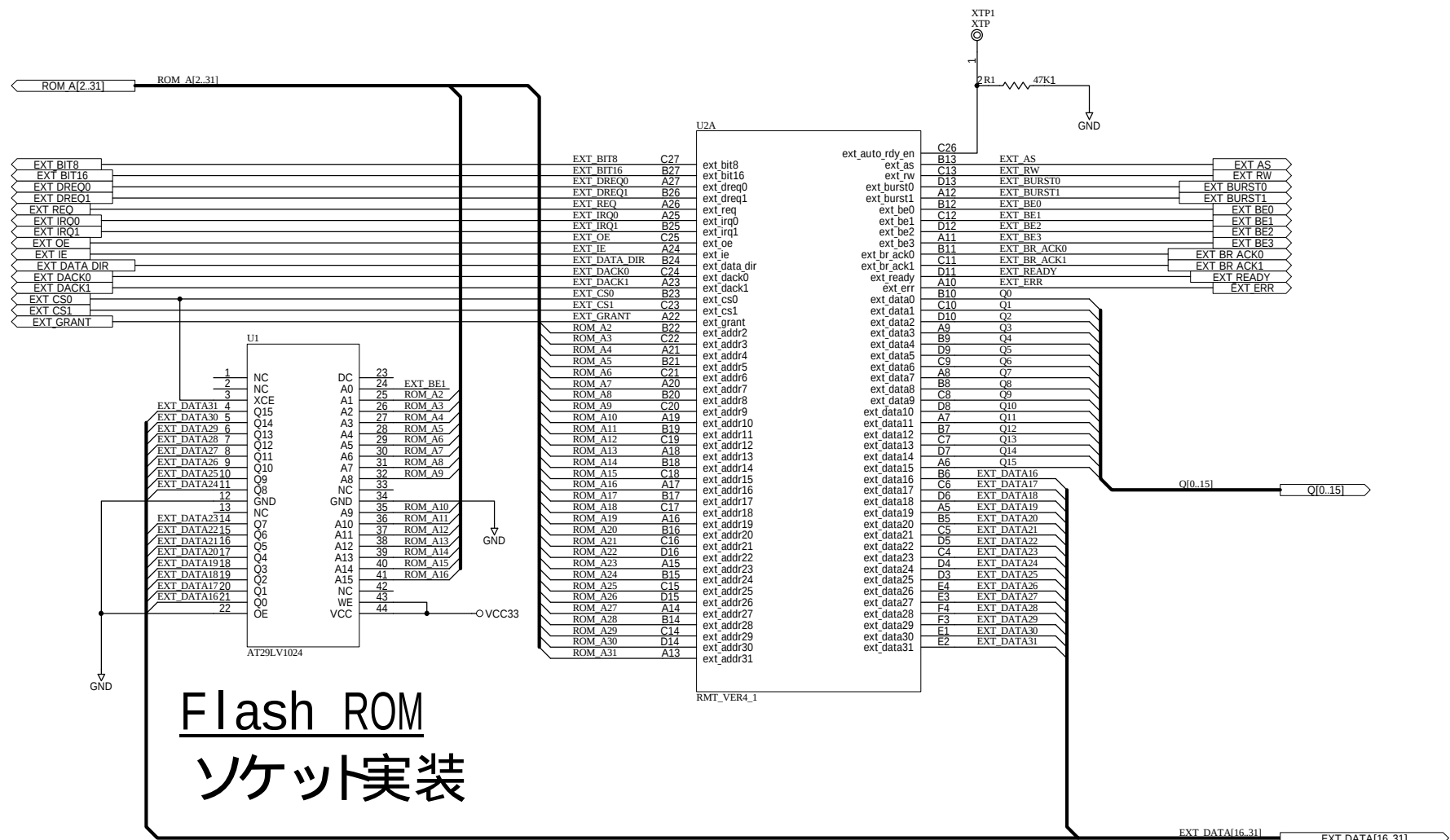
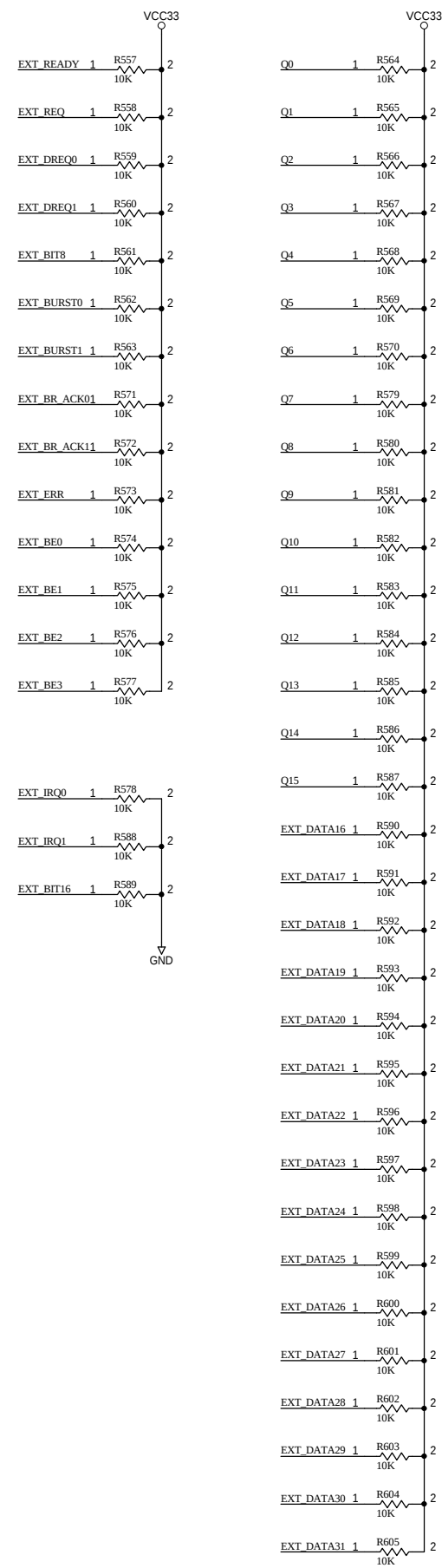
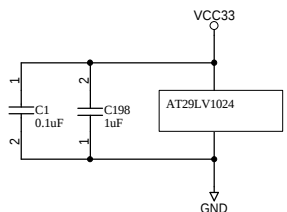




RMTプロセッサ

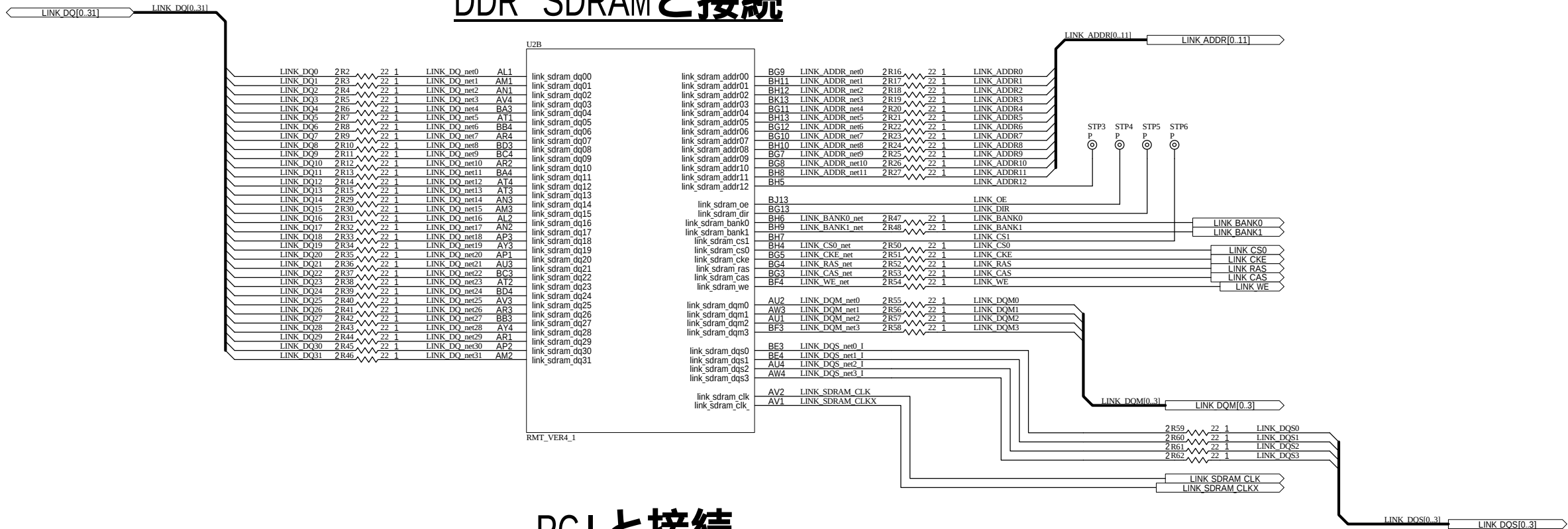


Flash ROM ソケット実装

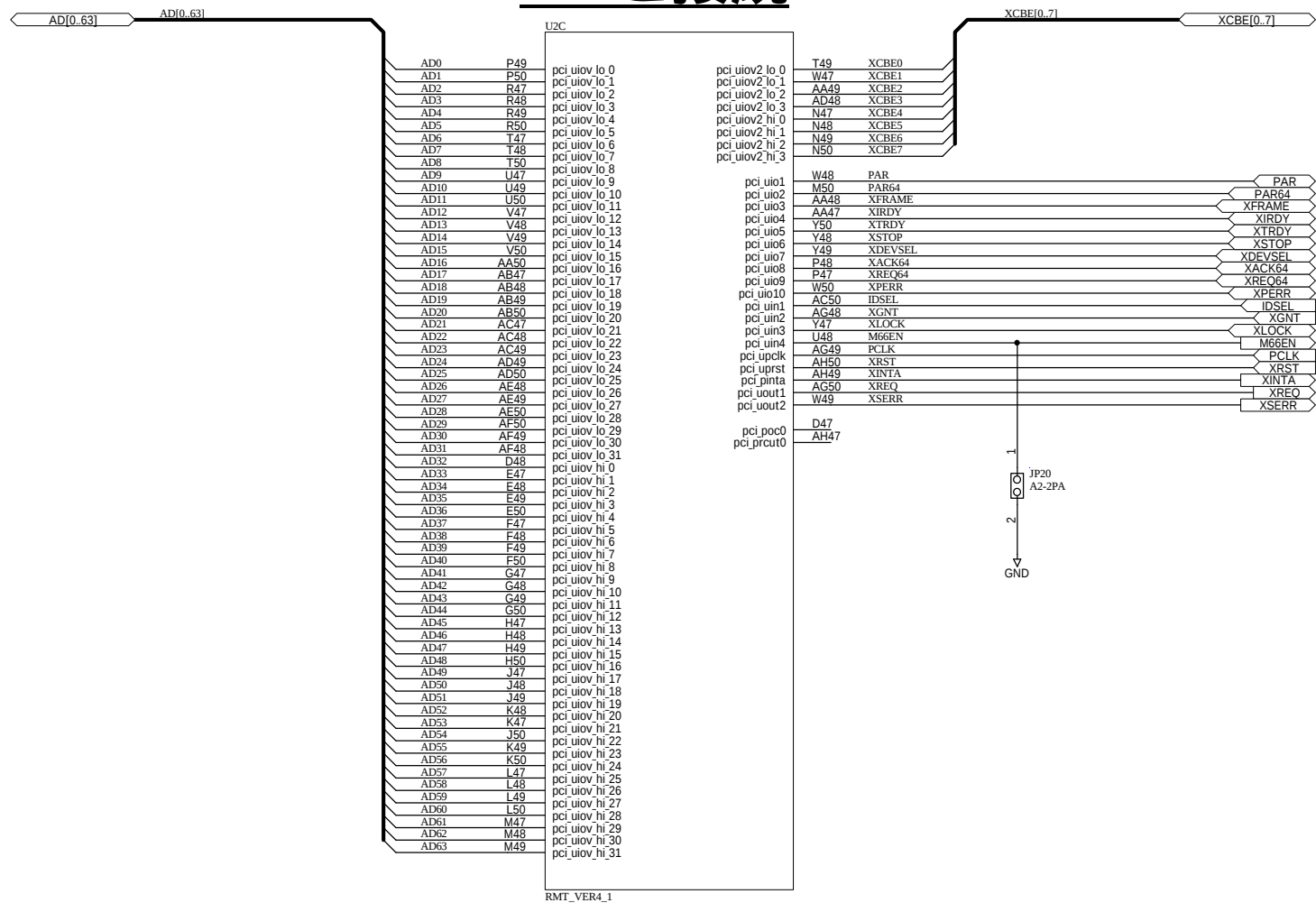


RMTプロセッサ

DDR SDRAMと接続

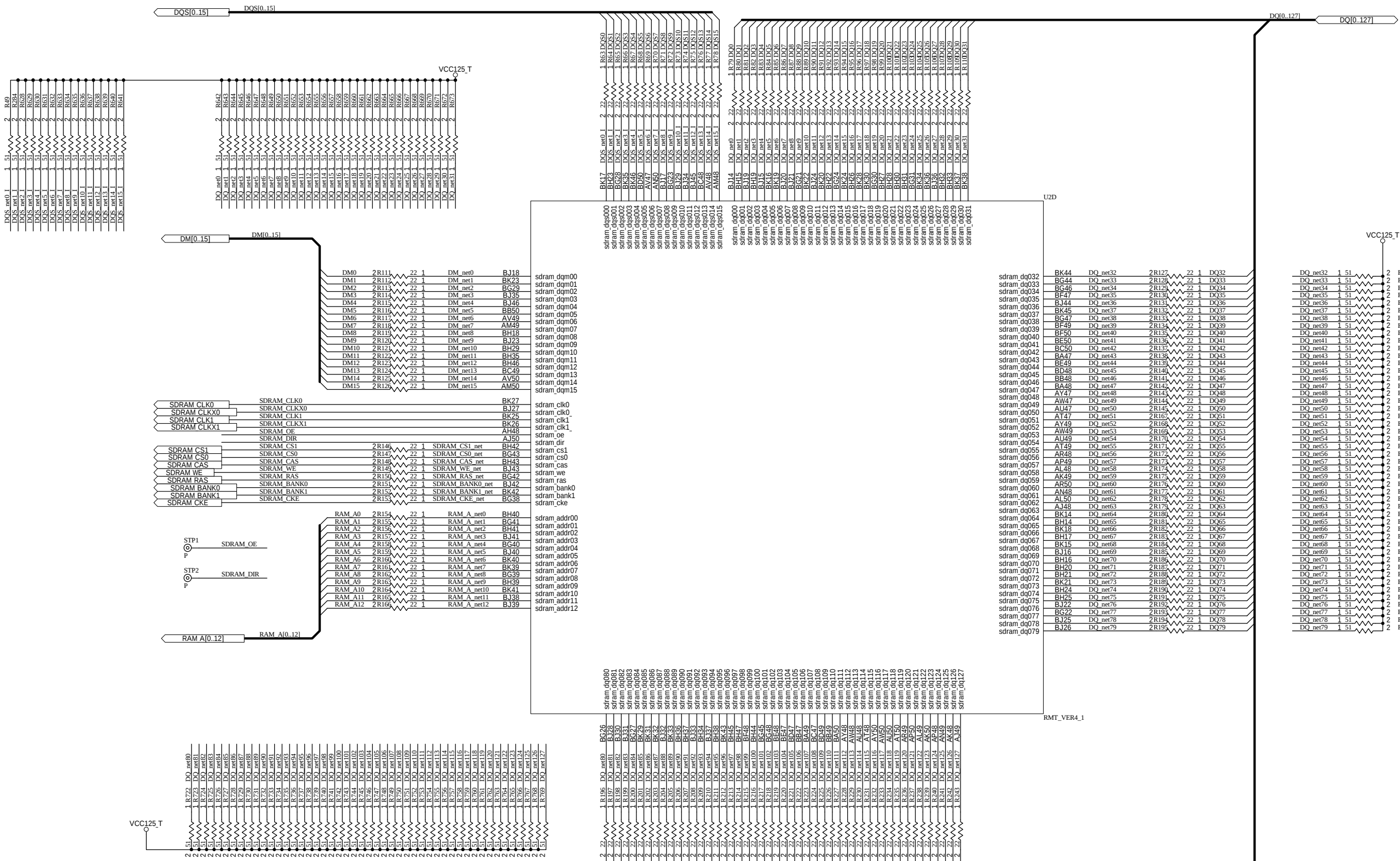


PCI と接続



名称		三精システム株式会社	
RMT to Link DDR SDRAM & PCI		〒474-0015 愛知県春日井市 矢崎町北2丁目124番地 TEL: 0568(51)3614 FAX: 0568(51)8832	
形式	Orcad C	番号	0
版数	0.0		
記録	03 sch	日付	24-May-2006 時間 10:27:19 頁 3 / 19

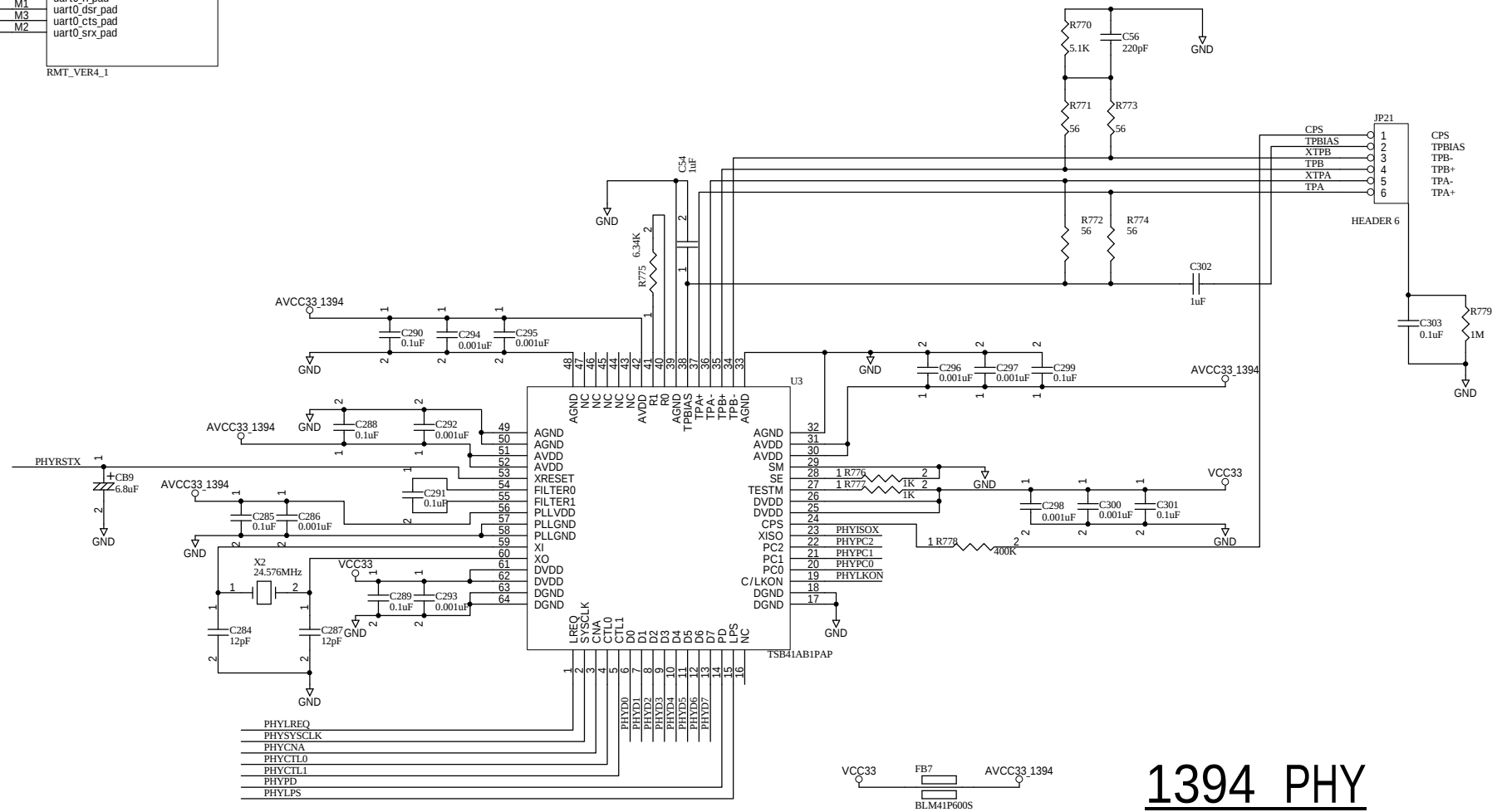
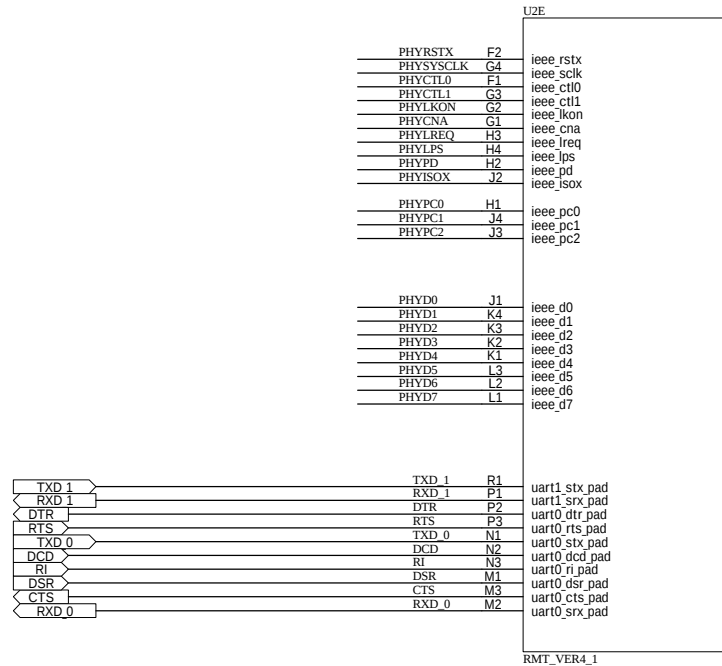
RMTプロセッサ



DDR SO-DIMMと接続

RMTプロセッサ

UART

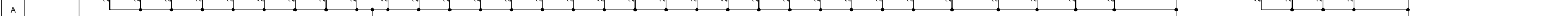


1394 PHY

D



A

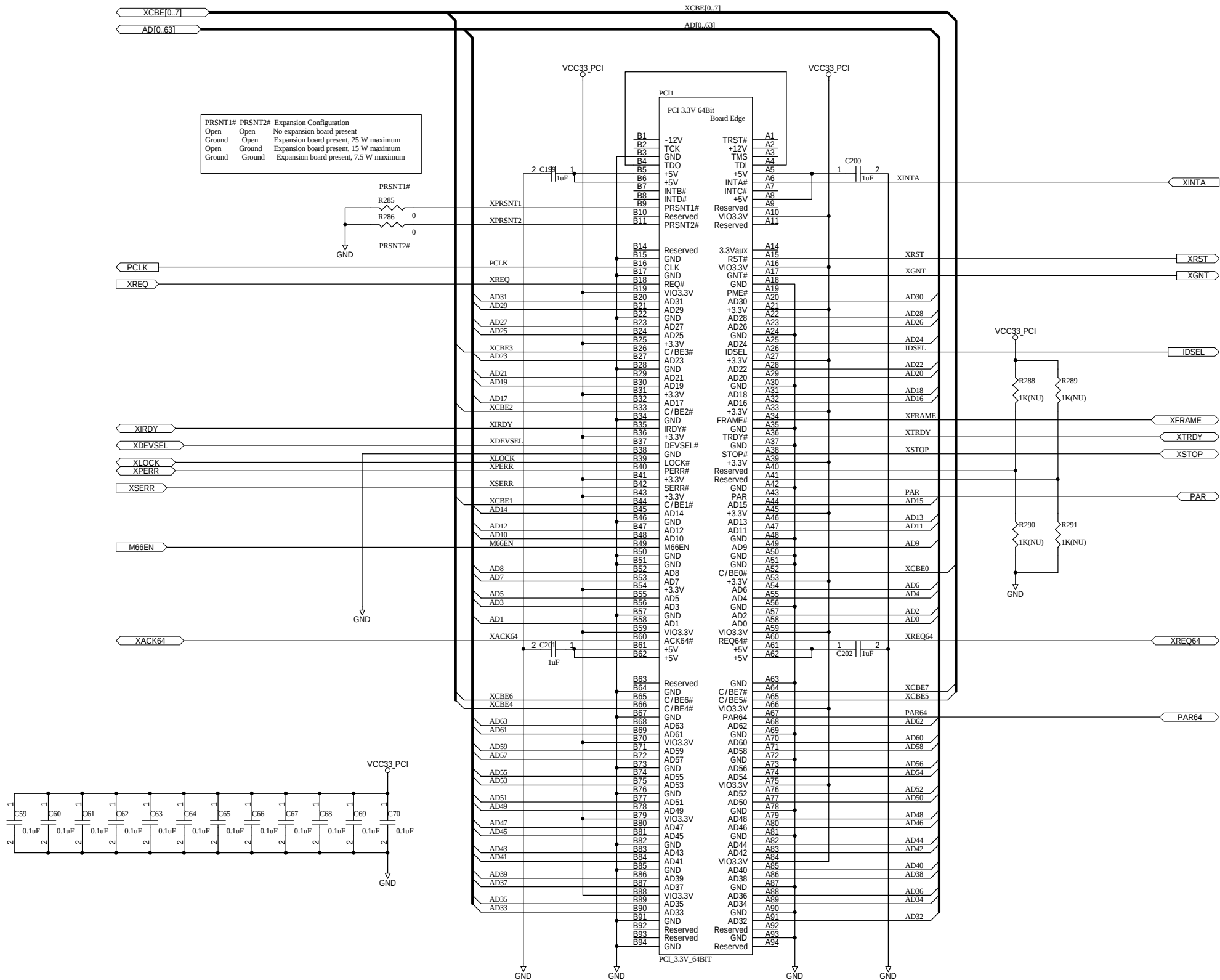


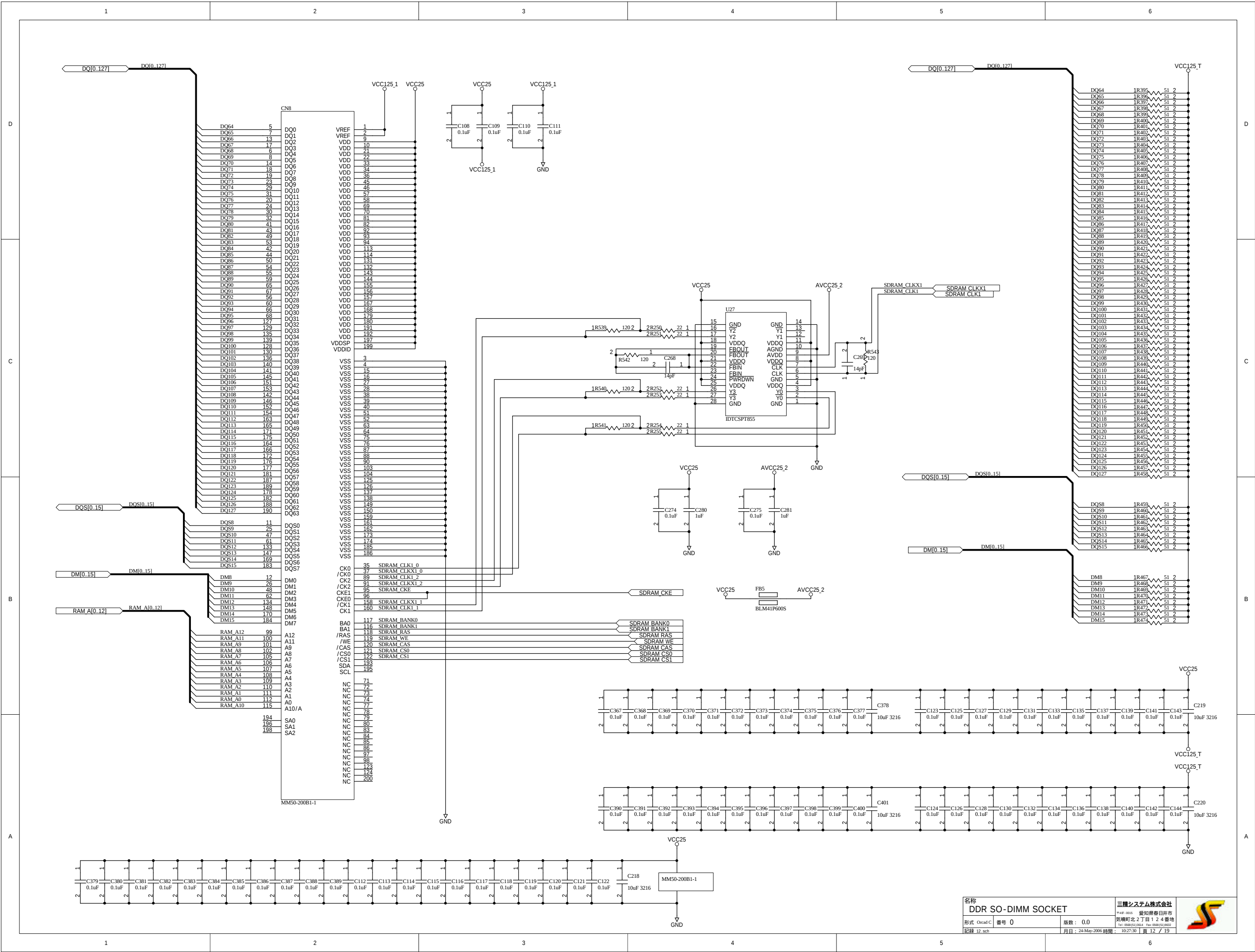
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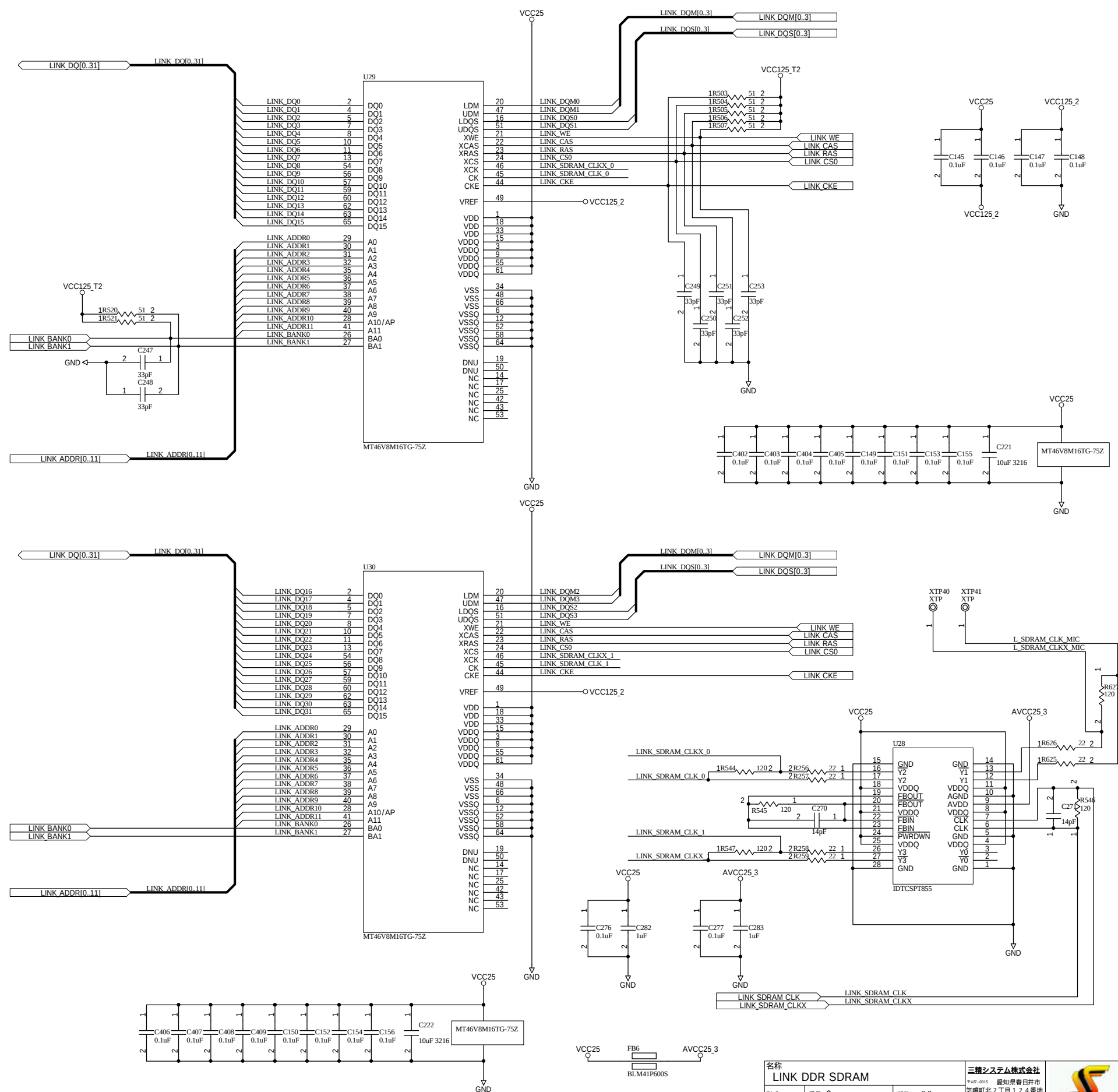
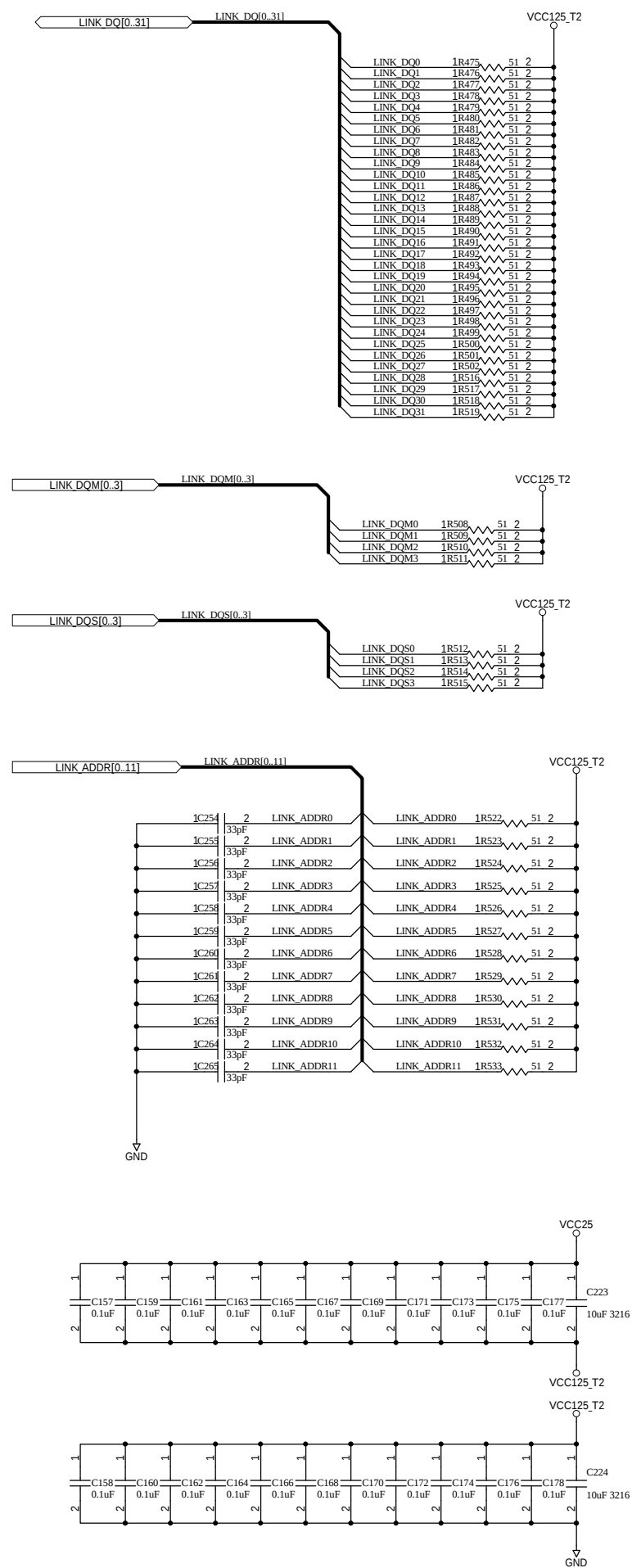
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PCI bus Edge



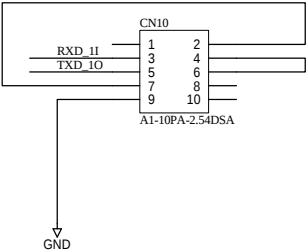
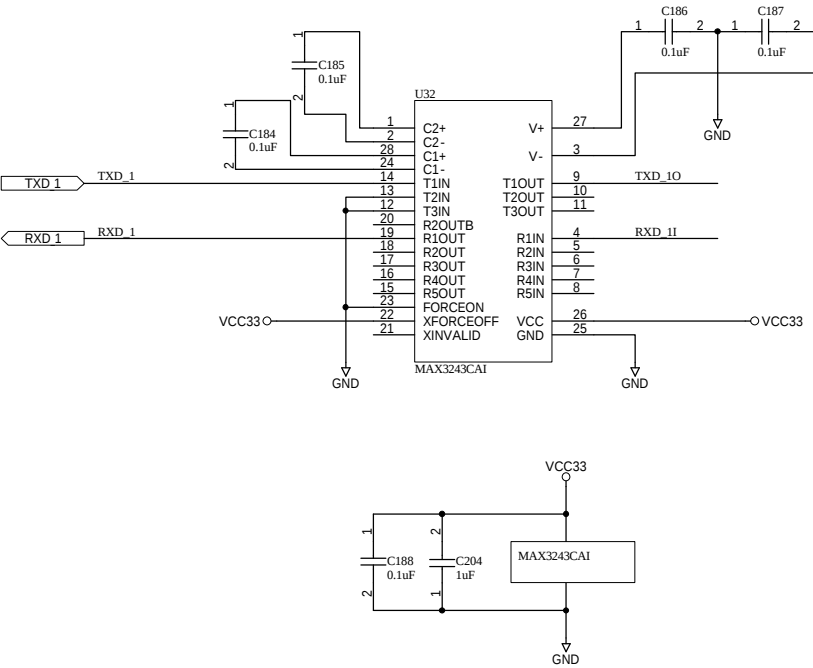
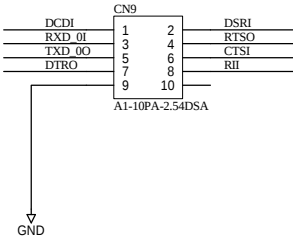
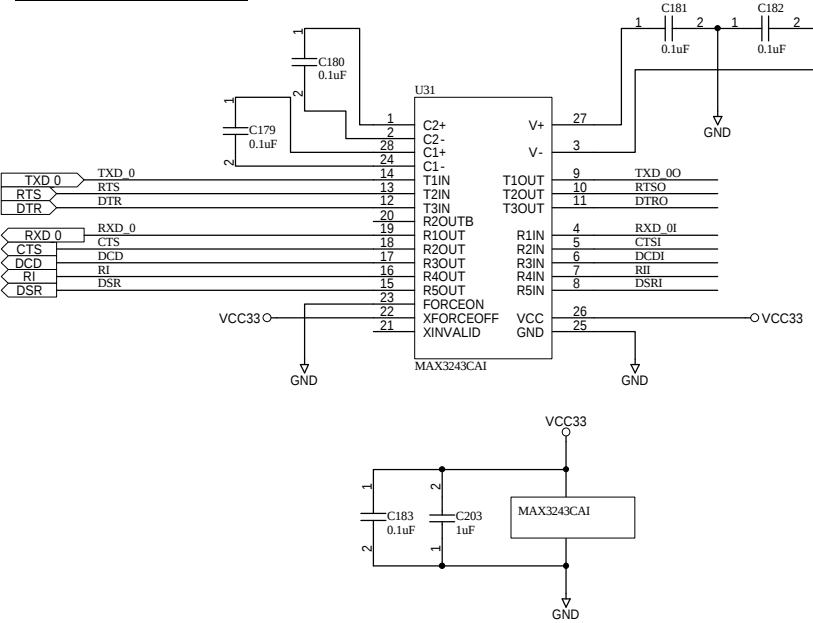


Link用 DDR SDRAM

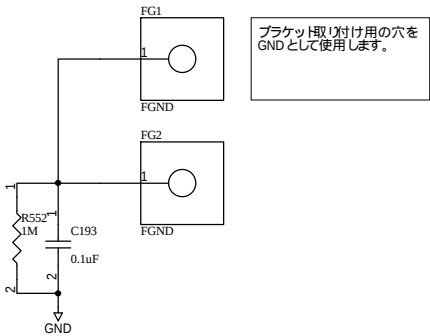
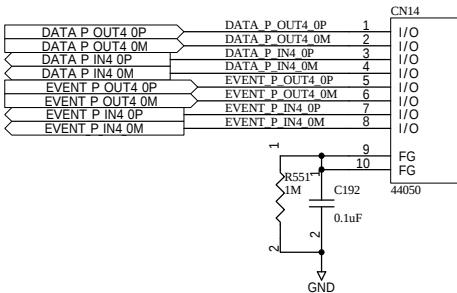
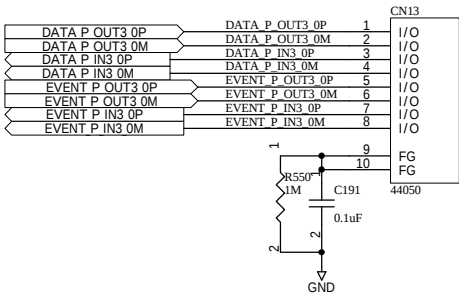
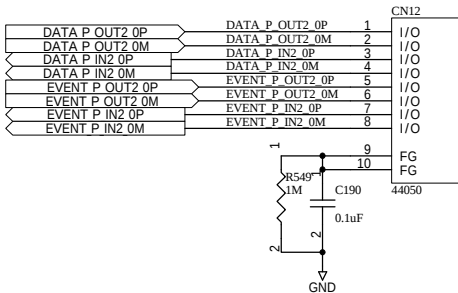
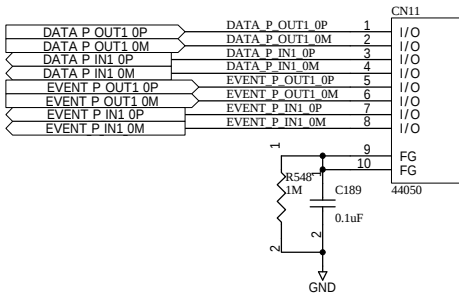


名称 LINK DDR SDRAM		三精システム株式会社 〒410-0015 愛知県春日井市 気町北2丁目124番地 Tel: 0568 (51) 3654 Fax: 0568 (51) 8832		
形式 OrCAD C	番号 0	版数: 0.0		
記録 13_sch		月日: 24-May-2006 時間: 10:27:32	頁 13 / 19	

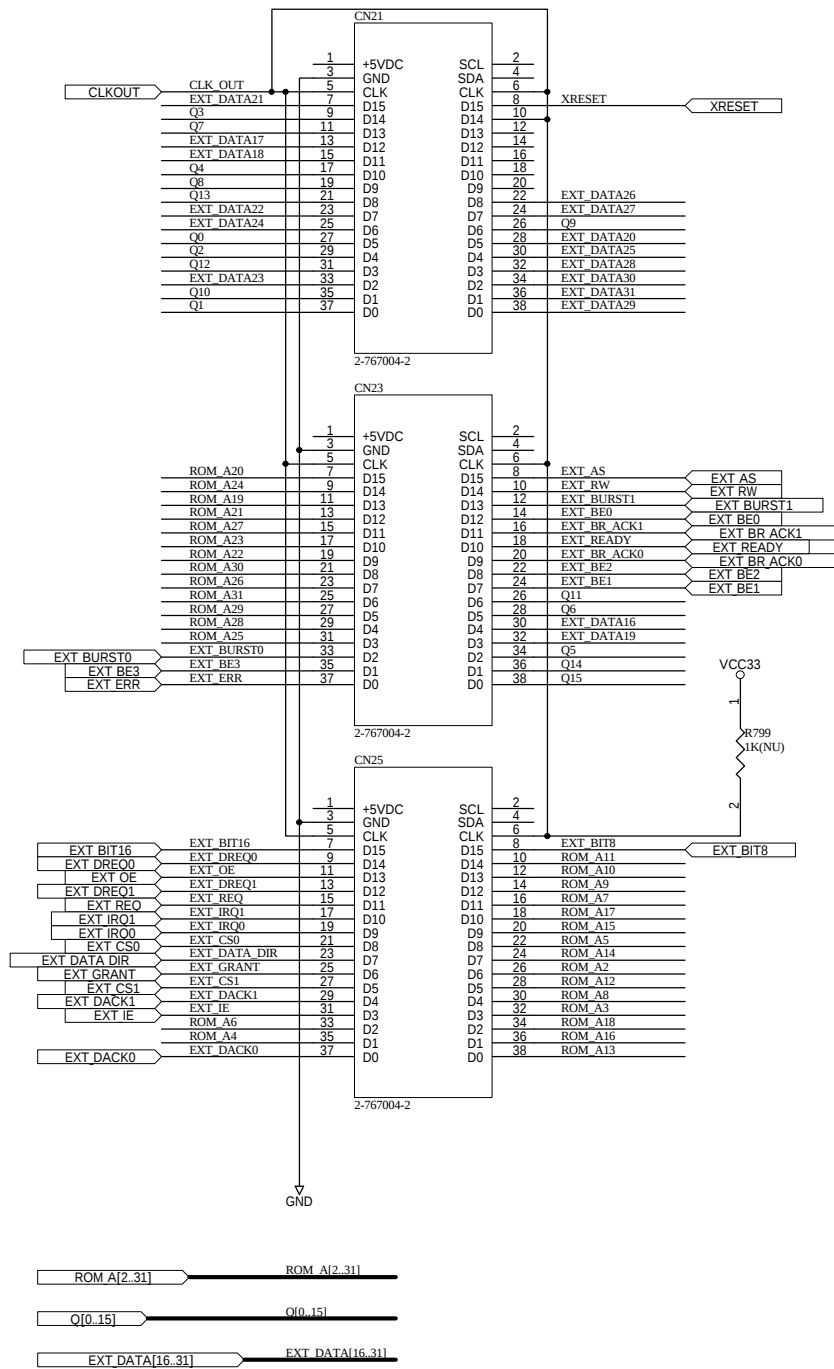
RS-232C



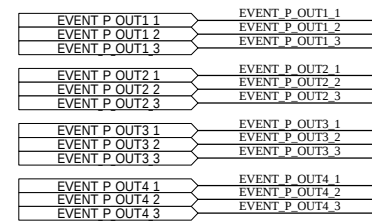
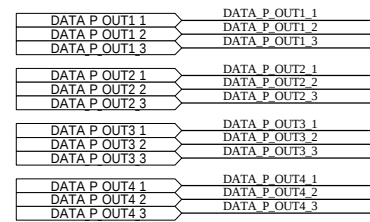
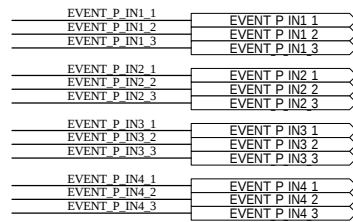
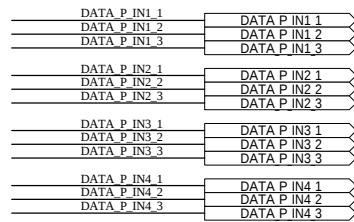
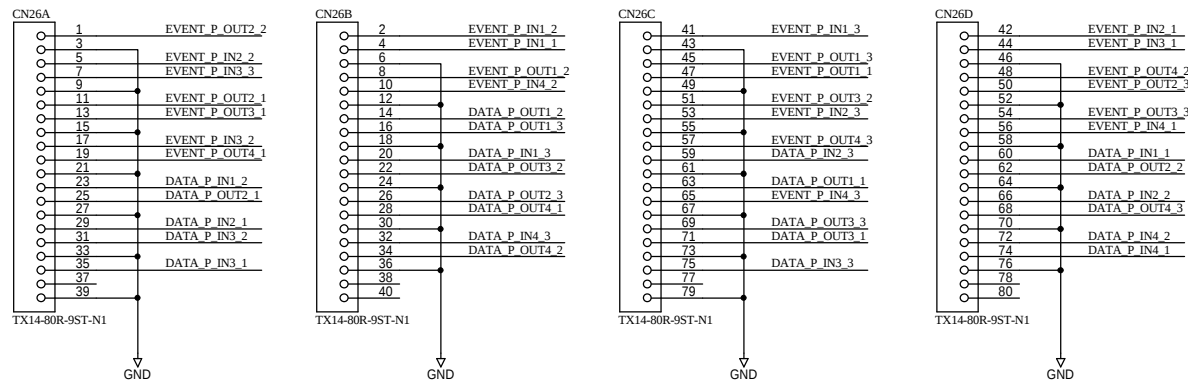
LINK Serial



FLASH-ROM部と接続



2ボード間接続コネクタ



D

D

C

C

B

B

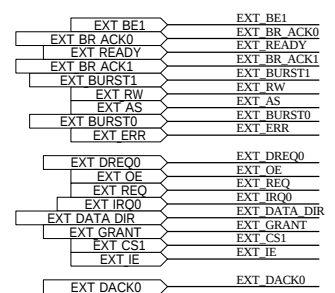
A

A

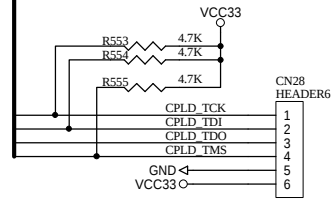
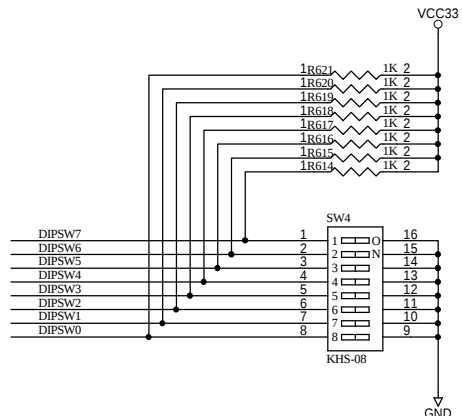
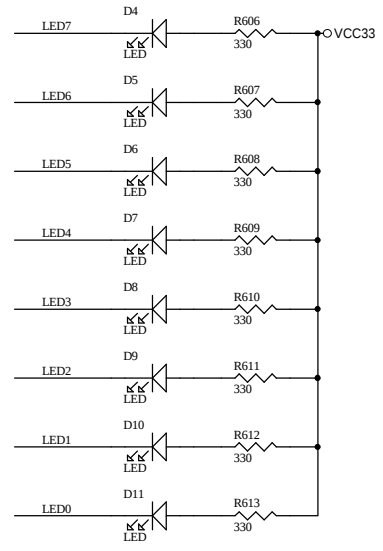
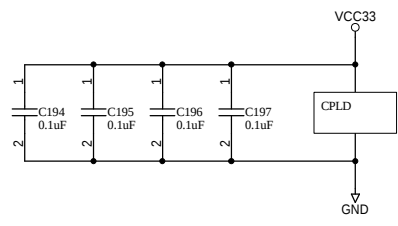
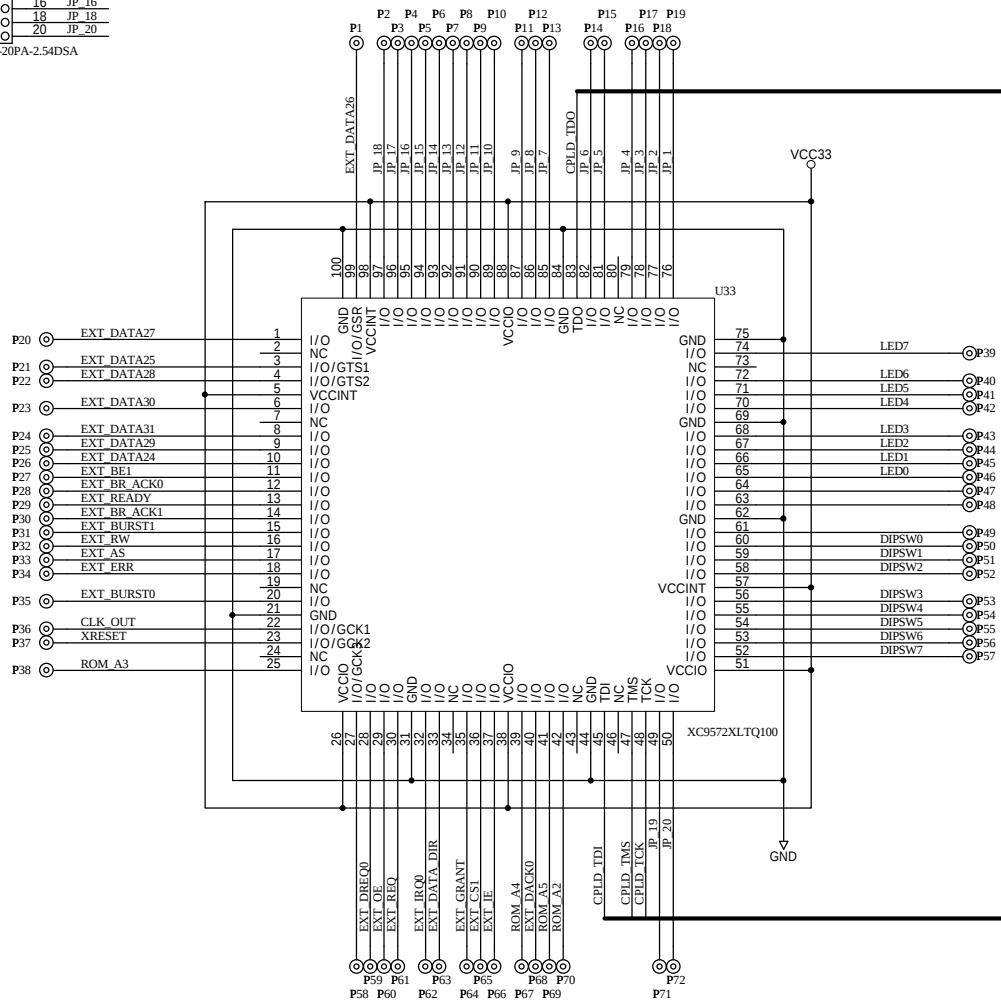
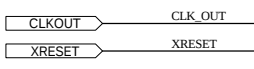
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JP 11	11		12	JP 12
JP 13	13		14	JP 14
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A1-20PA-2.54DSA

ROM A[2..31]

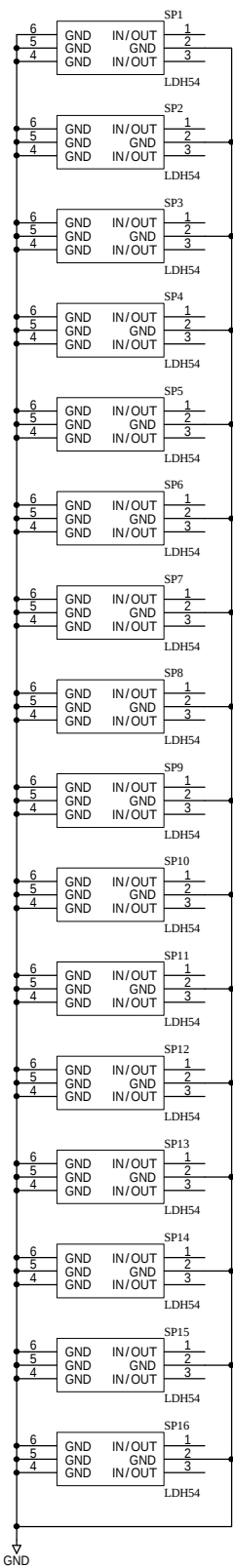


EXT DATA[16..31]



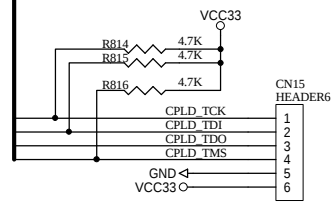
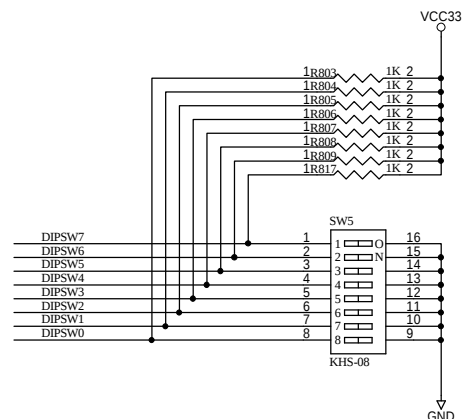
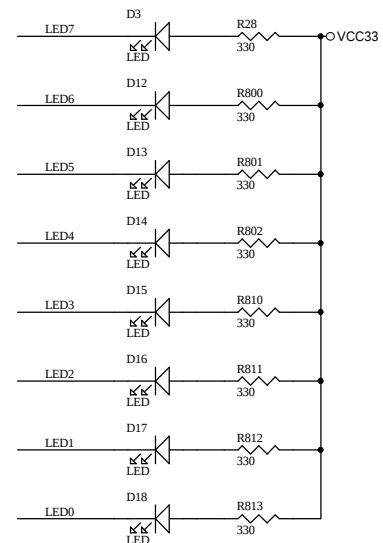
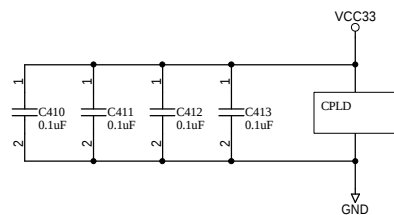
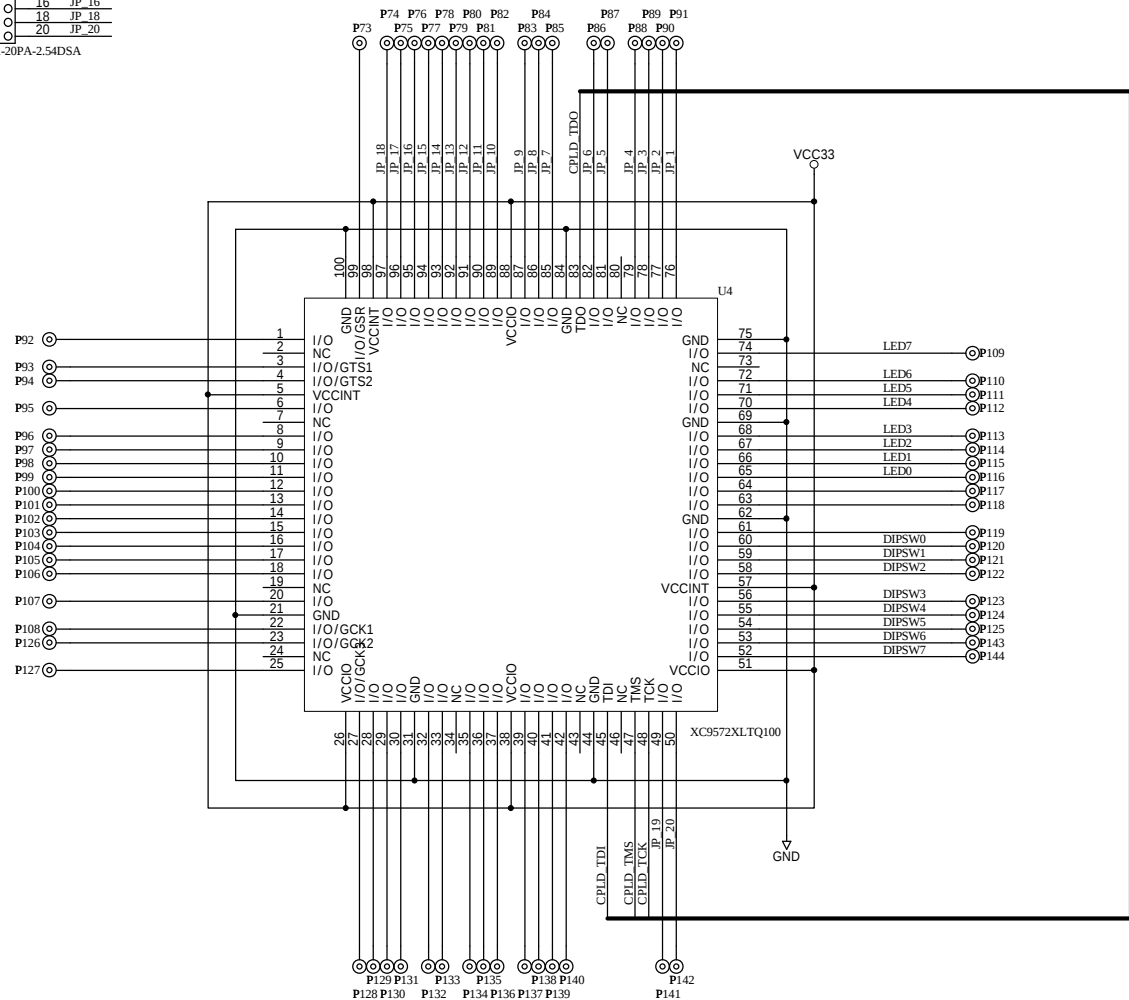
CPLD書き込みコネクタ

Reserve



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JP 15	15	JP 16	16	JP 16
JP 17	17	JP 18	18	JP 18
JP 19	19	JP 20	20	JP 20

A1-20PA-2.54DSA



CPLD書き込みコネクタ

