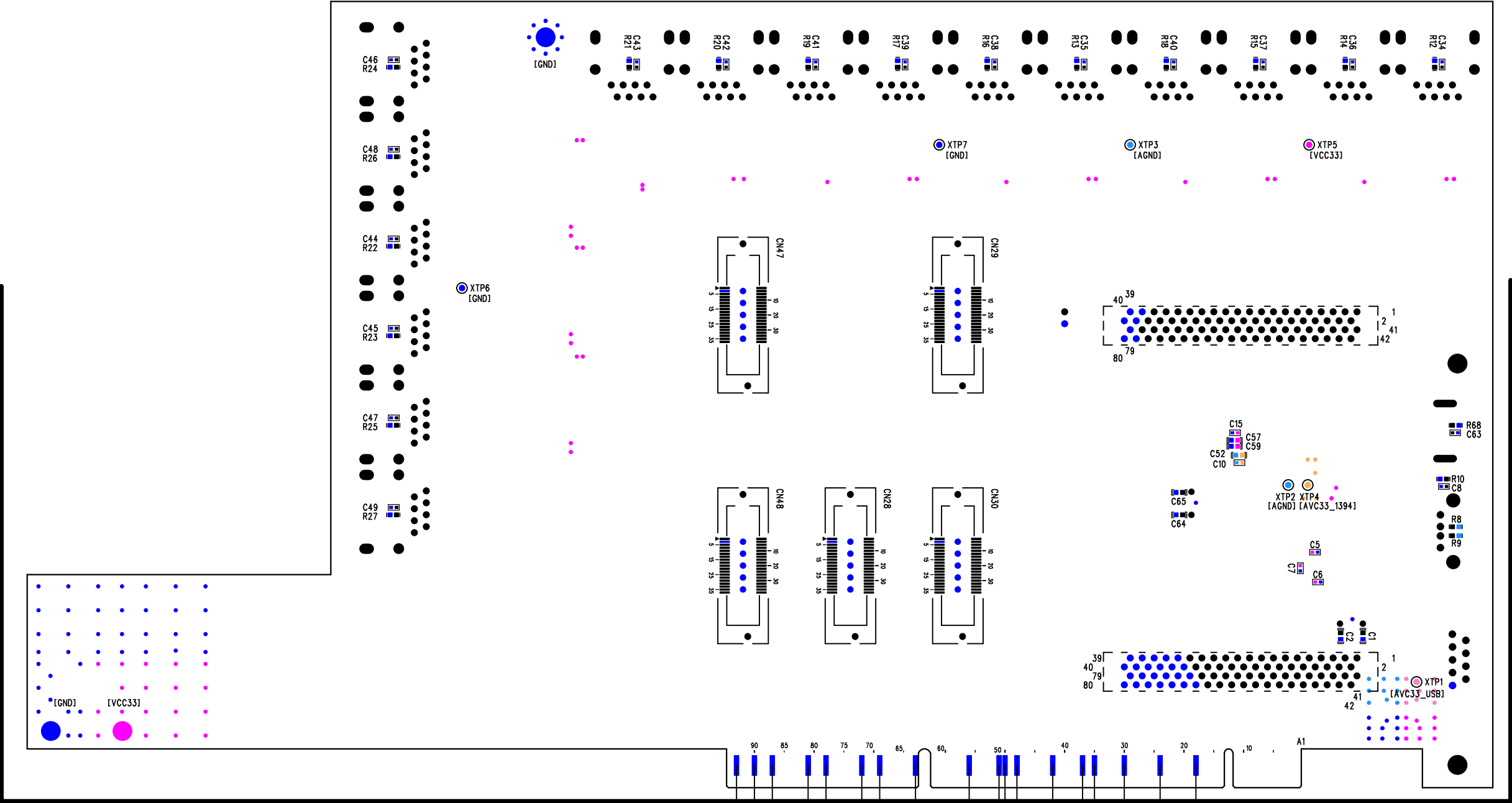


L1 TOP SILK
L1 ARTWORK

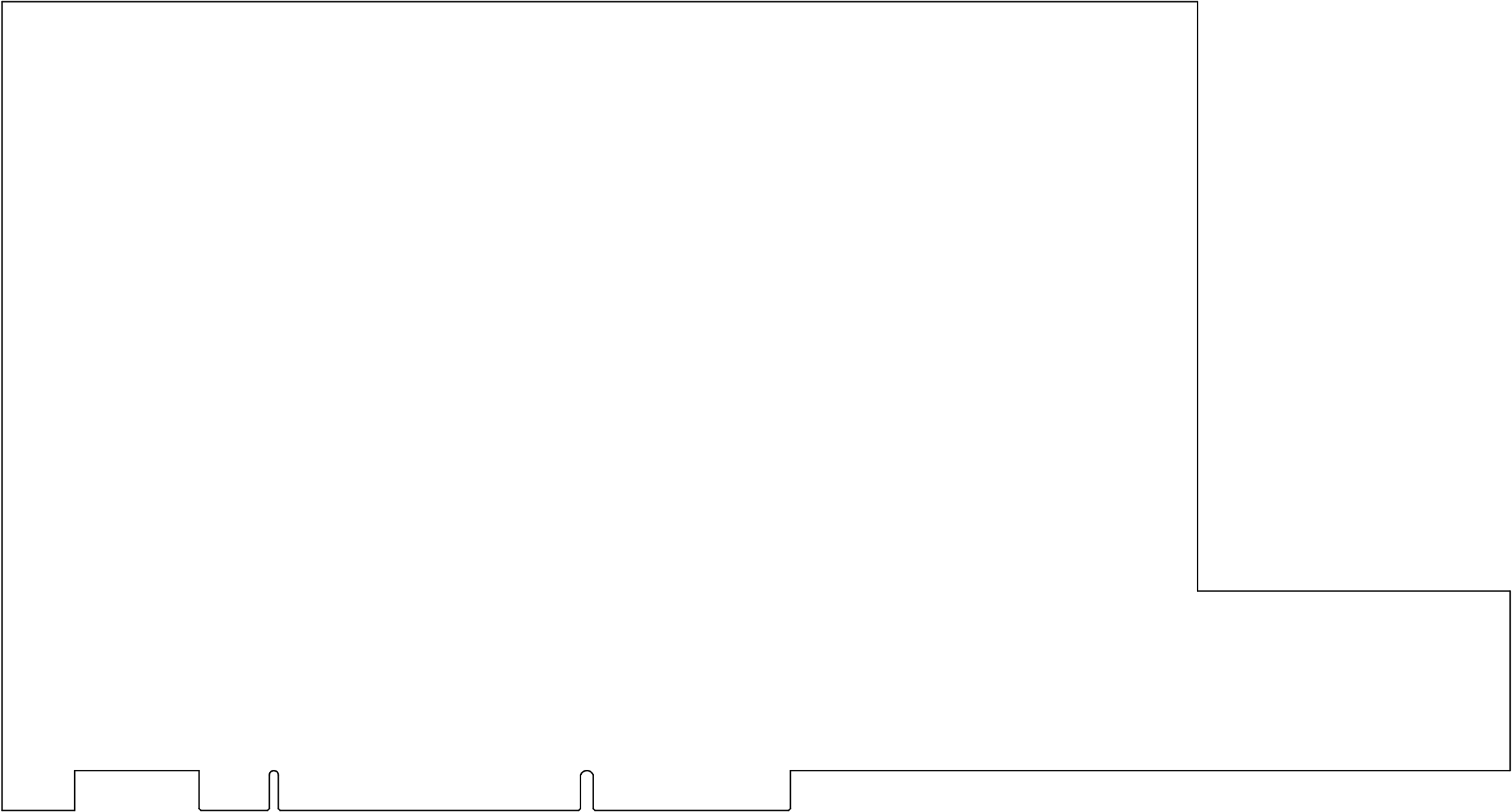
L1 TOP SILK	SST001020.PHO
L1 ARTWORK	ART001.PHO





L12 NETWORK
L12 BOTTOM SILK
22B015051.PHO
ART015.PHO
KB04015(RMT PCI SUB BOARD)2ANSEI SYSTEM 5004.05.09

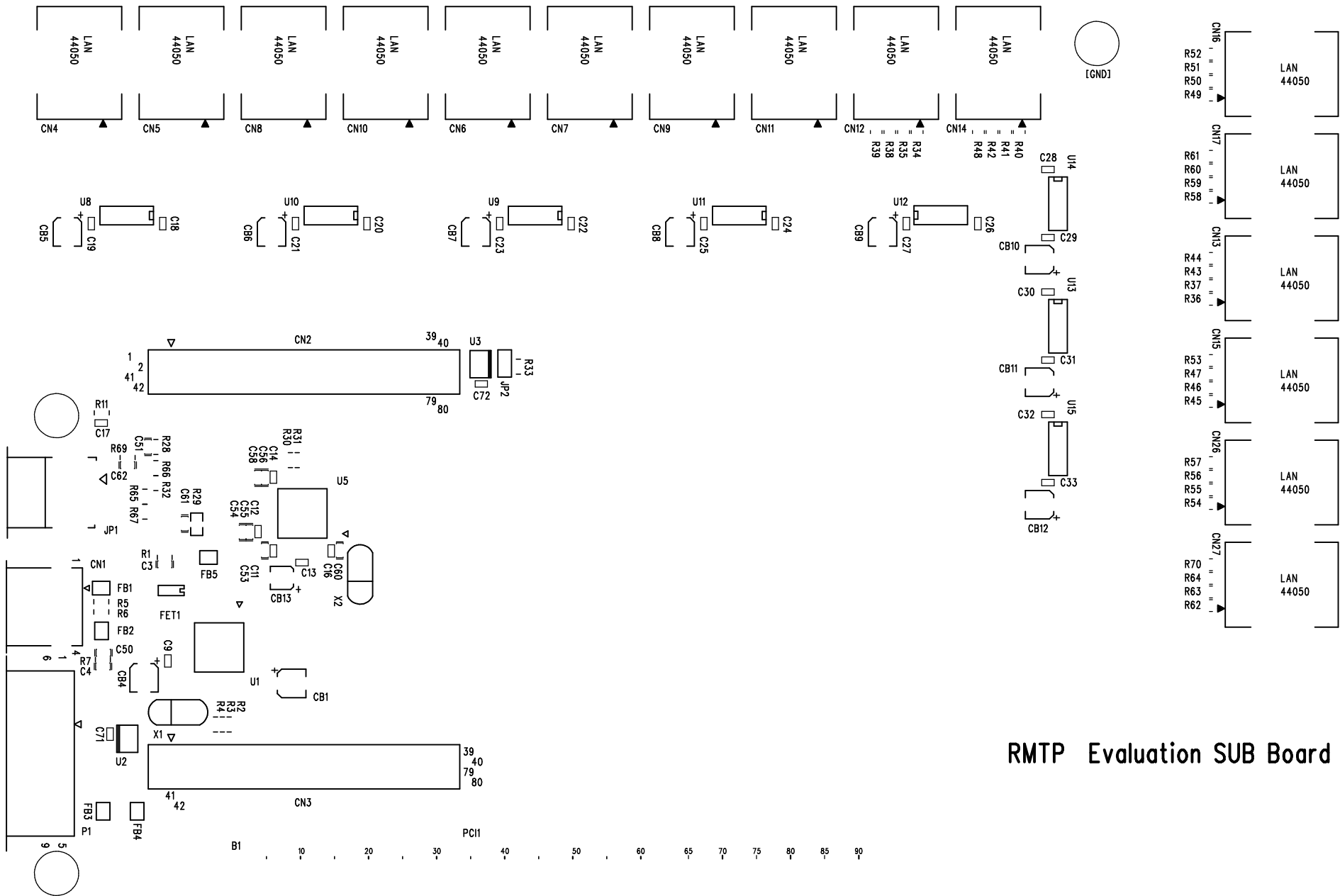
KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09
BOARDOUTLINE GEN030.PHO



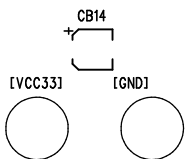
KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

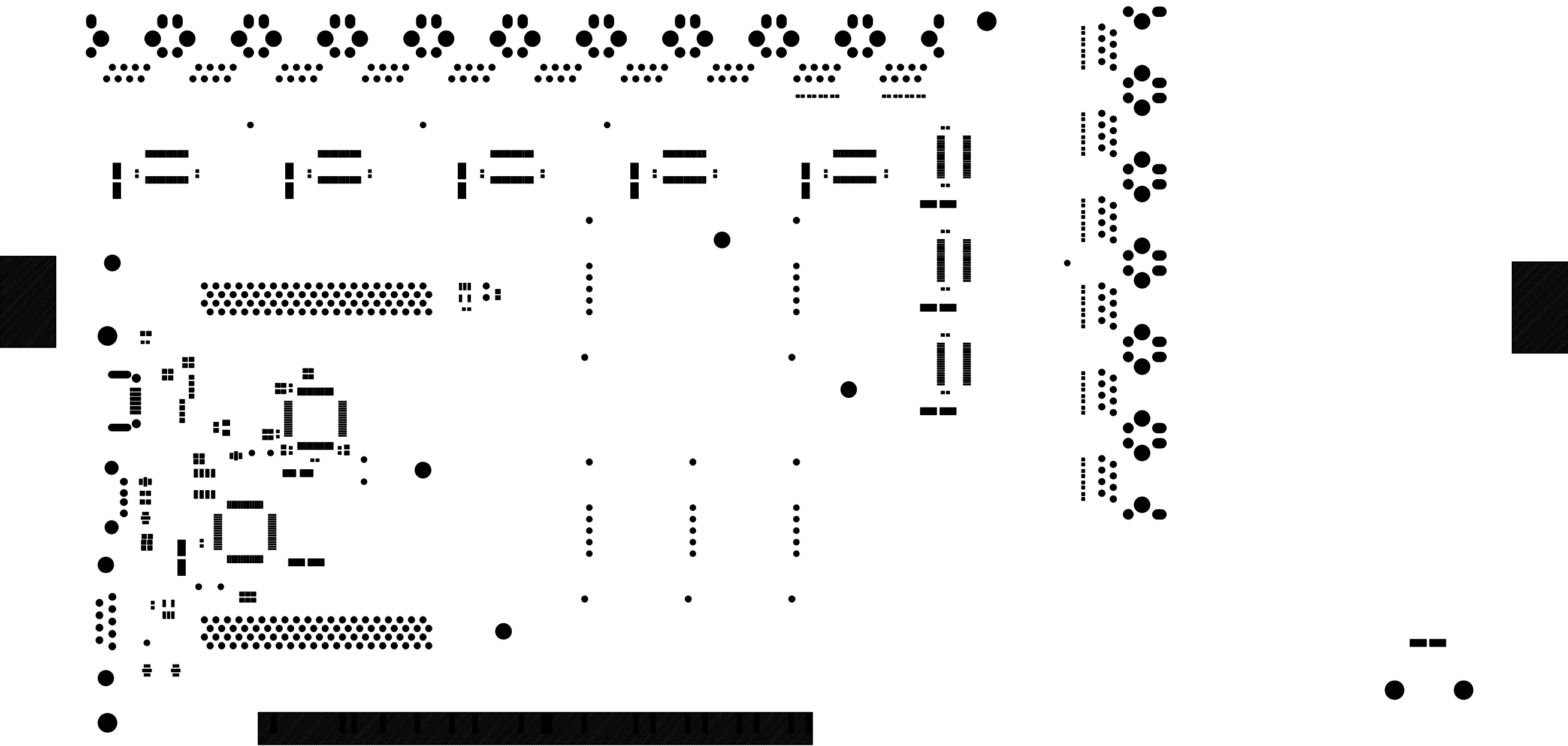
L1 TOP SILK
L1 ARTWORK

SST001020.PHO
ART001.PHO



RMT PCI SUB BOARD ver1.0

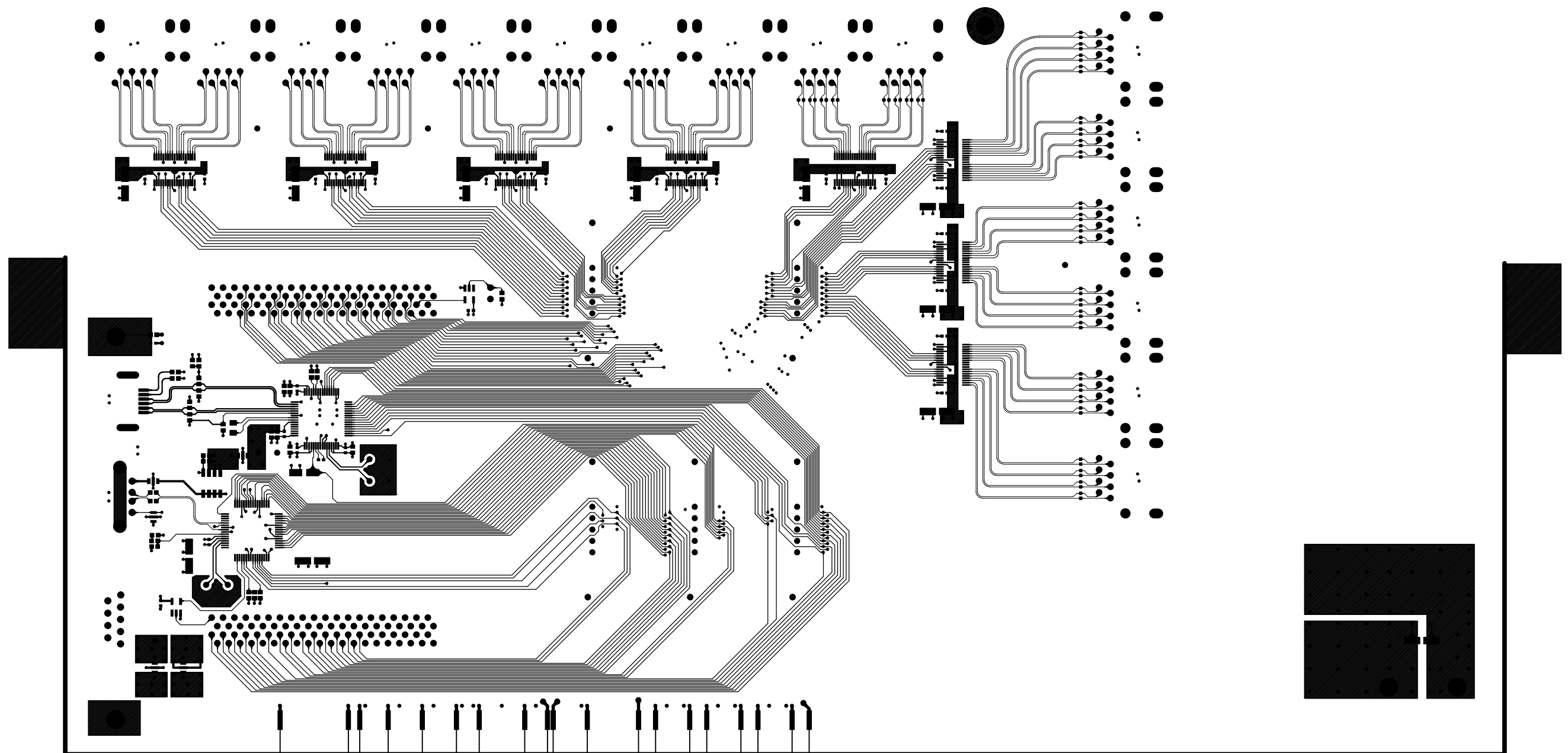




KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

L1 ARTWORK

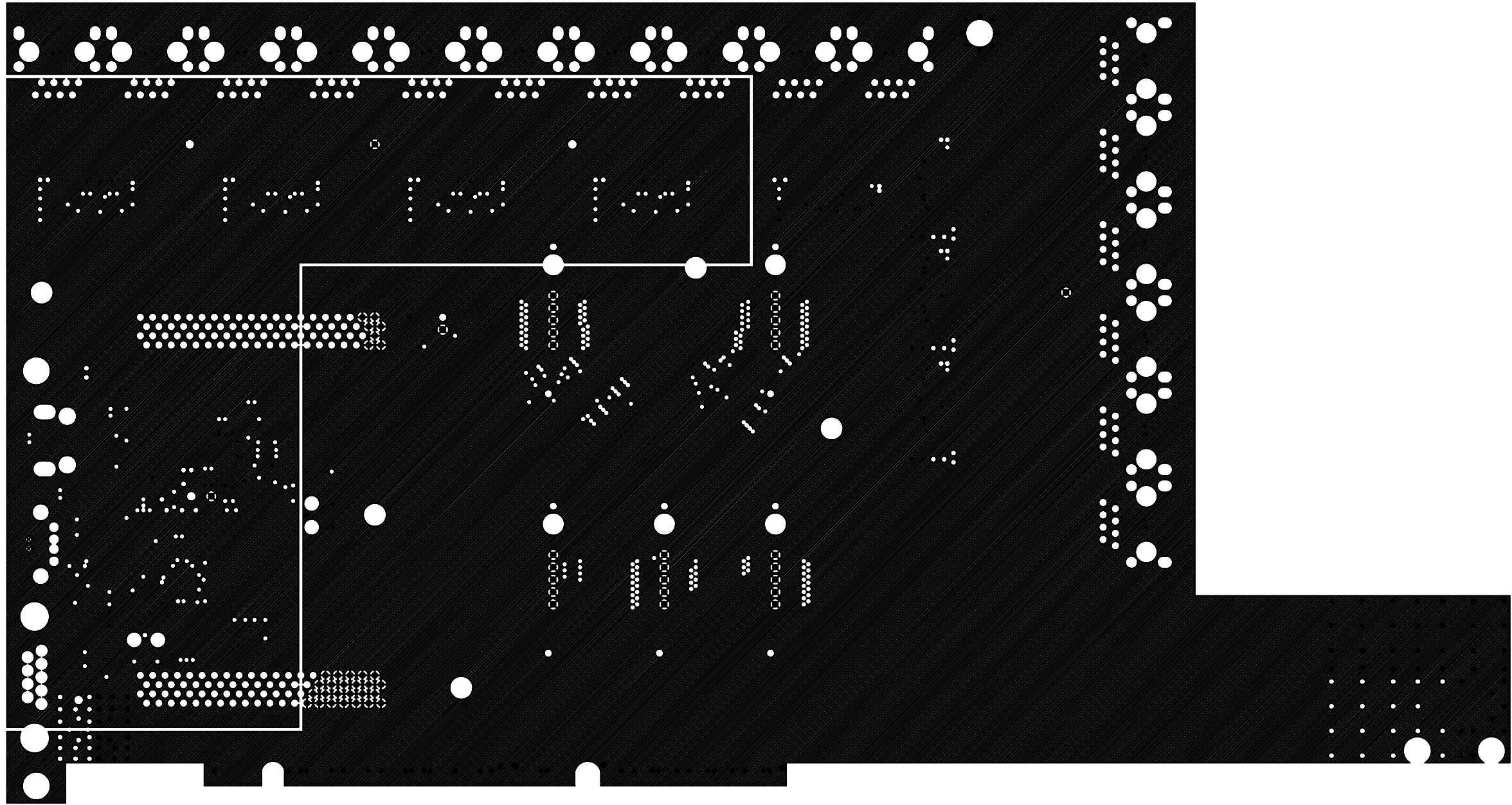
ART001.PHO



KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

L2 ARTWORK

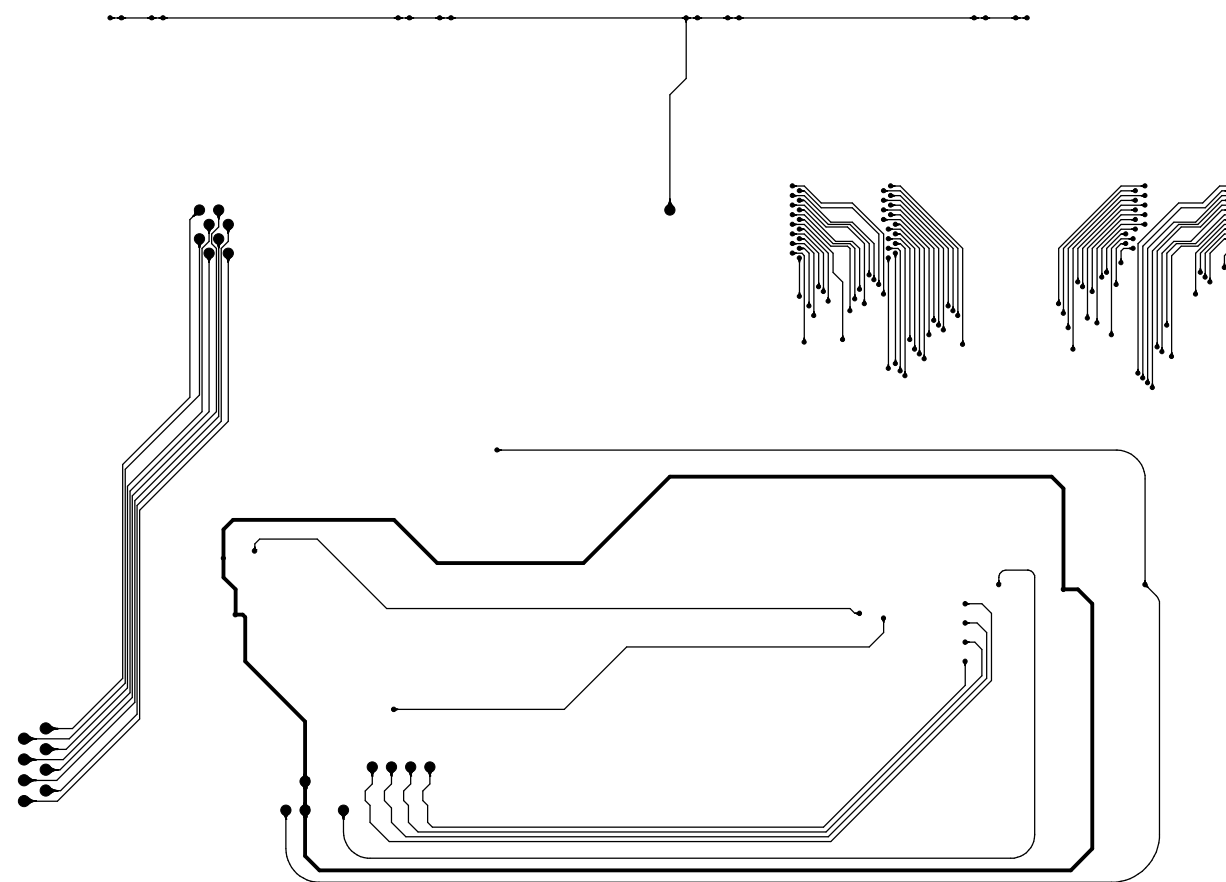
ART02.PHO



KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

L3 ARTWORK

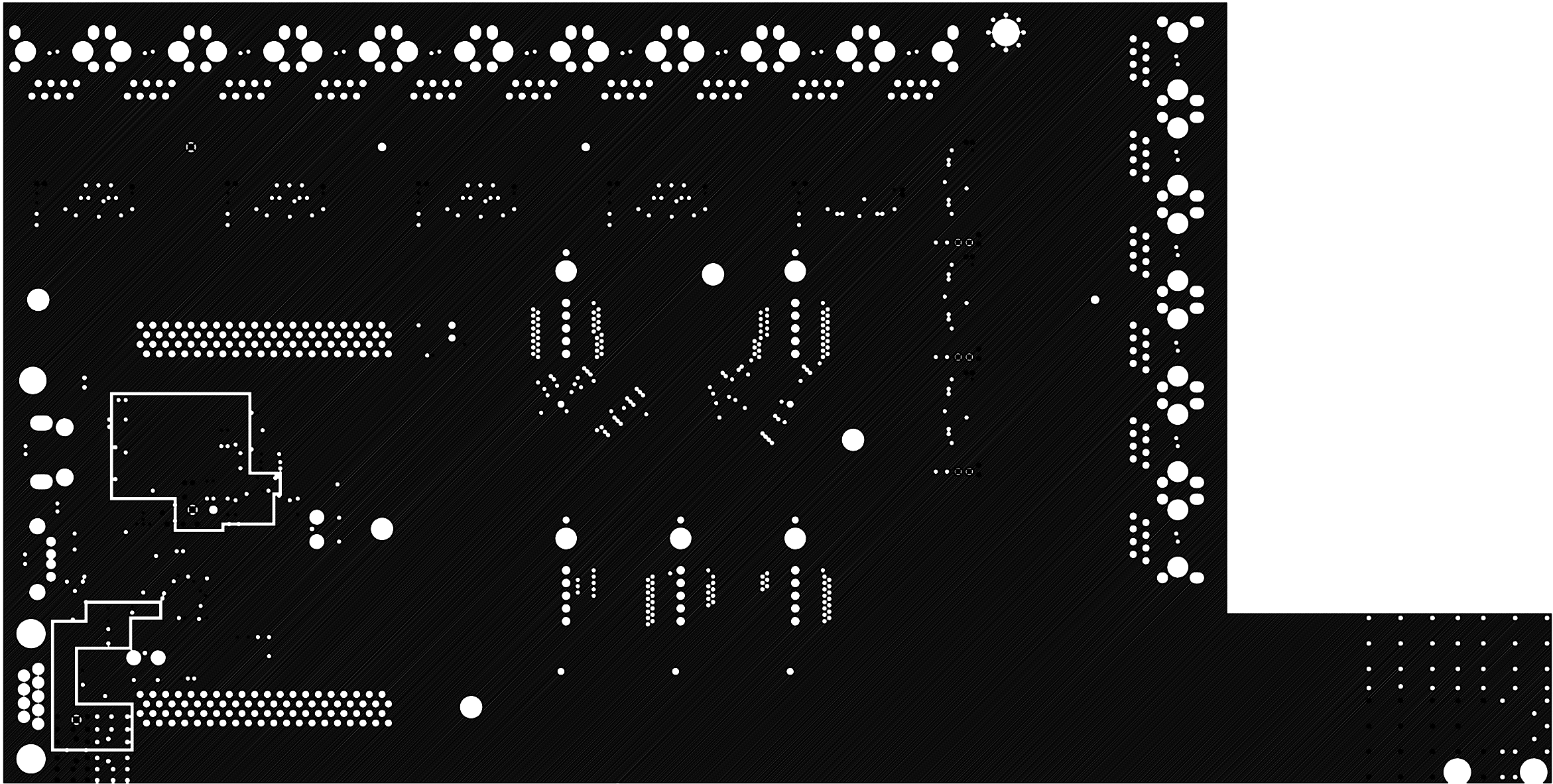
ART003.PHO



KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

L4 ARTWORK

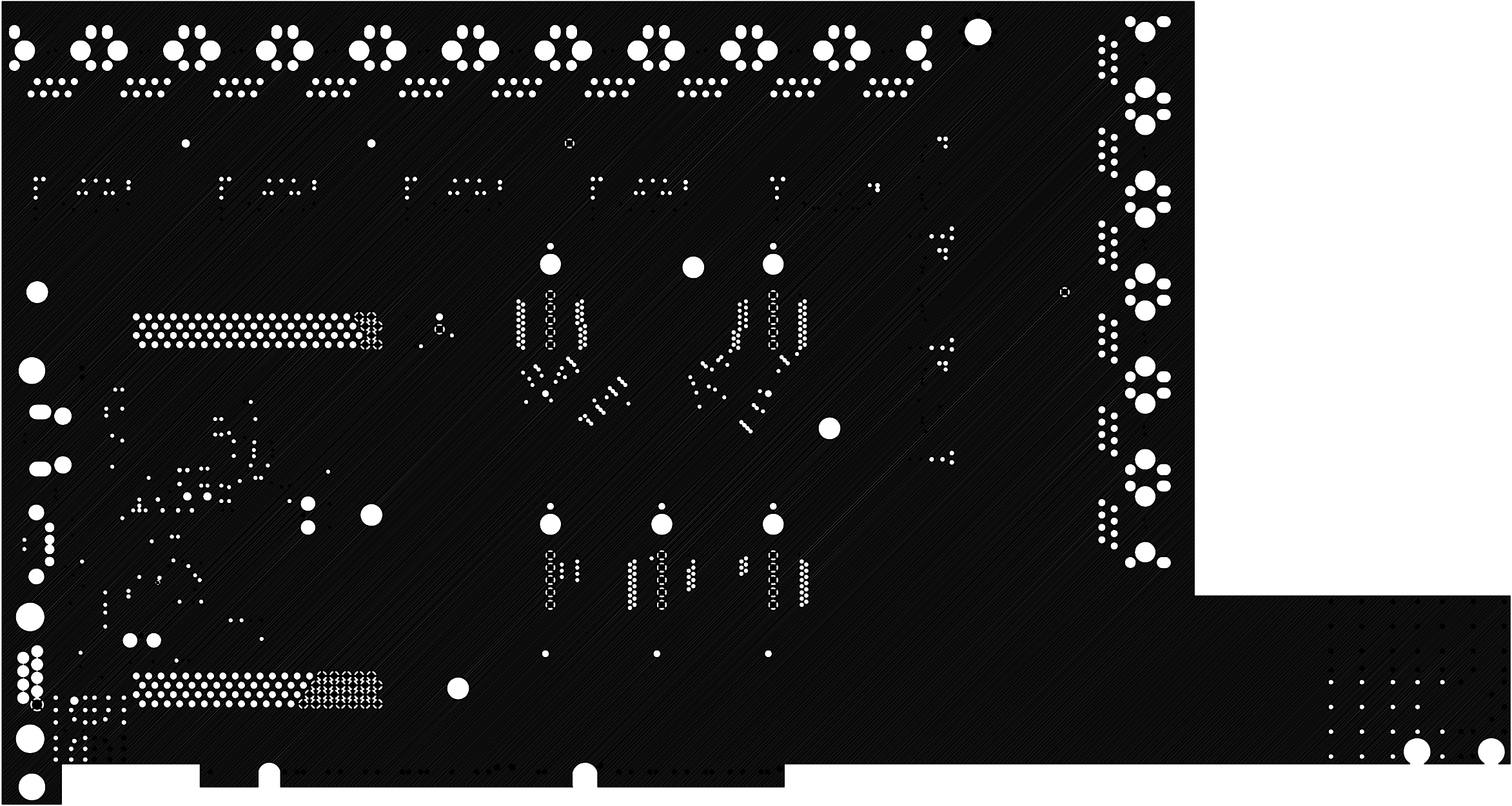
ART004.PHO

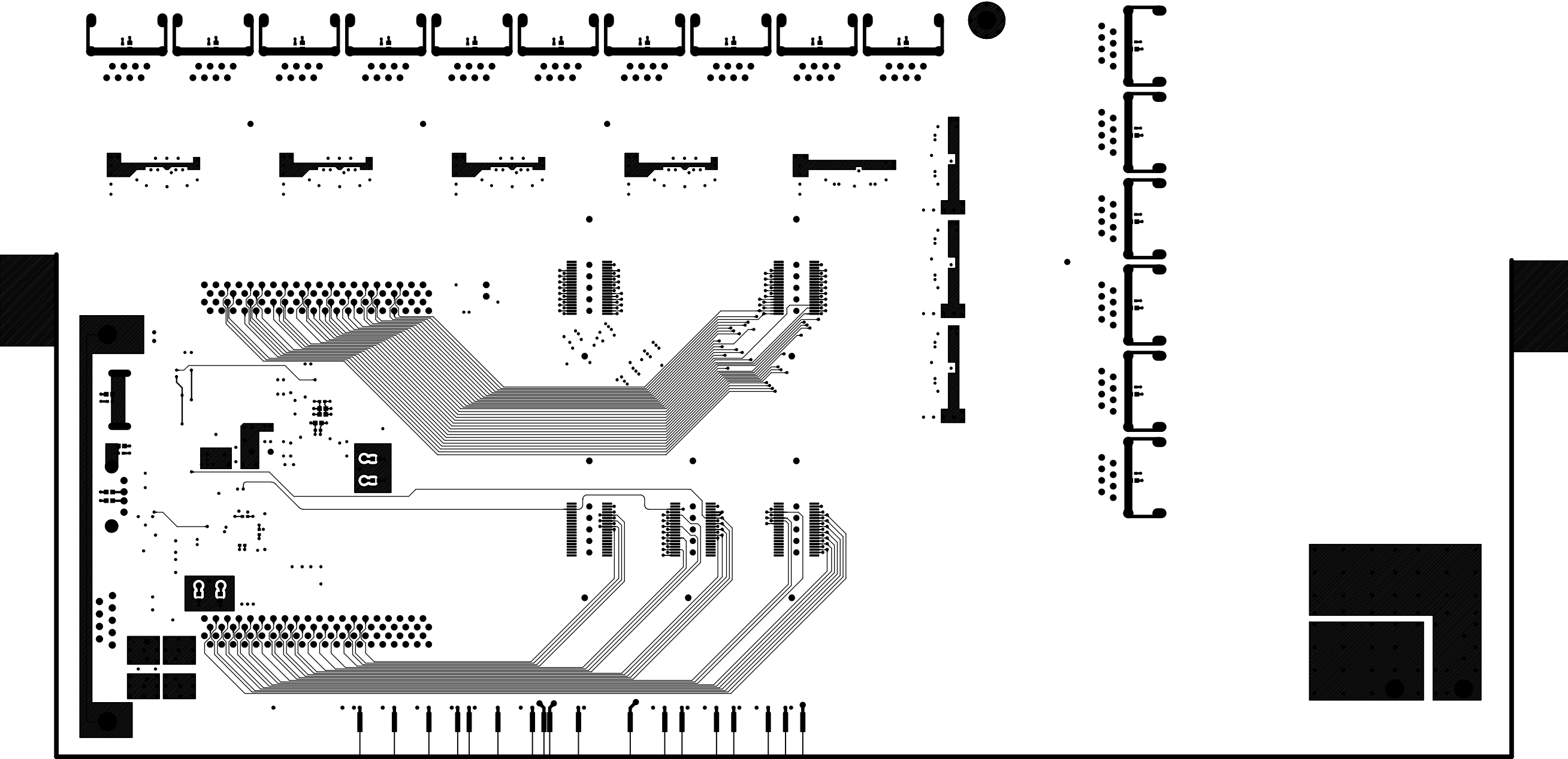


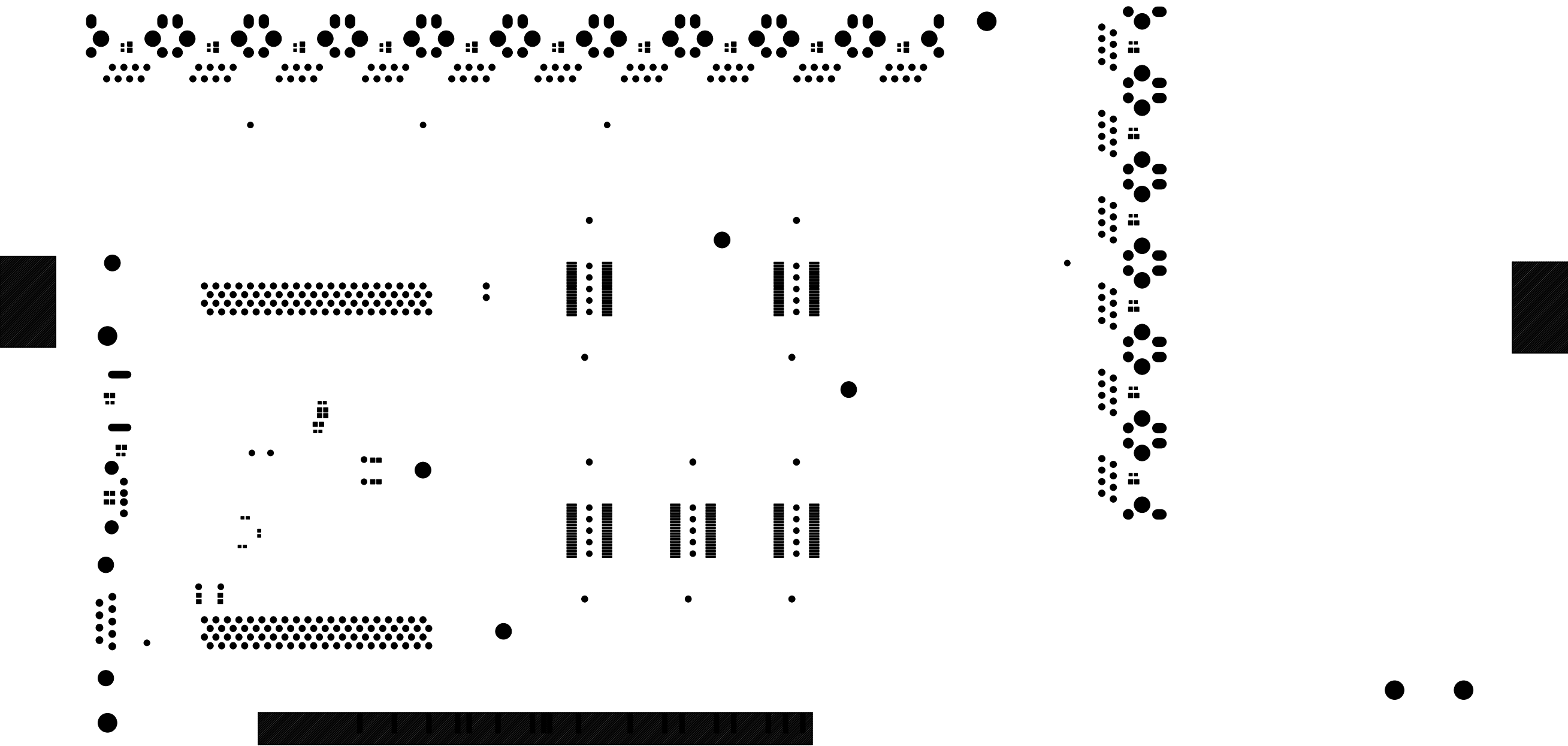
KB04012(RMT PCI SUB BOARD)SANSEI SYSTEM 2004.02.09

L5 ARTWORK

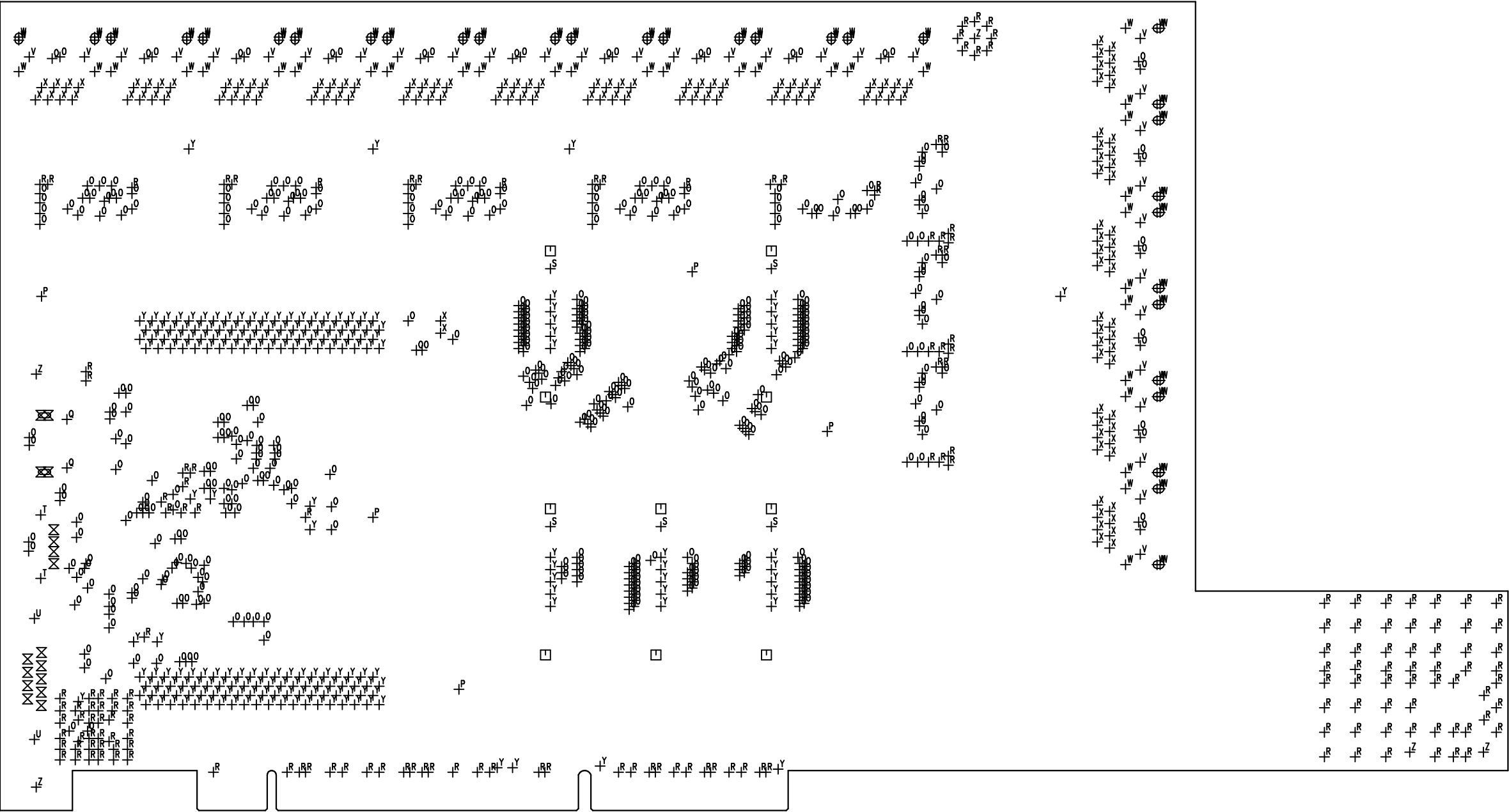
ART005.PHO







L12 BOTTOM SILK
L12 ARTWORK



寸法	個数	記号	PLTD
30	10	□	PLTD
40	17	⊗	PLTD
12	445	0	PLTD
98	5	P	NPLTD
63	2	Q	NPLTD
16	173	R	PLTD
93	5	S	NPLTD
91	2	T	PLTD
126	2	U	NPLTD
128	32	V	NPLTD
62	96	W	PLTD
36	130	X	PLTD
32	200	Y	PLTD
138	5	Z	PLTD
40 x 100	2	⊗	PLTD
62 x 87	32	W	PLTD